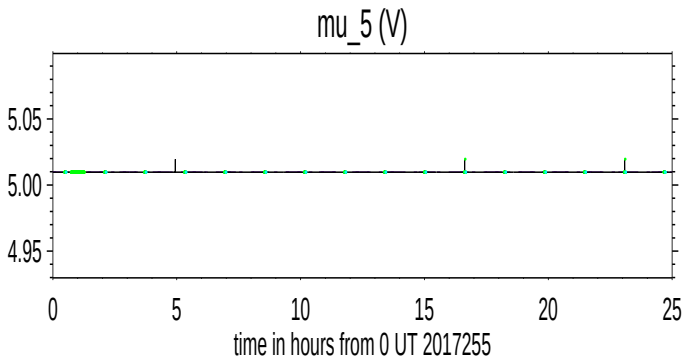
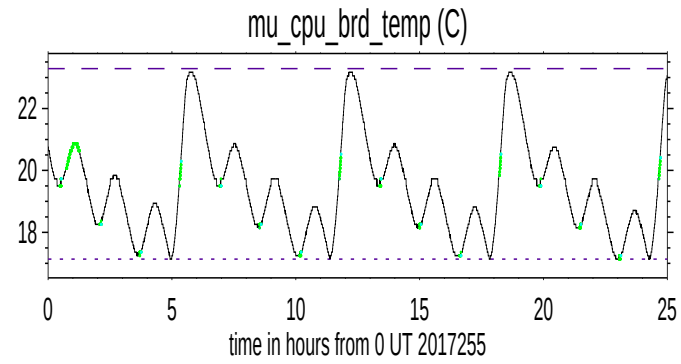
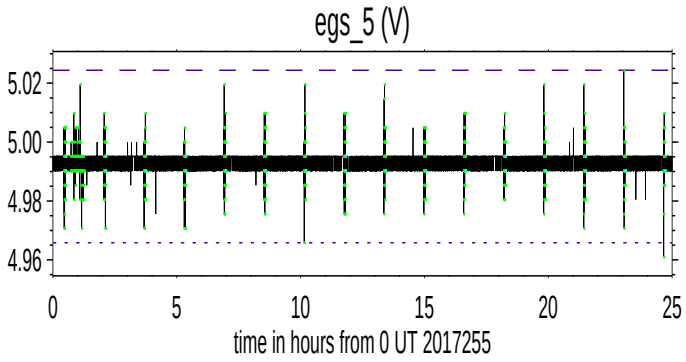
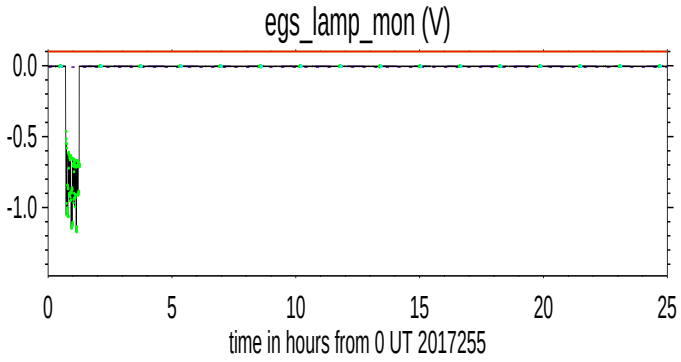
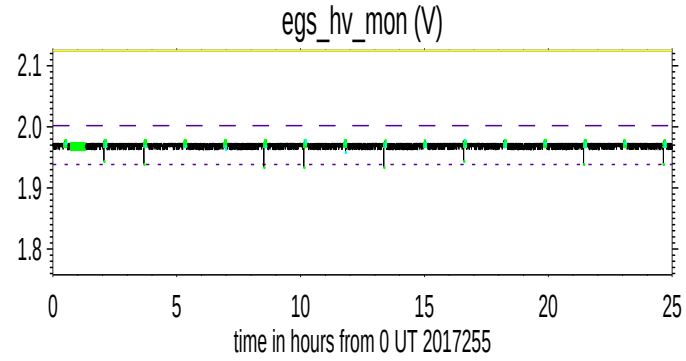
```
Information for      2017255
10:34:50.001: see_stat[1]: 5 internal error
11:34:55.001: see_stat[1]: 6 internal error
```

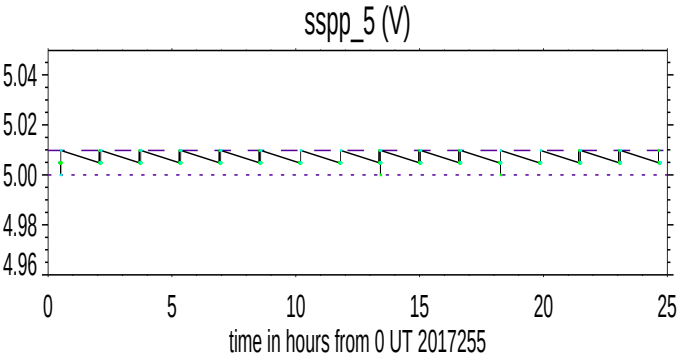

mu_5 (V)
5.05
5.00
4.95
0
5
10
15
20
25
time in hours from 0 UT 2017255

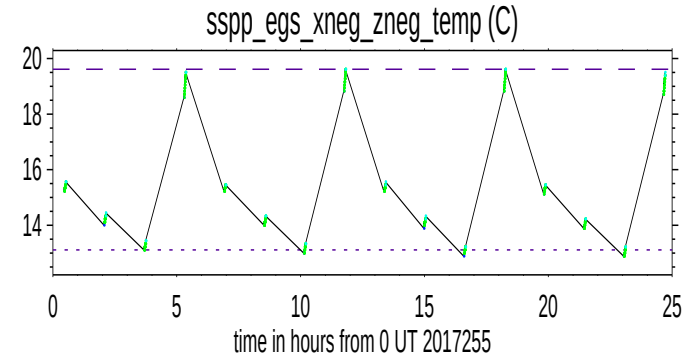

mu_cpu_brd_temp (C)
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

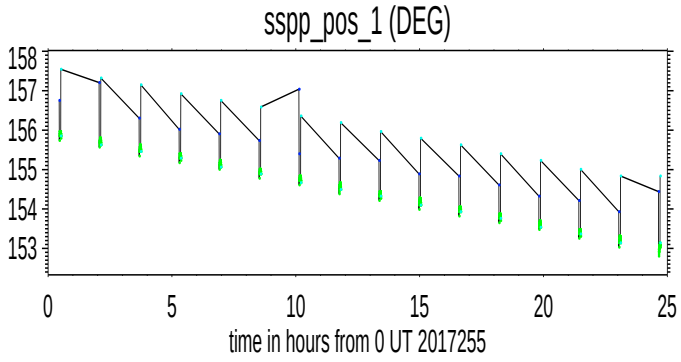

egs_5 (V)
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017255

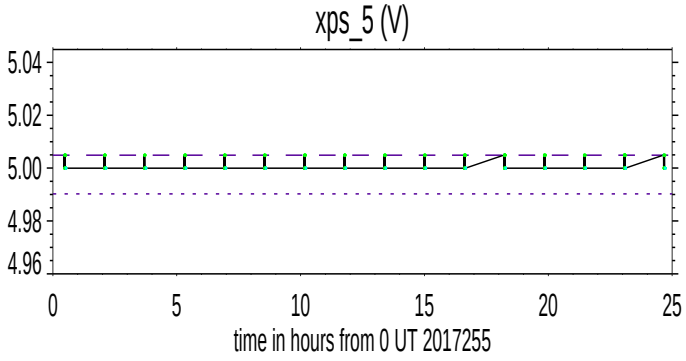

egs_lamp_mon (V)
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours from 0 UT 2017255

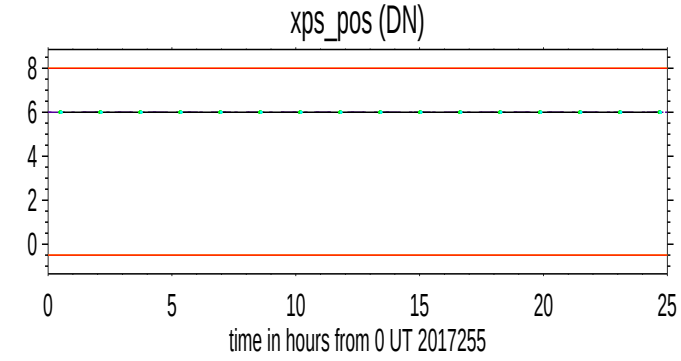

egs_hv_mon (V)
2.1
2.0
1.9
1.8
0
5
10
15
20
25
time in hours from 0 UT 2017255

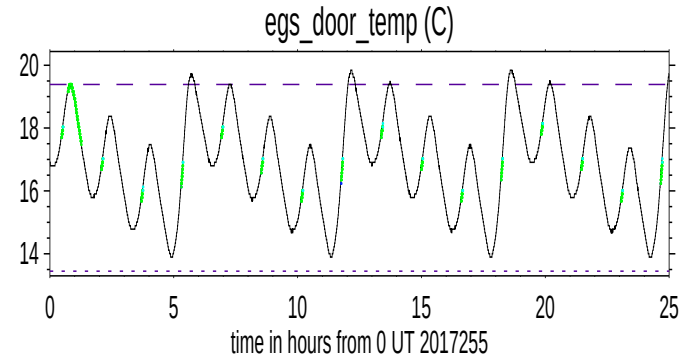

sspp_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017255

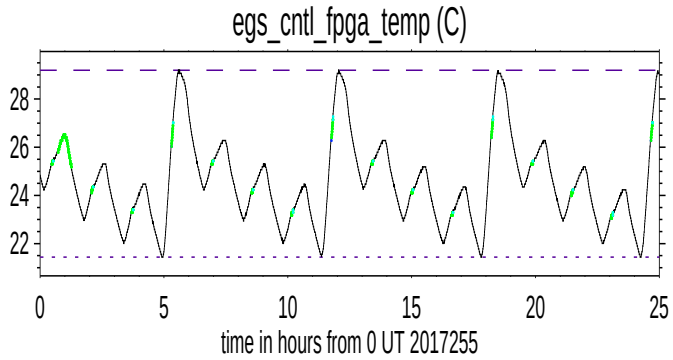

sspp_egs_xneg_zneg_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

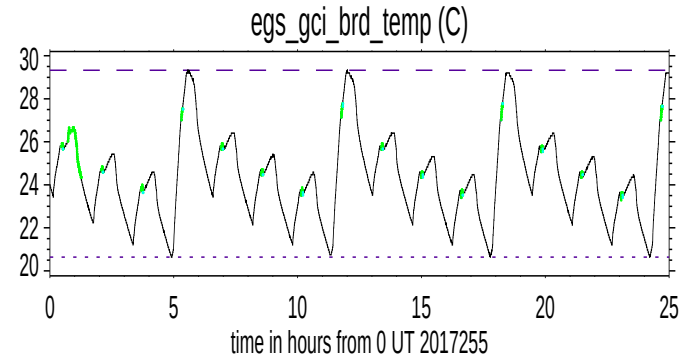

sspp_pos_1 (DEG)
158
157
156
155
154
153
0
5
10
15
20
25
time in hours from 0 UT 2017255

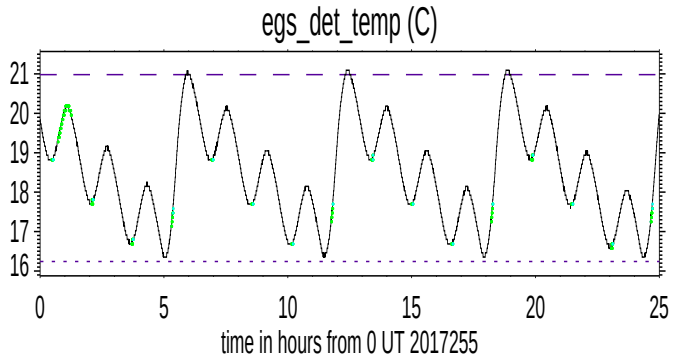

xps_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017255

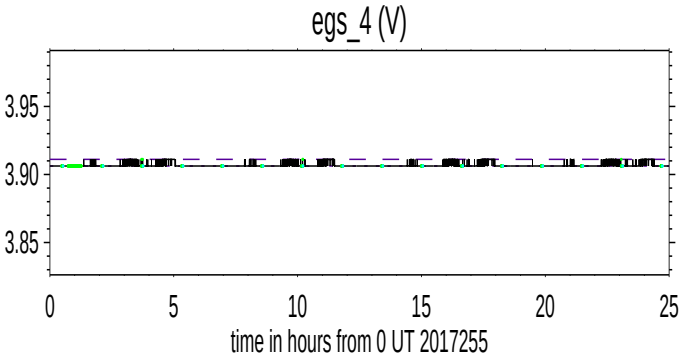

xps_pos (DN)
8
6
4
2
0
0
5
10
15
20
25
time in hours from 0 UT 2017255

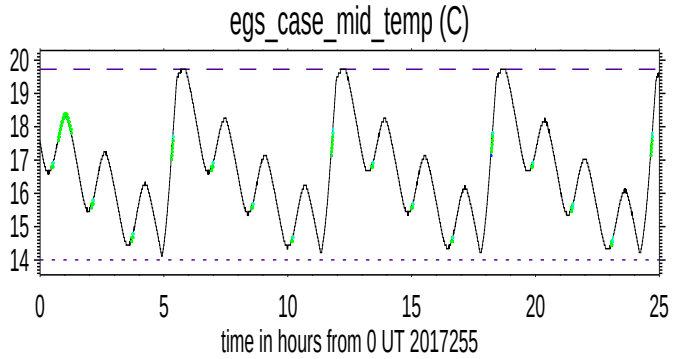

egs_door_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

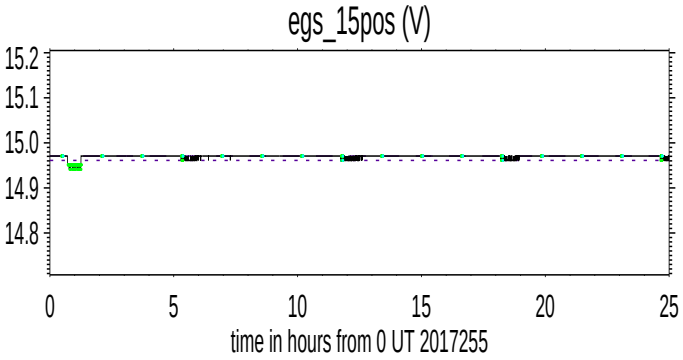

egs_cntl_fpga_temp (C)
28
26
24
22
0
5
10
15
20
25
time in hours from 0 UT 2017255

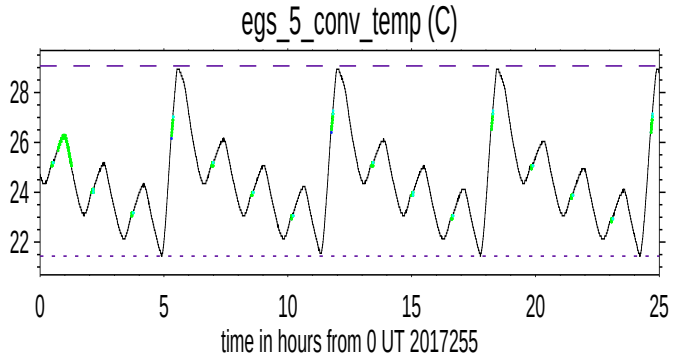

egs_gci_brd_temp (C)
30
28
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017255

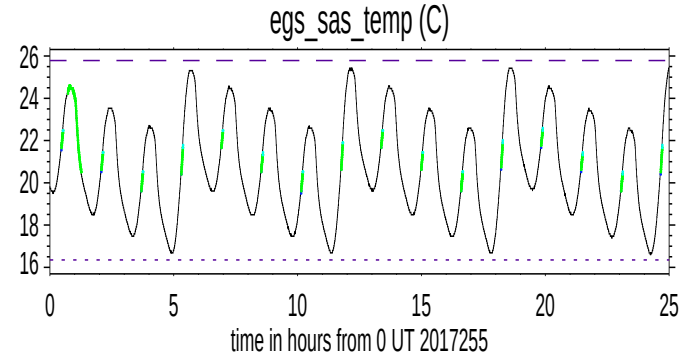

egs_det_temp (C)
21
20
19
18
17
16
0
5
10
15
20
25
time in hours from 0 UT 2017255

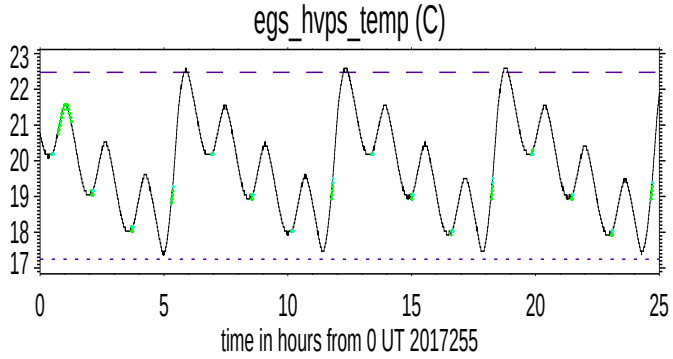

egs_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017255

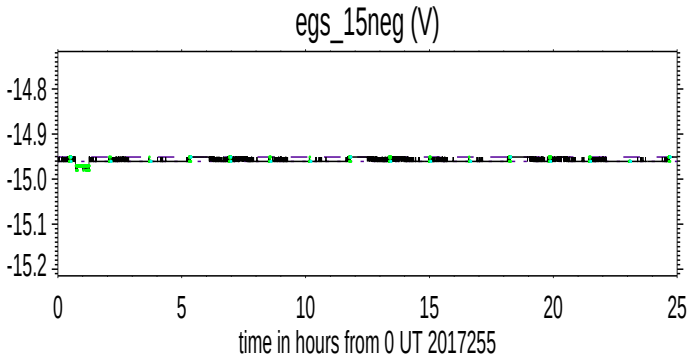

egs_case_mid_temp (C)
20
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

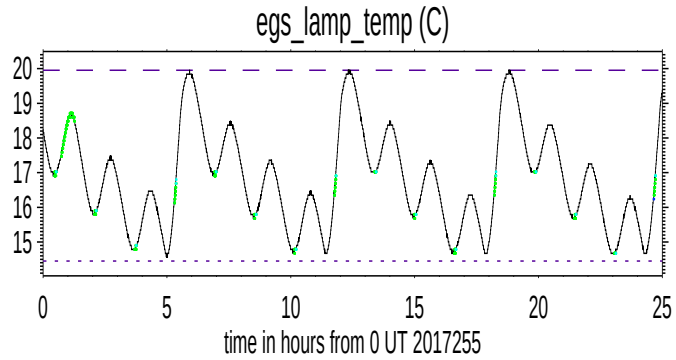

egs_15pos (V)
15.2
15.1
15.0
14.9
14.8
0
5
10
15
20
25
time in hours from 0 UT 2017255

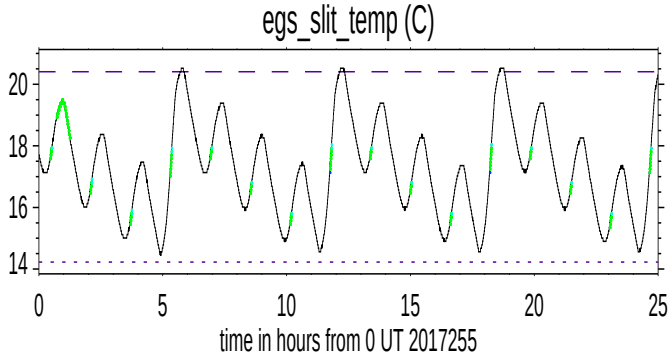

egs_5_conv_temp (C)
28
26
24
22
0
5
10
15
20
25
time in hours from 0 UT 2017255

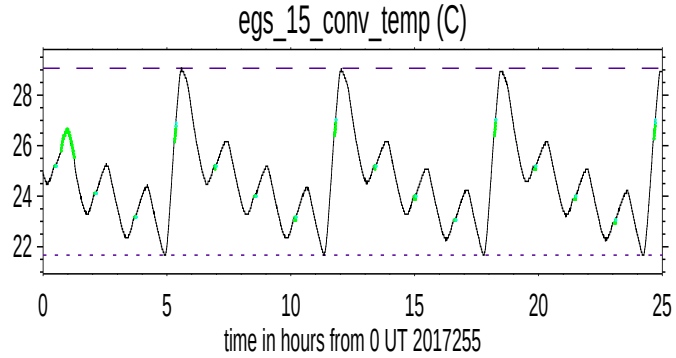

egs_sas_temp (C)
26
24
22
20
18
16
0
5
10
15
20
25
time in hours from 0 UT 2017255

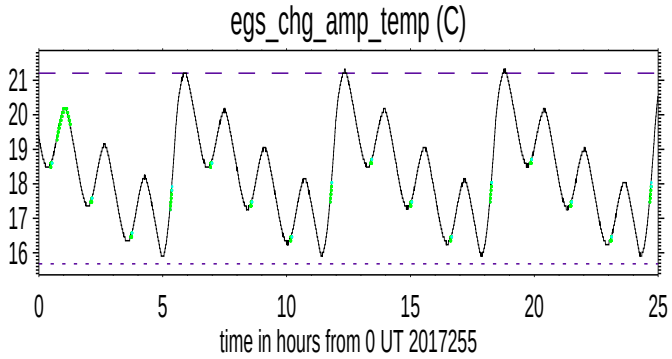

egs_hvps_temp (C)
23
22
21
20
19
18
17
0
5
10
15
20
25
time in hours from 0 UT 2017255

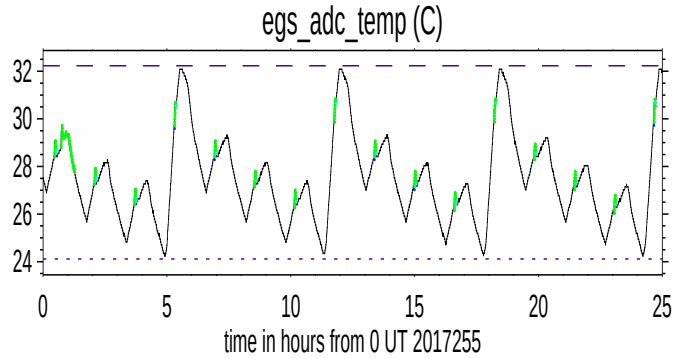

egs_15neg (V)
-14.8
-14.9
-15.0
-15.1
-15.2
0 5 10 15 20 25
time in hours from 0 UT 2017255

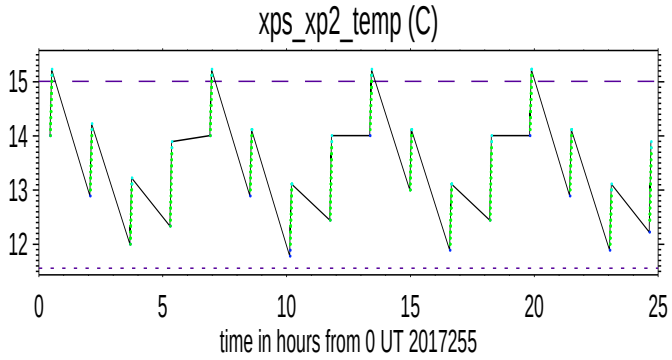

egs_lamp_temp (C)
20 19 18 17 16 15
0 5 10 15 20 25
time in hours from 0 UT 2017255

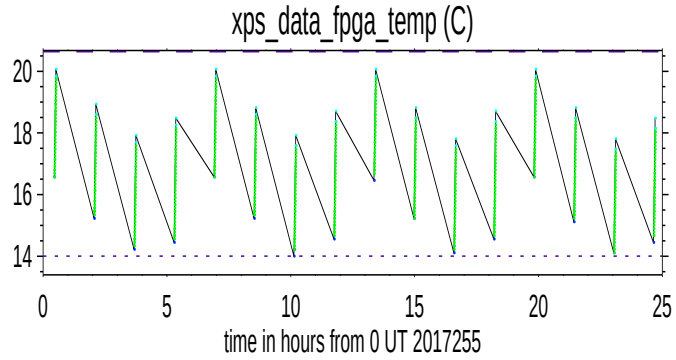

egs_slit_temp (C)
20 18 16 14
0 5 10 15 20 25
time in hours from 0 UT 2017255

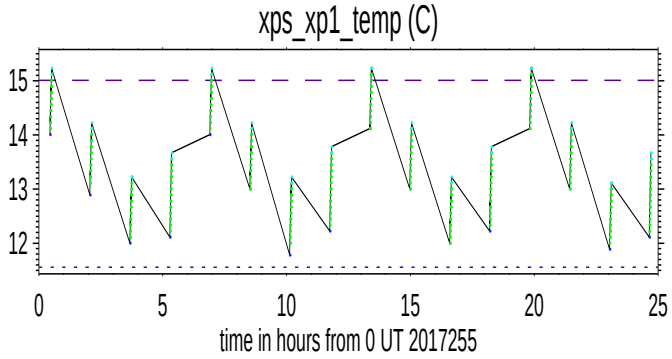

egs_15_conv_temp (C)
28 26 24 22
0 5 10 15 20 25
time in hours from 0 UT 2017255

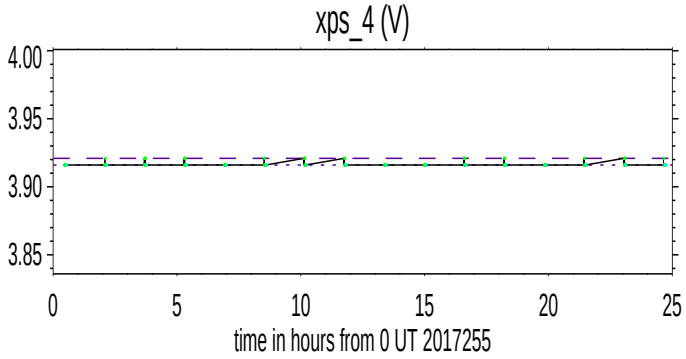

egs_chg_amp_temp (C)
21 20 19 18 17 16
0 5 10 15 20 25
time in hours from 0 UT 2017255

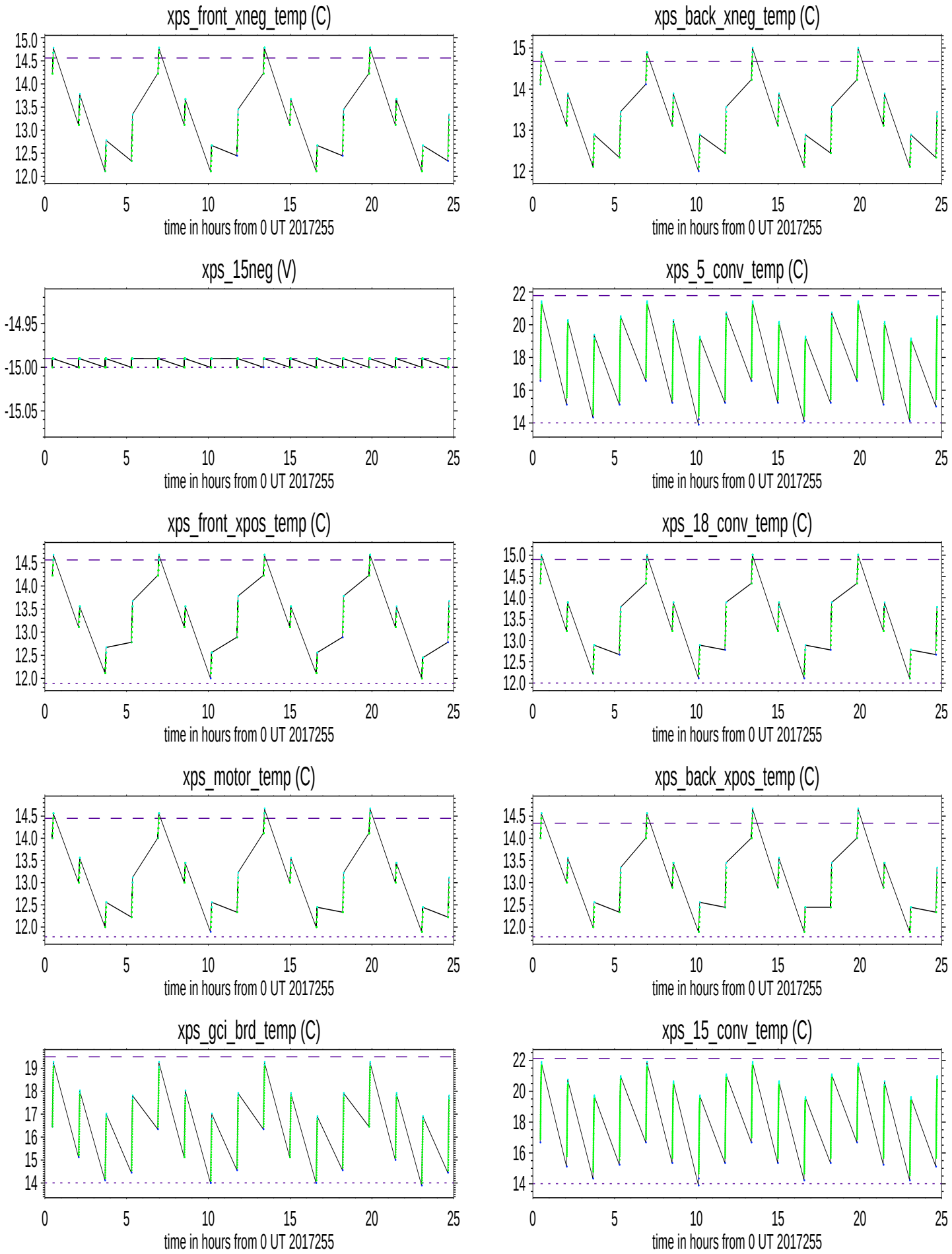

egs_adc_temp (C)
32 30 28 26 24
0 5 10 15 20 25
time in hours from 0 UT 2017255

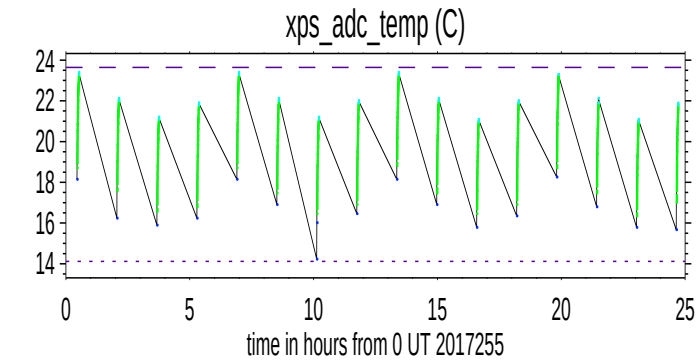

xps_xp2_temp (C)
15 14 13 12
0 5 10 15 20 25
time in hours from 0 UT 2017255

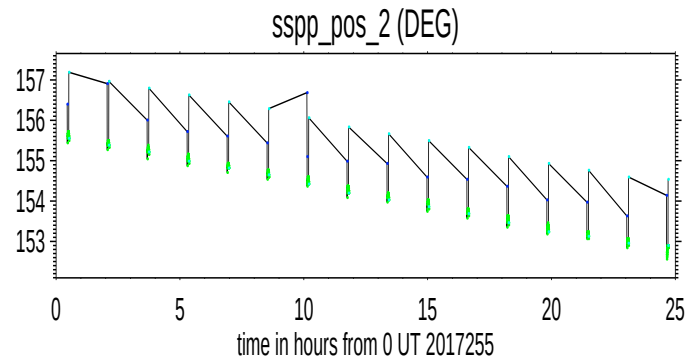

xps_data_fpga_temp (C)
20 18 16 14
0 5 10 15 20 25
time in hours from 0 UT 2017255

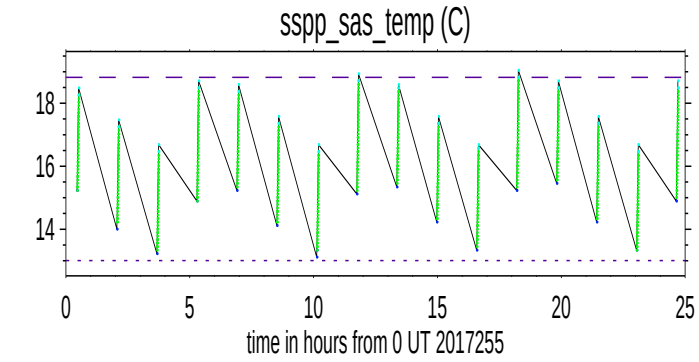

xps_xp1_temp (C)
15 14 13 12
0 5 10 15 20 25
time in hours from 0 UT 2017255

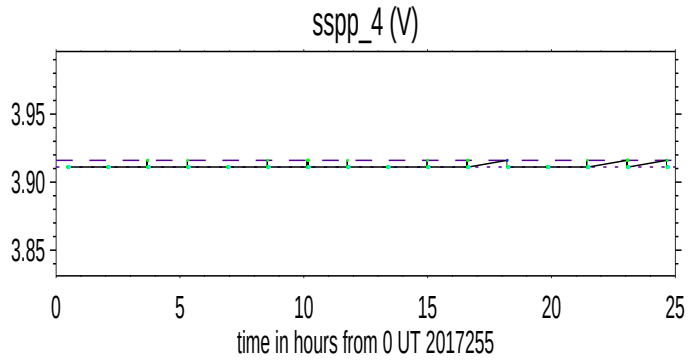

xps_4 (V)
4.00 3.95 3.90 3.85
0 5 10 15 20 25
time in hours from 0 UT 2017255

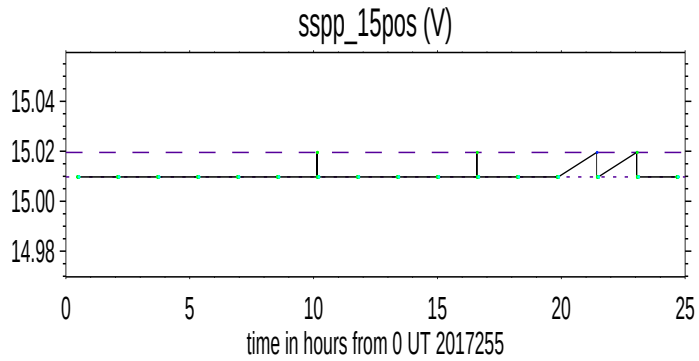

xps_front_xneg_temp (C)
xps_back_xneg_temp (C)
xps_15neg (V)
xps_5_conv_temp (C)
xps_front_xpos_temp (C)
xps_18_conv_temp (C)
xps_motor_temp (C)
xps_back_xpos_temp (C)
xps_gci_brd_temp (C)
xps_15_conv_temp (C)
time in hours from 0 UT 2017255

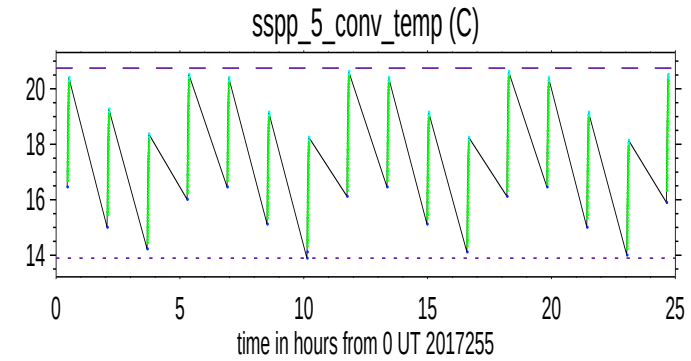

xps_adc_temp (C)
24
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

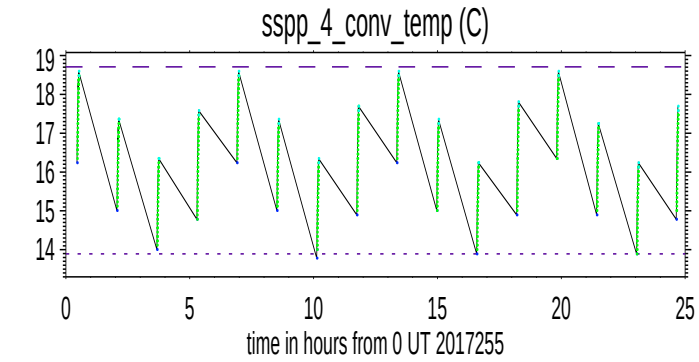

sspp_pos_2 (DEG)
157
156
155
154
153
0
5
10
15
20
25
time in hours from 0 UT 2017255

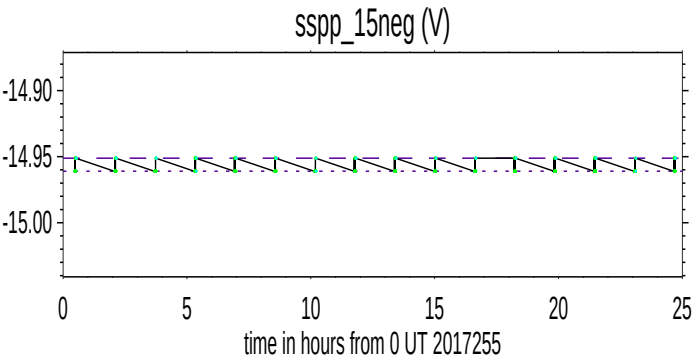

sspp_sas_temp (C)
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

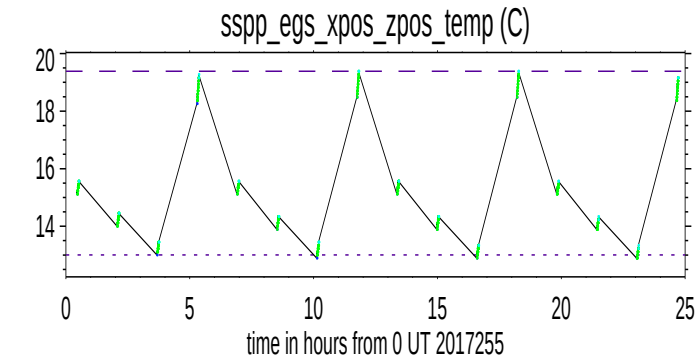

sspp_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017255

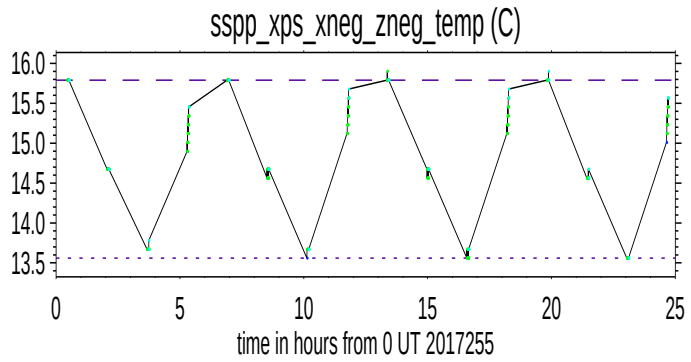

sspp_15pos (V)
15.04
15.02
15.00
14.98
0
5
10
15
20
25
time in hours from 0 UT 2017255

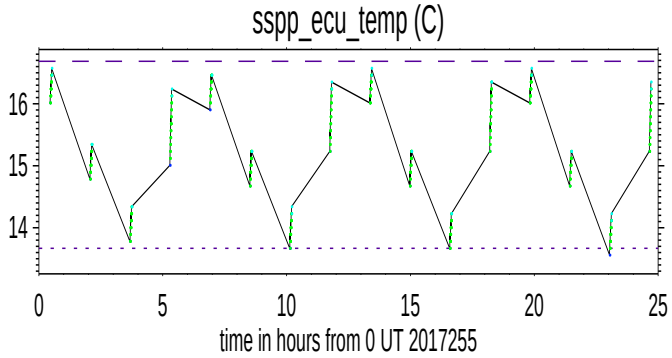

sspp_5_conv_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

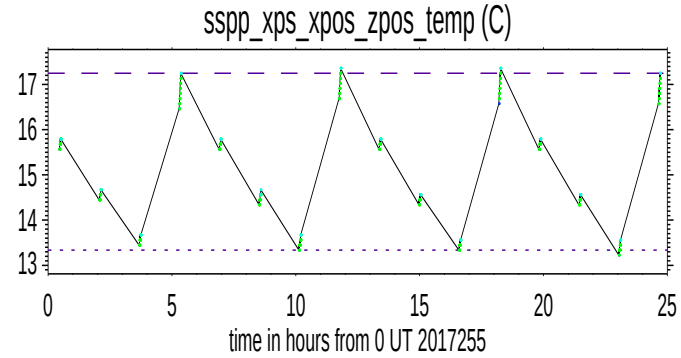

sspp_4_conv_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

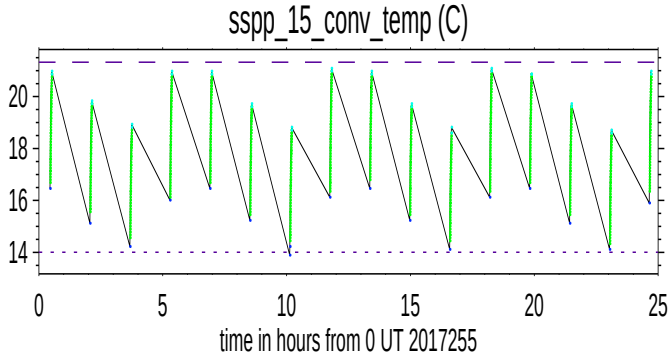

sspp_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2017255

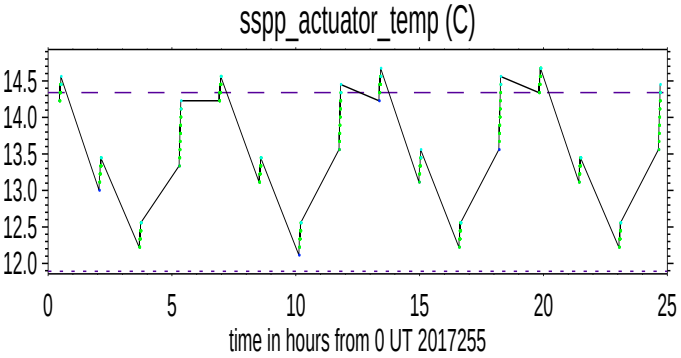

sspp_egs_xpos_zpos_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

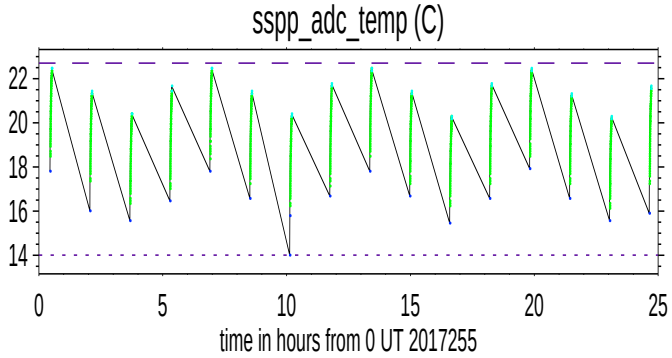

sspp_xps_xneg_zneg_temp (C)
16.0
15.5
15.0
14.5
14.0
13.5
0
5
10
15
20
25
time in hours from 0 UT 2017255

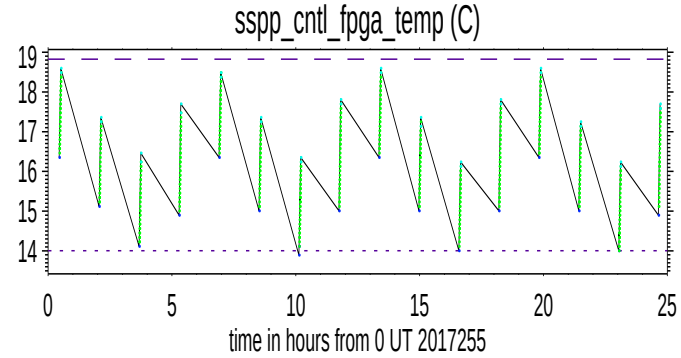

sspp_ecu_temp (C)
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

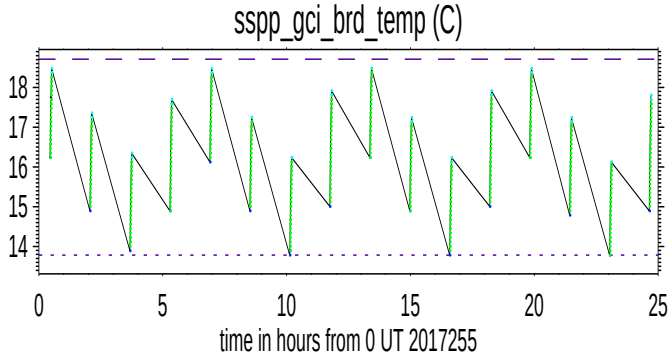

sspp_xps_xpos_zpos_temp (C)
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017255

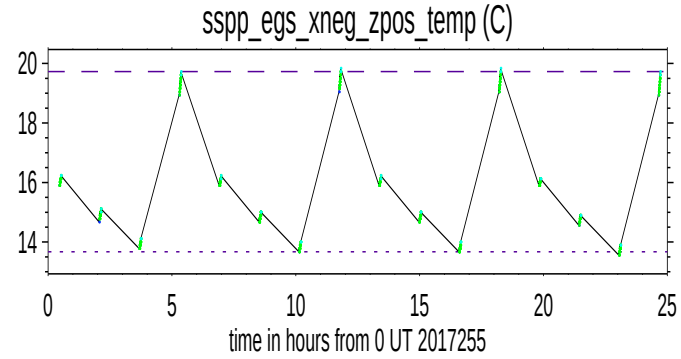

sspp_15_conv_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

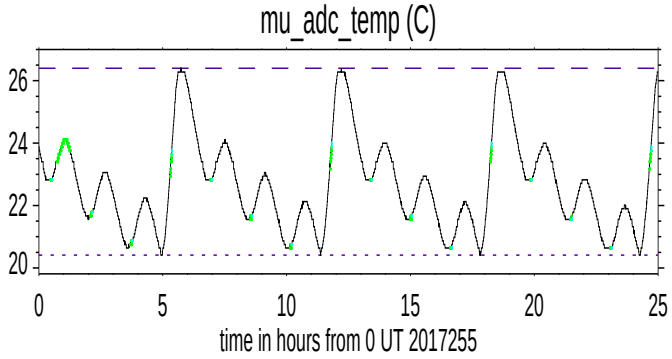

sspp_actuator_temp (C)
14.5
14.0
13.5
13.0
12.5
12.0
0
5
10
15
20
25
time in hours from 0 UT 2017255

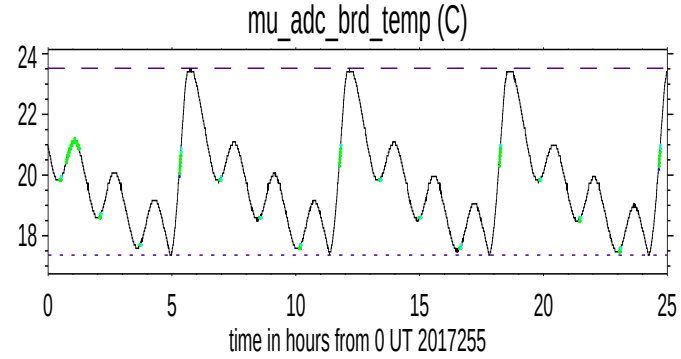

sspp_adc_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

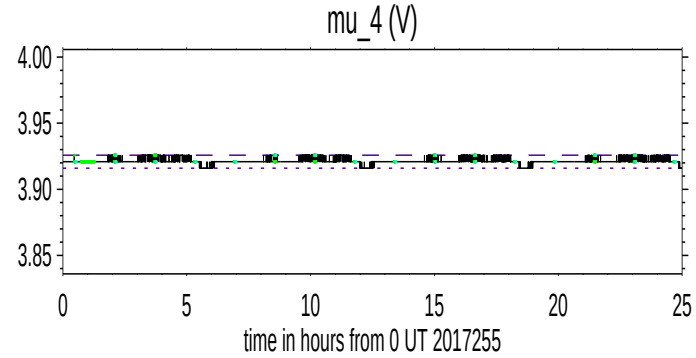

sspp_cntl_fpga_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

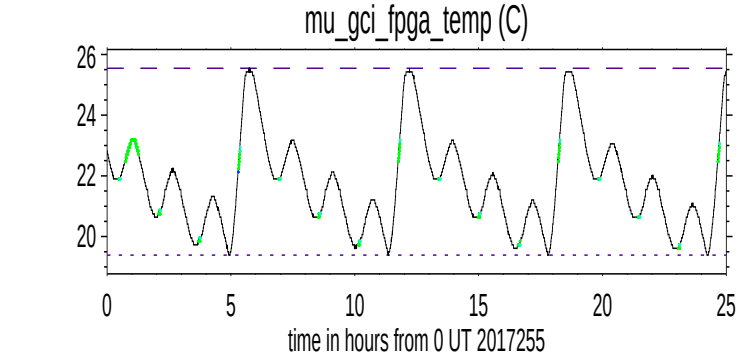

sspp_gci_brd_temp (C)
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

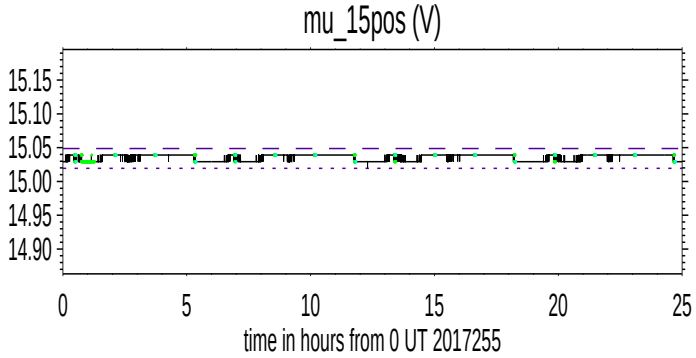

sspp_egs_xneg_zpos_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017255

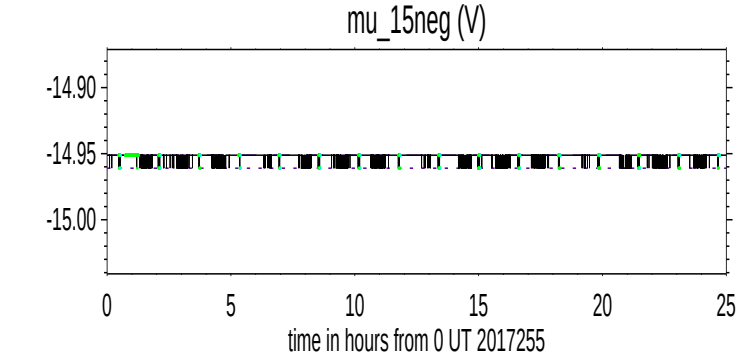

mu_adc_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017255

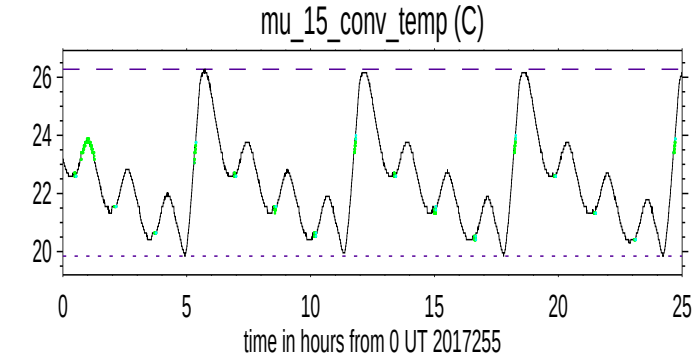

mu_adc_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

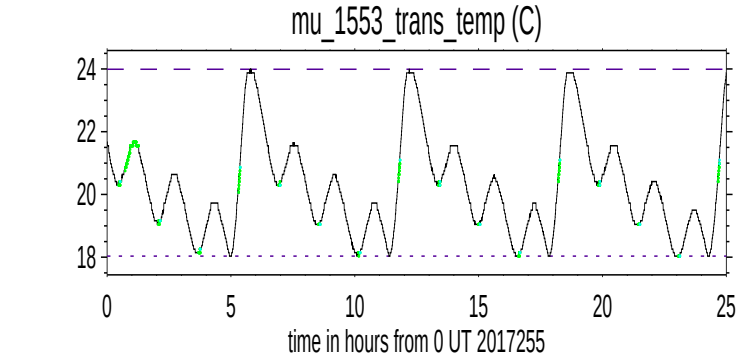

mu_4 (V)
4.00
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017255

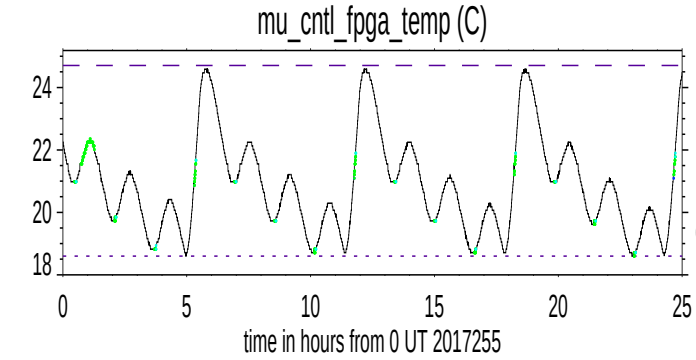

mu_gci_fpga_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017255

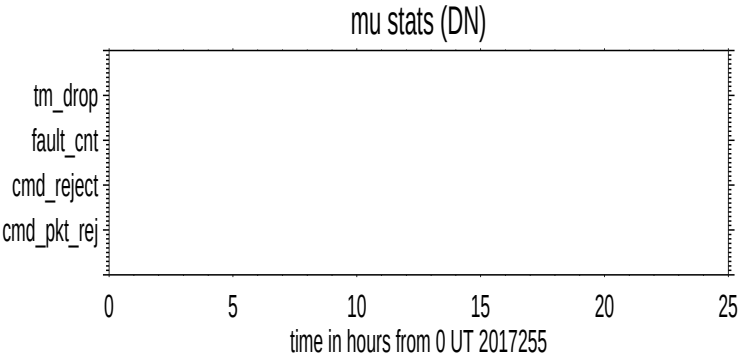

mu_15pos (V)
15.15
15.10
15.05
15.00
14.95
14.90
0
5
10
15
20
25
time in hours from 0 UT 2017255

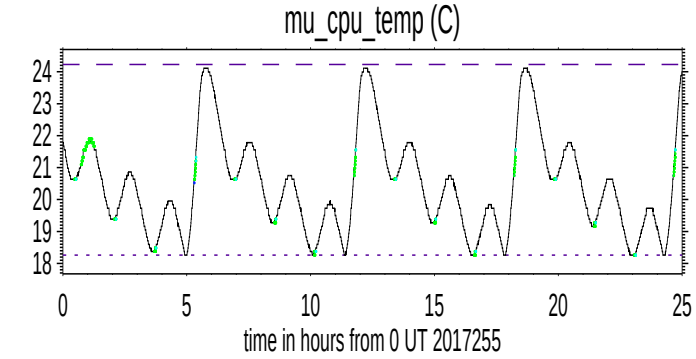

mu_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2017255

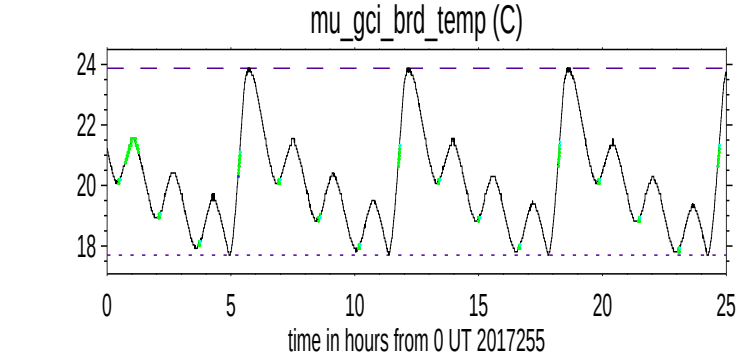

mu_15_conv_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017255

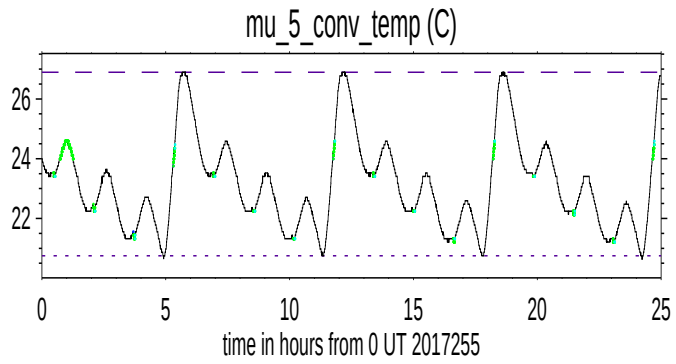

mu_1553_trans_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

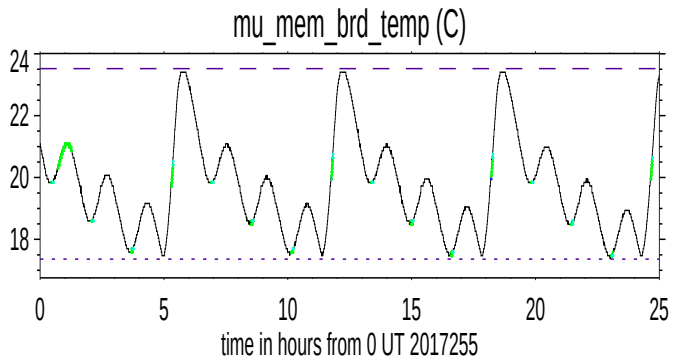

mu_cntl_fpga_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

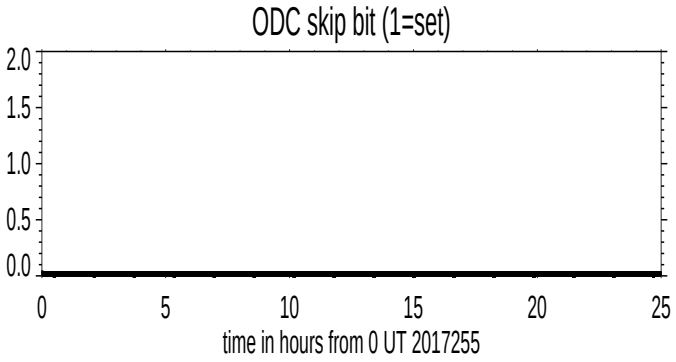

mu stats (DN)
tm_drop
fault_cnt
cmd_reject
cmd_pkt_rej
0
5
10
15
20
25
time in hours from 0 UT 2017255

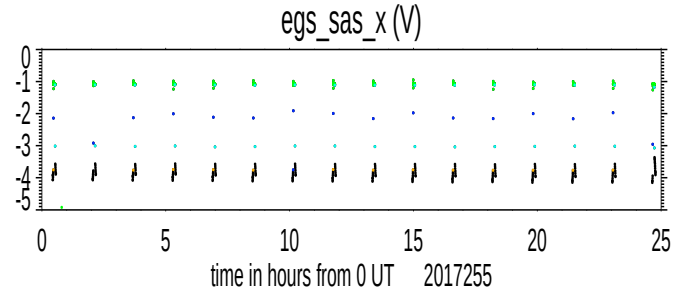

mu_cpu_temp (C)
24
23
22
21
20
19
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

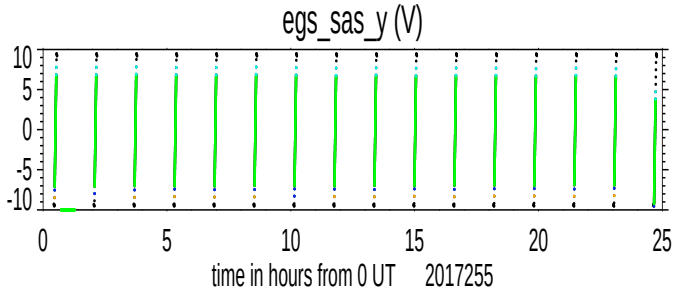

mu_gci_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

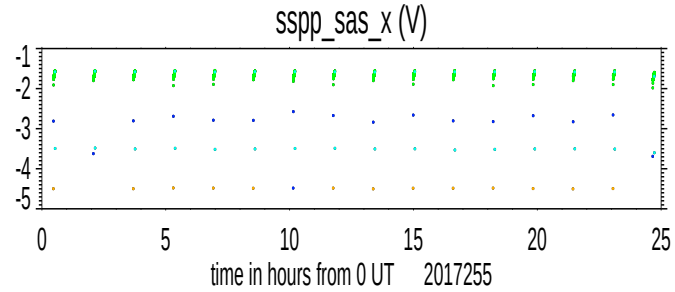

mu_5_conv_temp (C)
26
24
22
0
5
10
15
20
25
time in hours from 0 UT 2017255

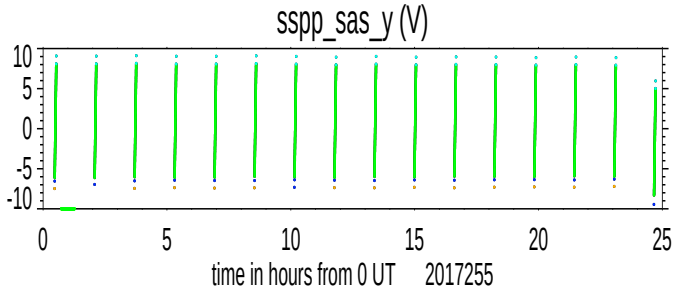

mu_mem_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017255

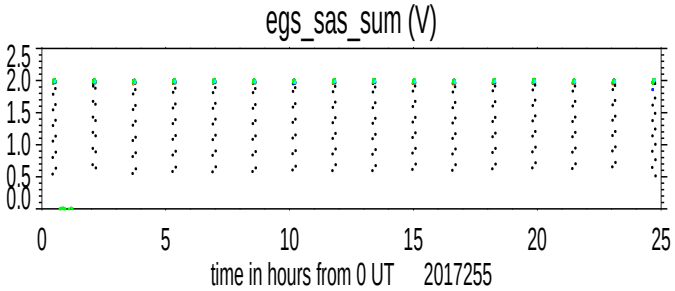

ODC skip bit (1=set)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017255

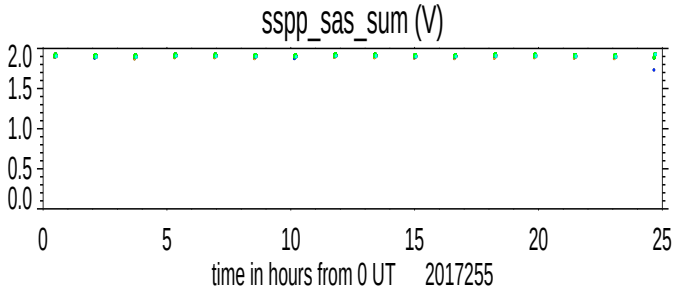

egs_sas_x (V)
time in hours from 0 UT 2017255

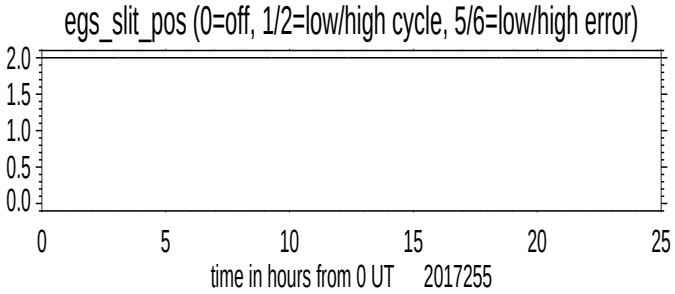

egs_sas_y (V)
time in hours from 0 UT 2017255

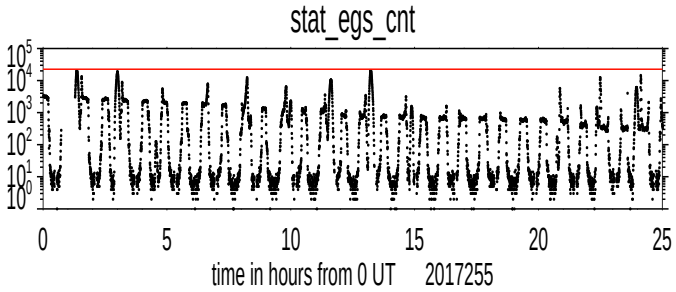

sspp_sas_x (V)
time in hours from 0 UT 2017255

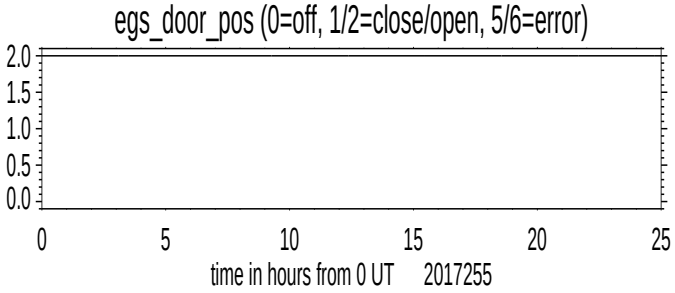

sspp_sas_y (V)
time in hours from 0 UT 2017255

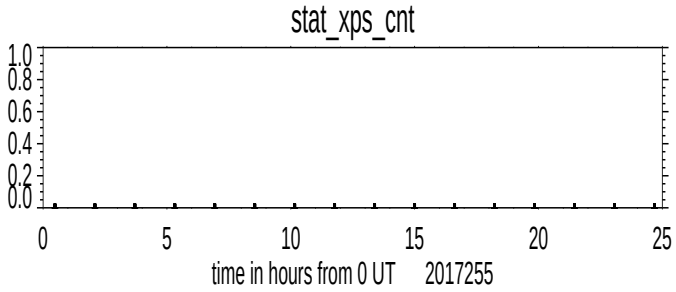

egs_sas_sum (V)
time in hours from 0 UT 2017255

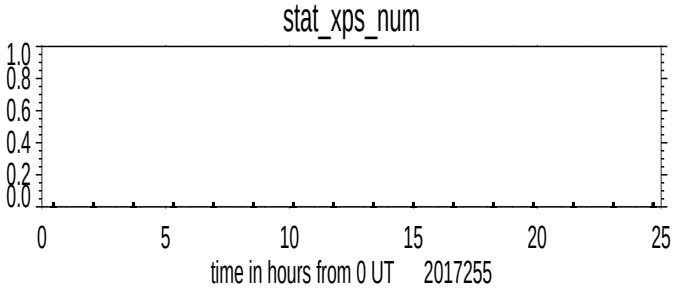

sspp_sas_sum (V)
time in hours from 0 UT 2017255

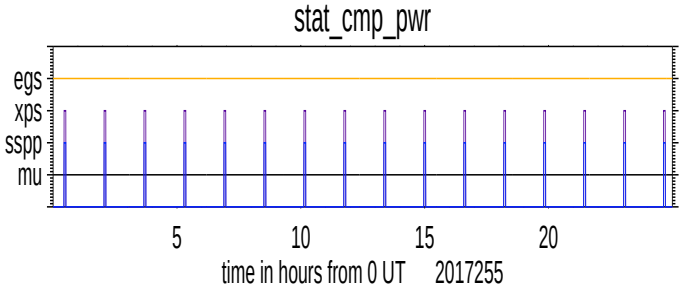

egs_slit_pos (0=off, 1/2=low/high cycle, 5/6=low/high error)
time in hours from 0 UT 2017255


stat_egs_cnt
time in hours from 0 UT 2017255

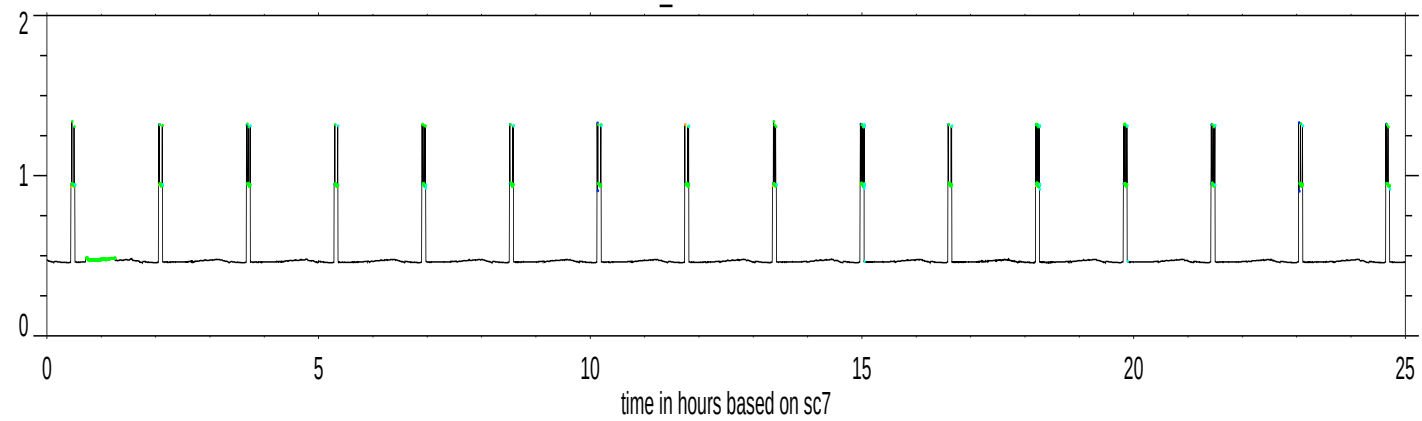

egs_door_pos (0=off, 1/2=close/open, 5/6=error)
time in hours from 0 UT 2017255


stat_xps_cnt
time in hours from 0 UT 2017255

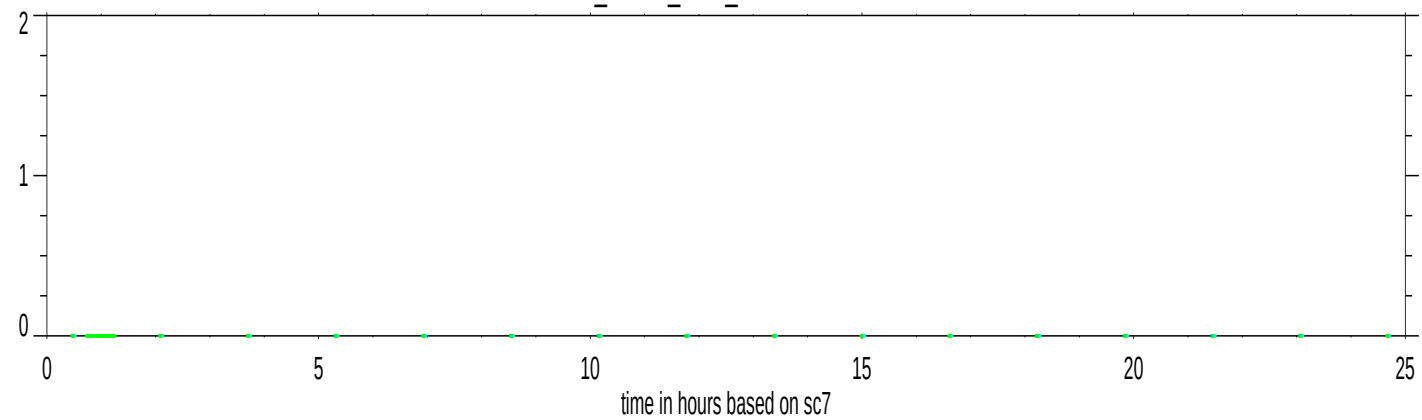

stat_xps_num
time in hours from 0 UT 2017255


stat_cmp_pwr
egs
xps
sspp
mu
time in hours from 0 UT 2017255

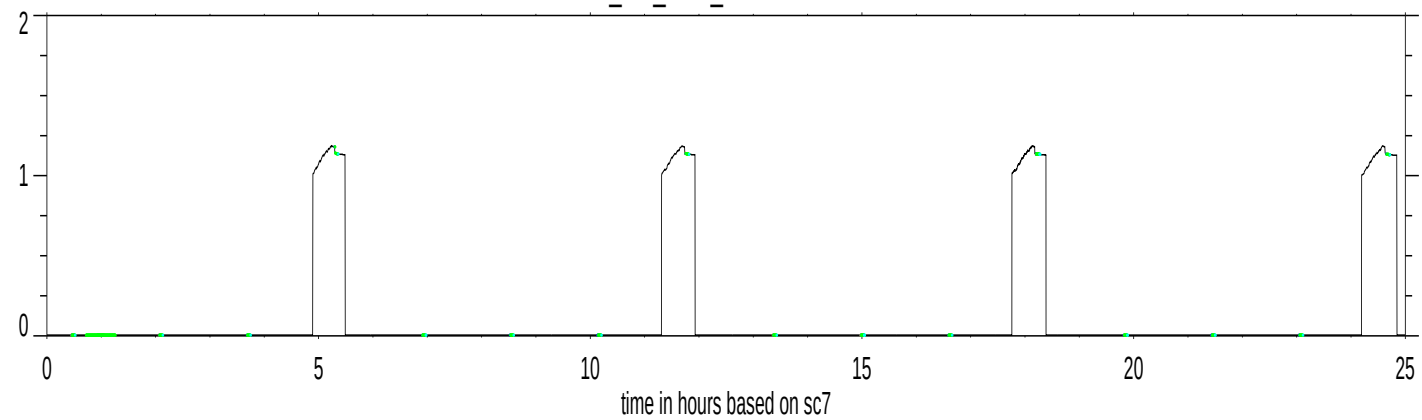
SEE_CUR 2017255



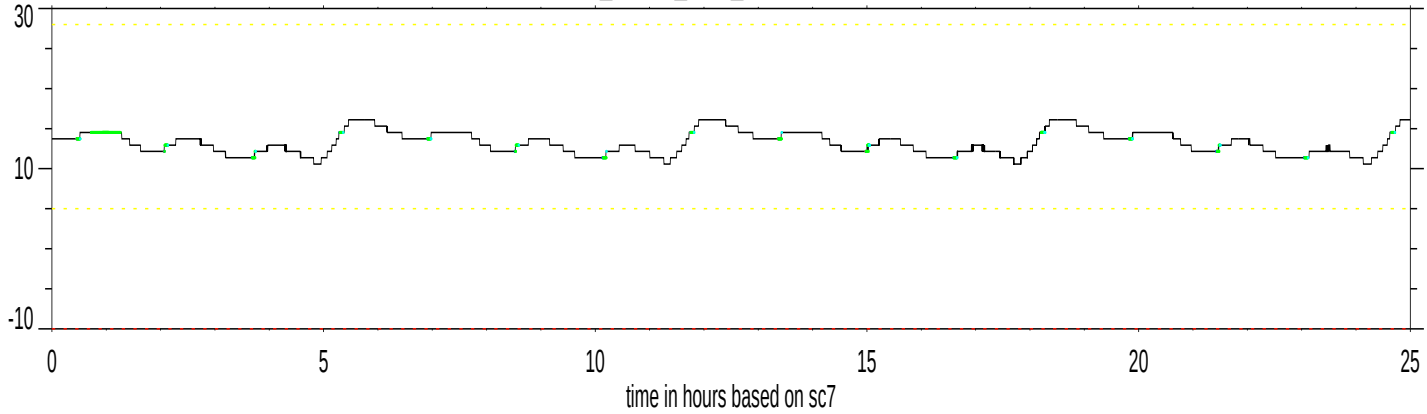
SEE_SURV_HTR_CUR 2017255

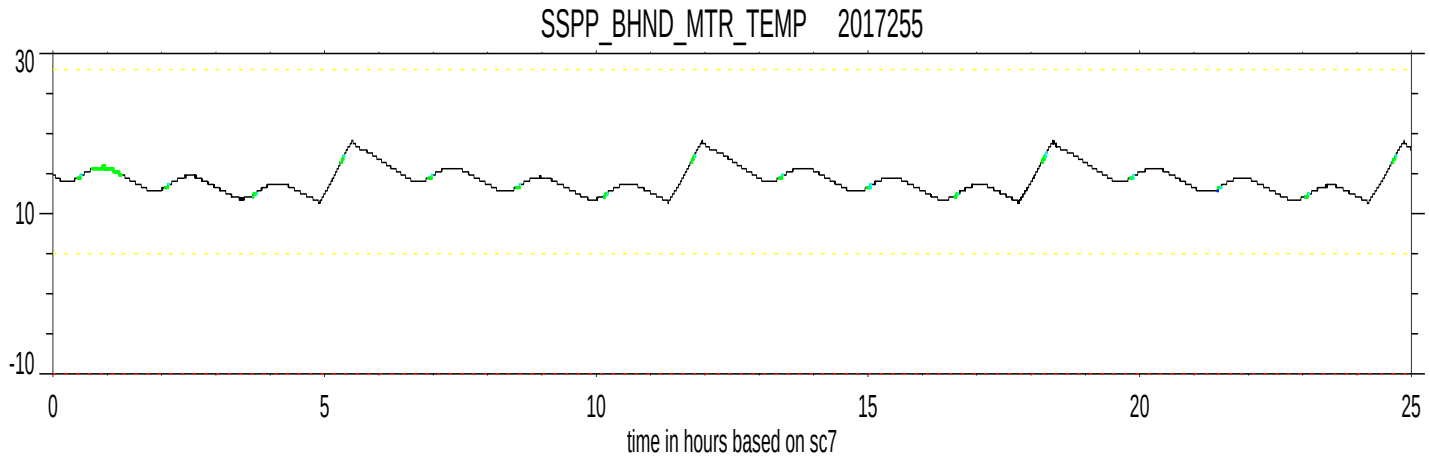


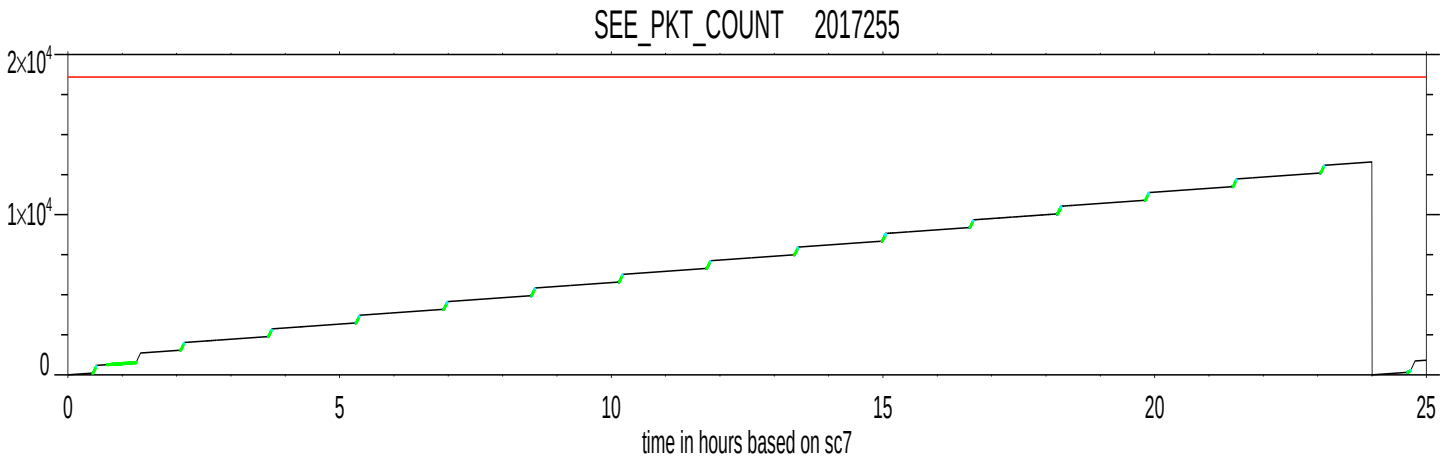
SEE_OP_HTR_CUR 2017255

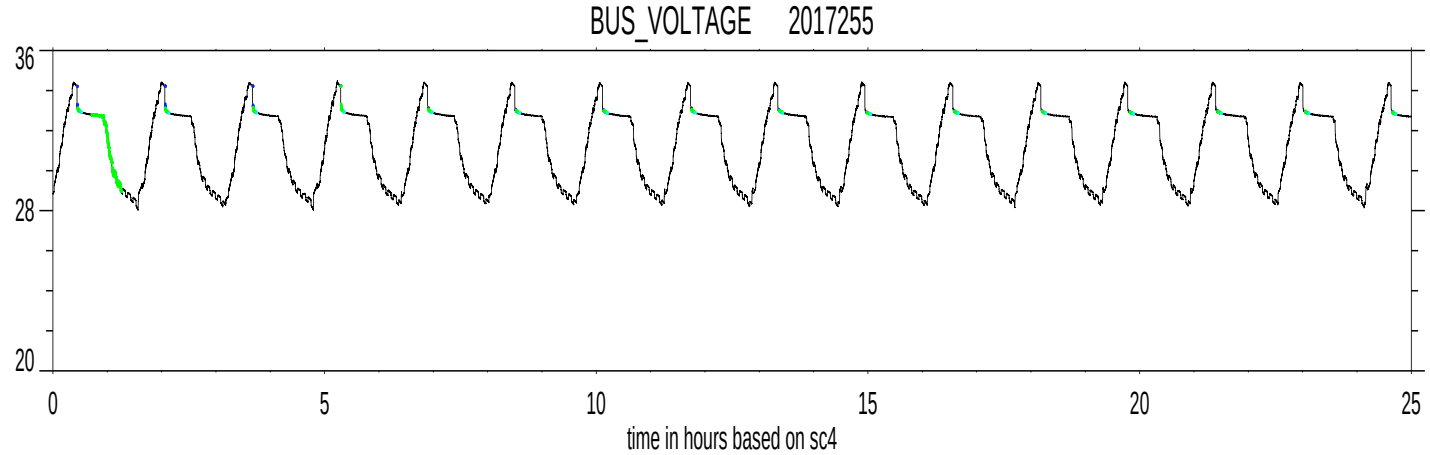


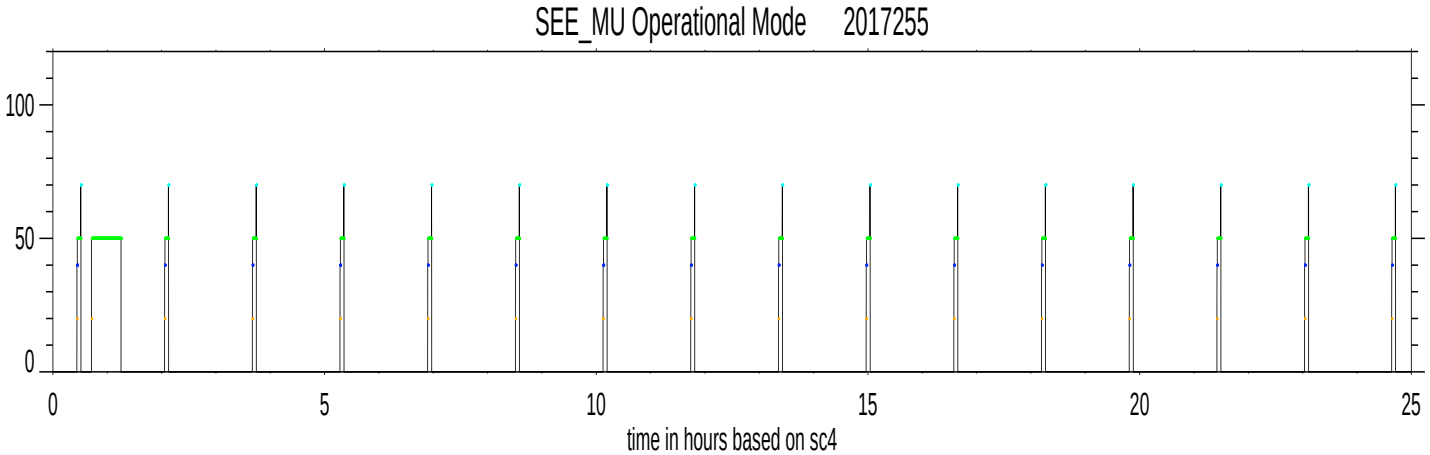
SSPP_BHND_XPS_TEMP 2017255

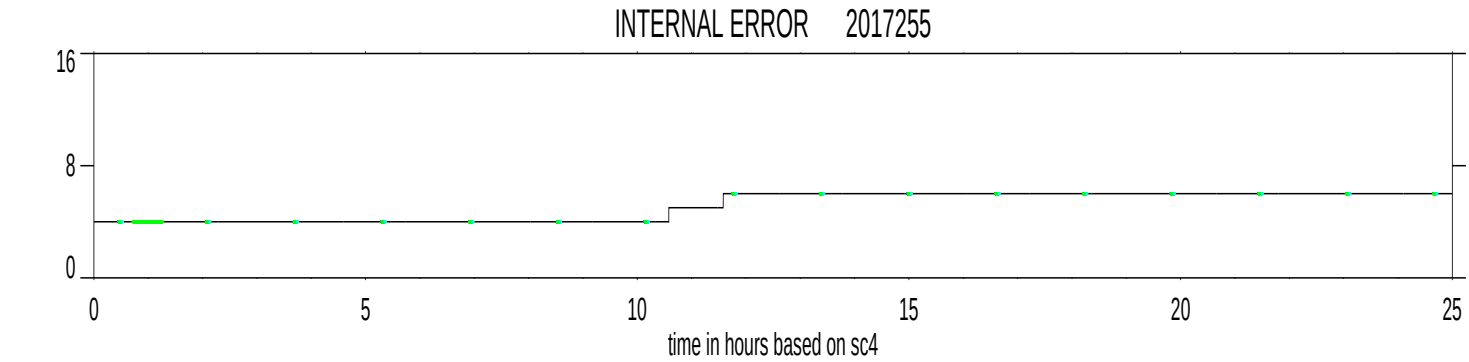


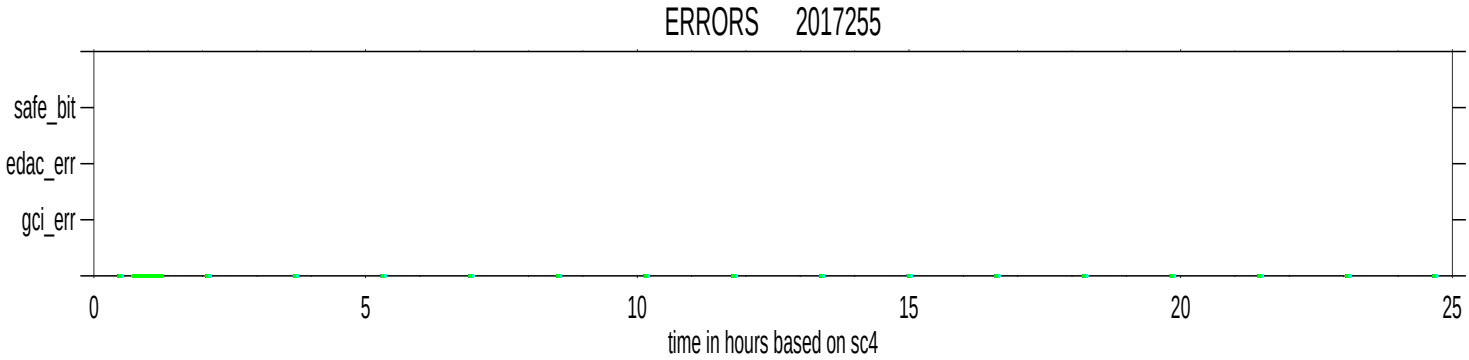

SSPP_BHND_MTR_TEMP 2017255
30
10
-10
0
5
10
15
20
25
time in hours based on sc7

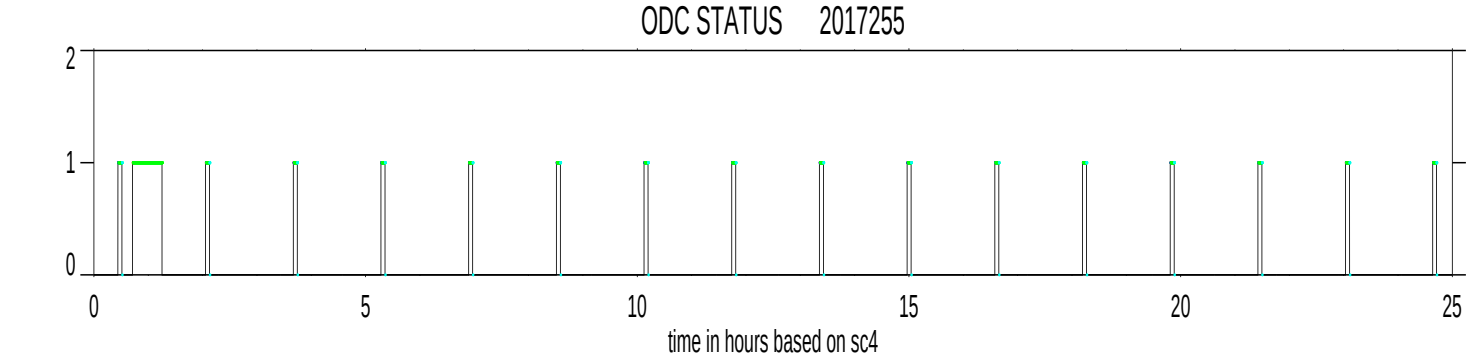

SEE_PKT_COUNT 2017255
2x10^4
1x10^4
0
0
5
10
15
20
25
time in hours based on sc7

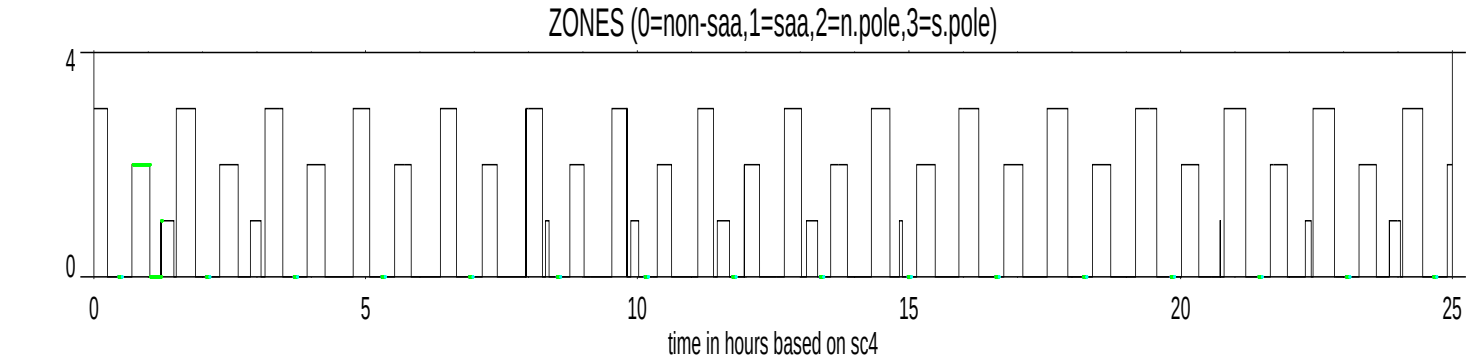

BUS_VOLTAGE 2017255
36
28
20
0
5
10
15
20
25
time in hours based on sc4

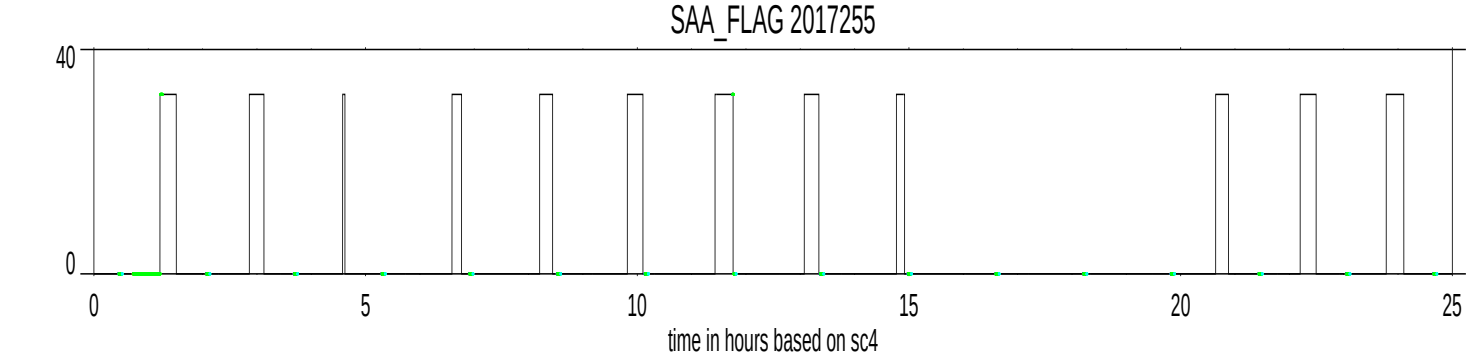

SEE_MU Operational Mode 2017255
100
50
0
0
5
10
15
20
25
time in hours based on sc4

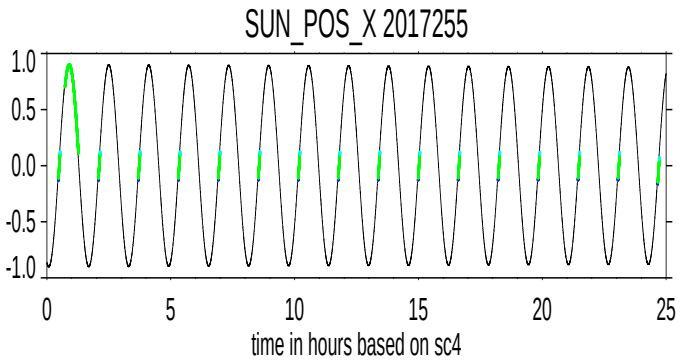

INTERNAL ERROR 2017255
16
8
0
0
5
10
15
20
25
time in hours based on sc4

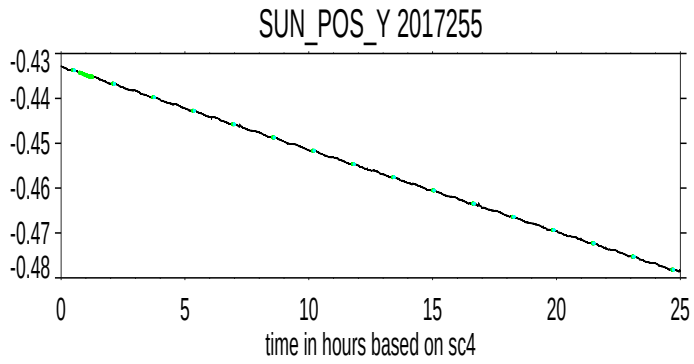

ERRORS 2017255
safe_bit
edac_err
gci_err
0
5
10
15
20
25
time in hours based on sc4

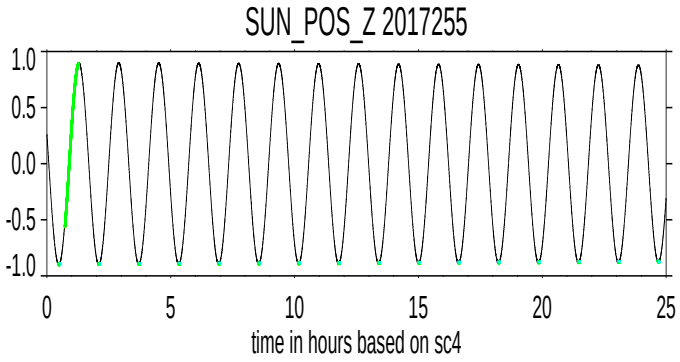

ODC STATUS 2017255
2
1
0
0
5
10
15
20
25
time in hours based on sc4

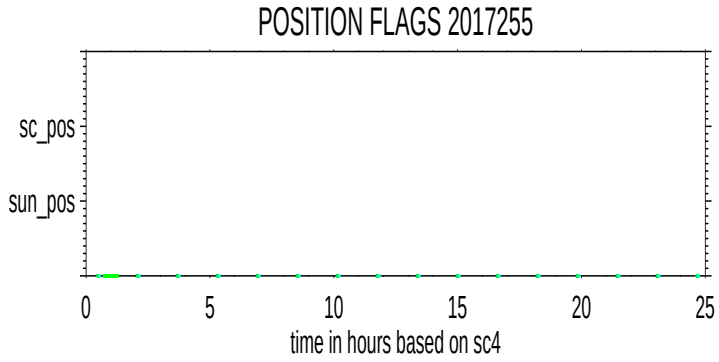

ZONES (0=non-saa,1=saa,2=n.pole,3=s.pole)
4
0
0
5
10
15
20
25
time in hours based on sc4

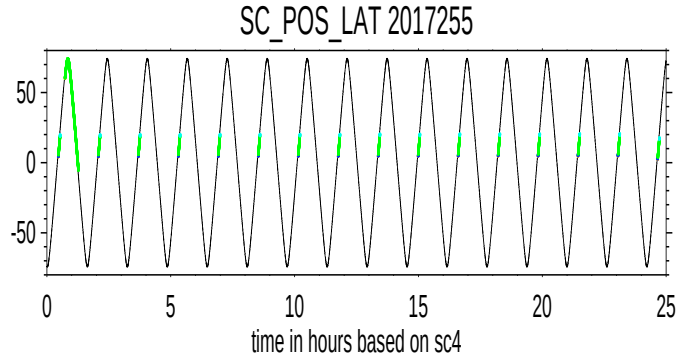

SAA_FLAG 2017255
40
0
0
5
10
15
20
25
time in hours based on sc4

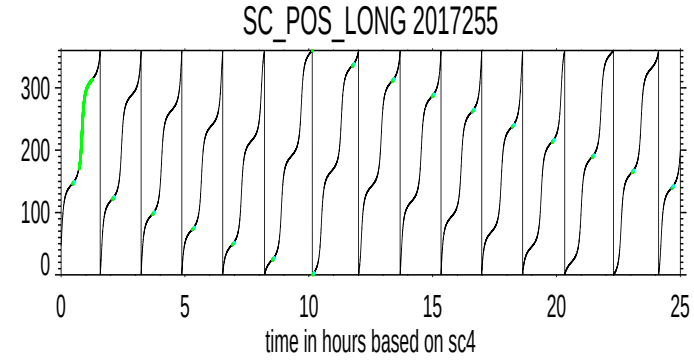

SUN_POS_X 2017255
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4

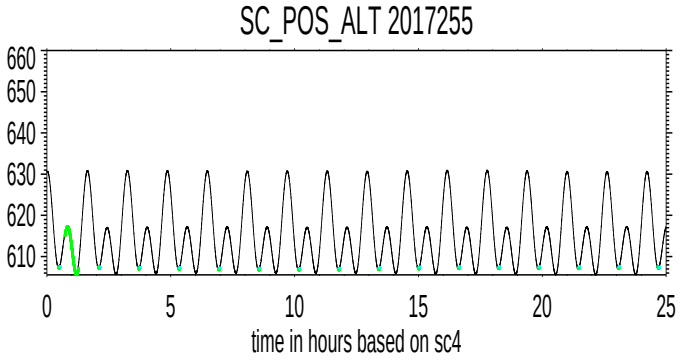

SUN_POS_Y 2017255
-0.43
-0.44
-0.45
-0.46
-0.47
-0.48
0
5
10
15
20
25
time in hours based on sc4

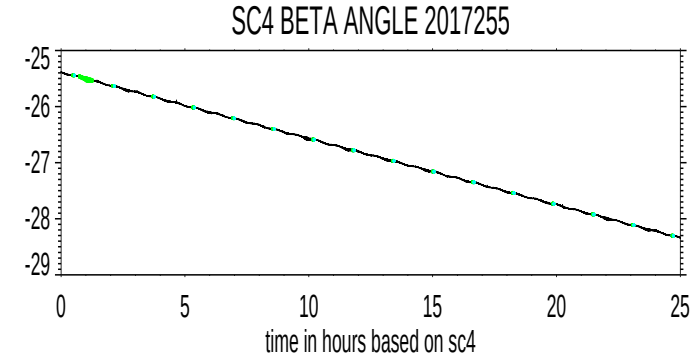

SUN_POS_Z 2017255
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4


POSITION FLAGS 2017255
sc_pos
sun_pos
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LAT 2017255
50
0
-50
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LONG 2017255
300
200
100
0
0
5
10
15
20
25
time in hours based on sc4


SC_POS_ALT 2017255
660
650
640
630
620
610
0
5
10
15
20
25
time in hours based on sc4


SC4 BETA ANGLE 2017255
-25
-26
-27
-28
-29
0
5
10
15
20
25
time in hours based on sc4