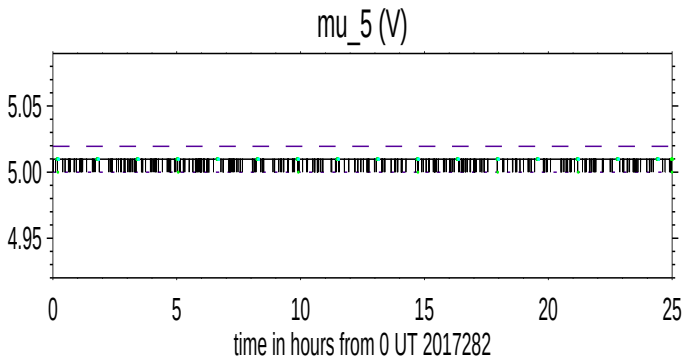
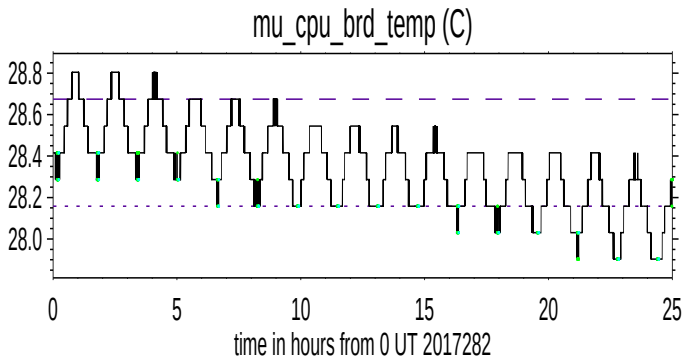
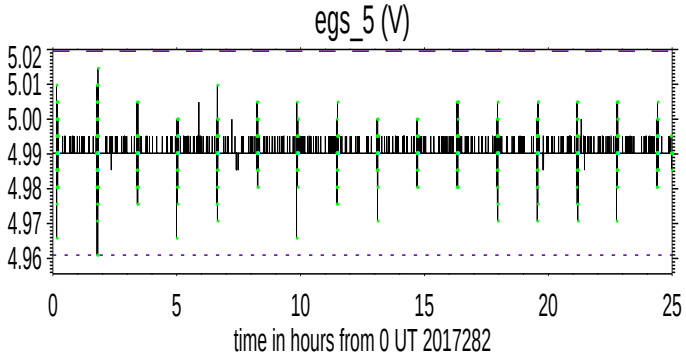
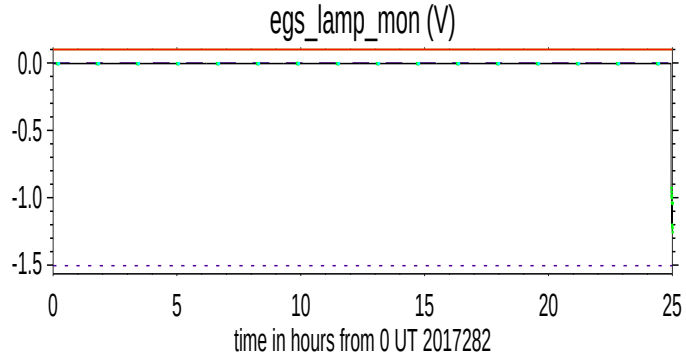
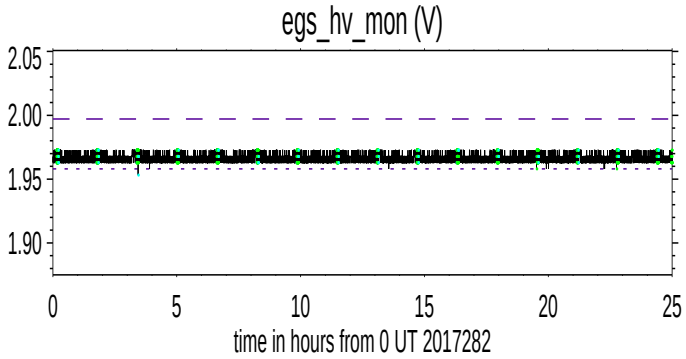
```
Information for      2017282
09:04:48.001: see_stat[1]: 4 internal error
```

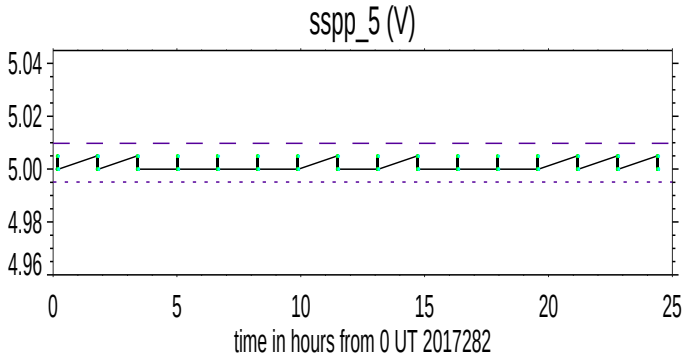

mu_5 (V)
5.05
5.00
4.95
0
5
10
15
20
25
time in hours from 0 UT 2017282

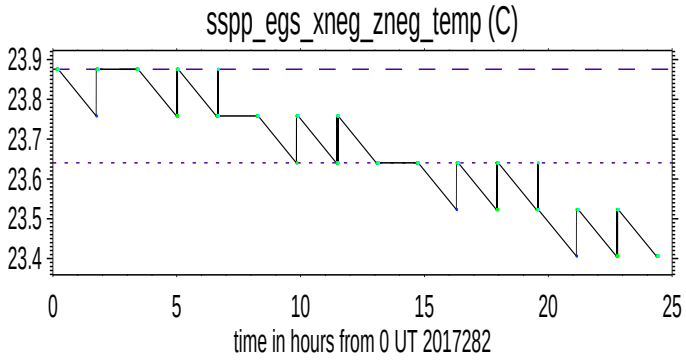

mu_cpu_brd_temp (C)
28.8
28.6
28.4
28.2
28.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

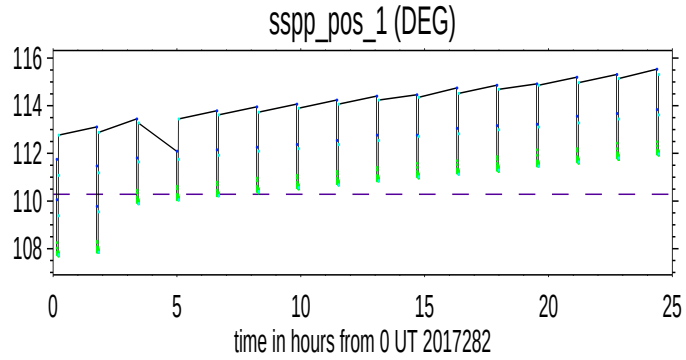

egs_5 (V)
5.02
5.01
5.00
4.99
4.98
4.97
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017282

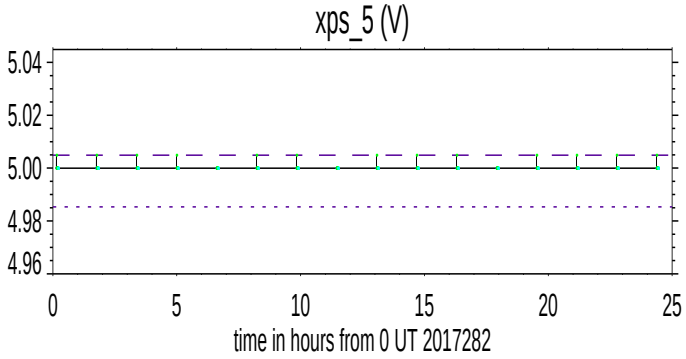

egs_lamp_mon (V)
0.0
-0.5
-1.0
-1.5
0
5
10
15
20
25
time in hours from 0 UT 2017282

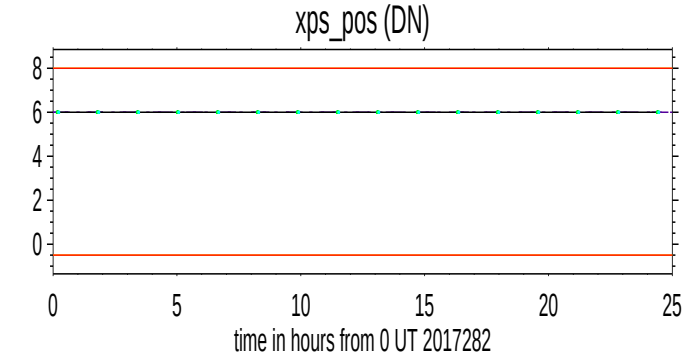

egs_hv_mon (V)
2.05
2.00
1.95
1.90
0
5
10
15
20
25
time in hours from 0 UT 2017282

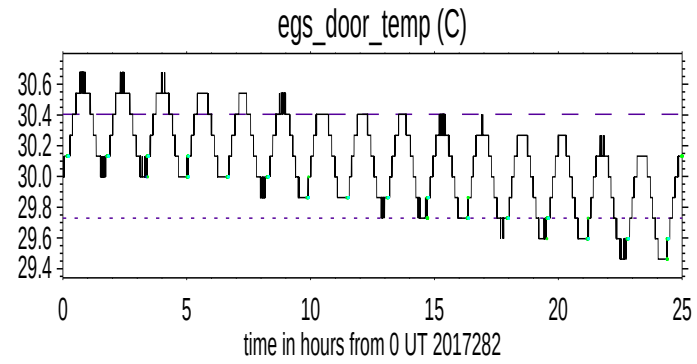

sspp_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017282

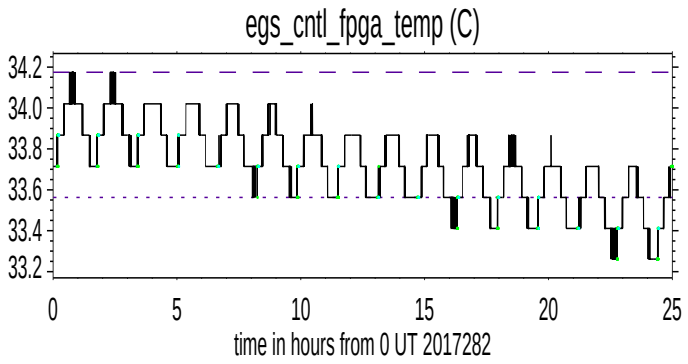

sspp_egs_xneg_zneg_temp (C)
23.9
23.8
23.7
23.6
23.5
23.4
0
5
10
15
20
25
time in hours from 0 UT 2017282

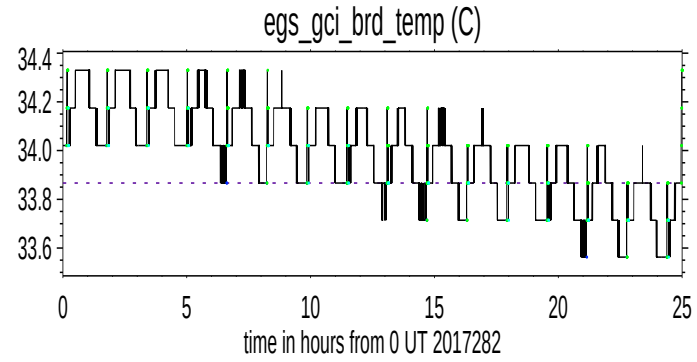

sspp_pos_1 (DEG)
116
114
112
110
108
0
5
10
15
20
25
time in hours from 0 UT 2017282

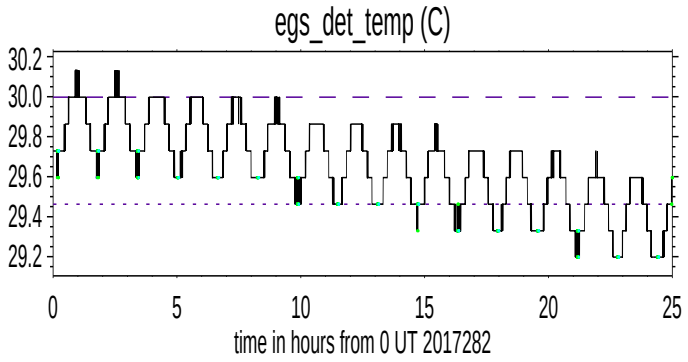

xps_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017282


xps_pos (DN)
8
6
4
2
0
0
5
10
15
20
25
time in hours from 0 UT 2017282


egs_door_temp (C)
30.6
30.4
30.2
30.0
29.8
29.6
29.4
0
5
10
15
20
25
time in hours from 0 UT 2017282


egs_cntl_fpga_temp (C)
34.2
34.0
33.8
33.6
33.4
33.2
0
5
10
15
20
25
time in hours from 0 UT 2017282


egs_gci_brd_temp (C)
34.4
34.2
34.0
33.8
33.6
0
5
10
15
20
25
time in hours from 0 UT 2017282


egs_det_temp (C)
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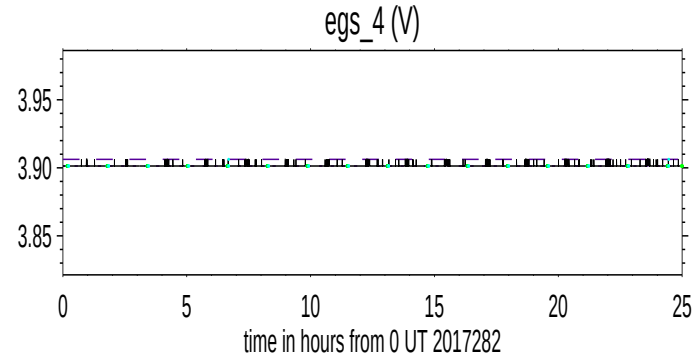
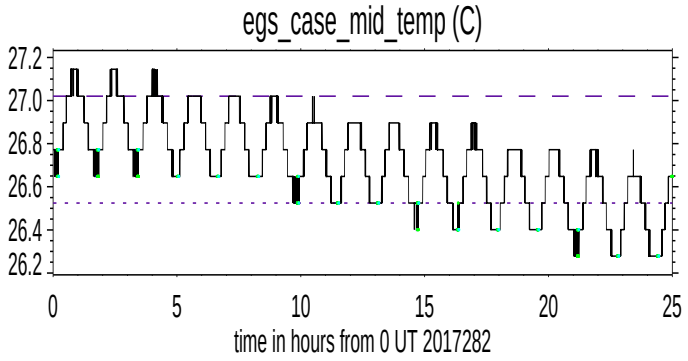
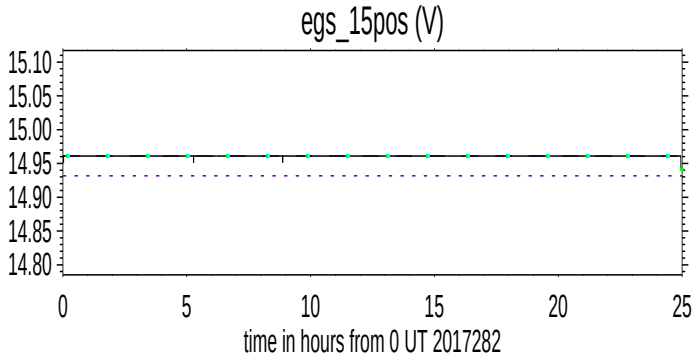
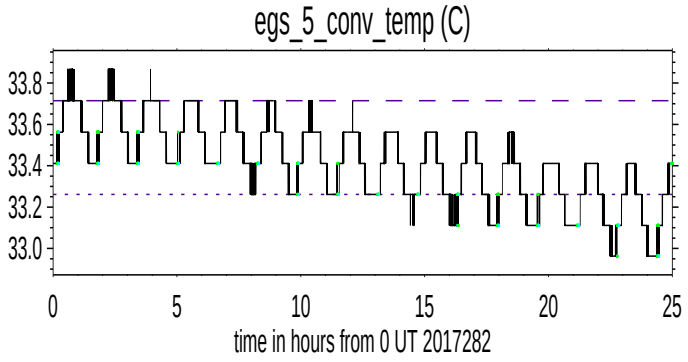
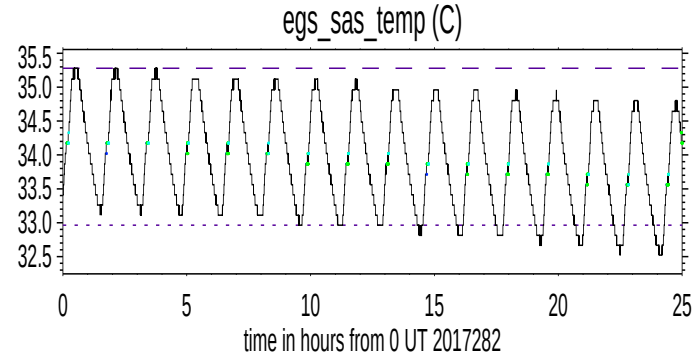
30.0
29.8
29.6
29.4
29.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

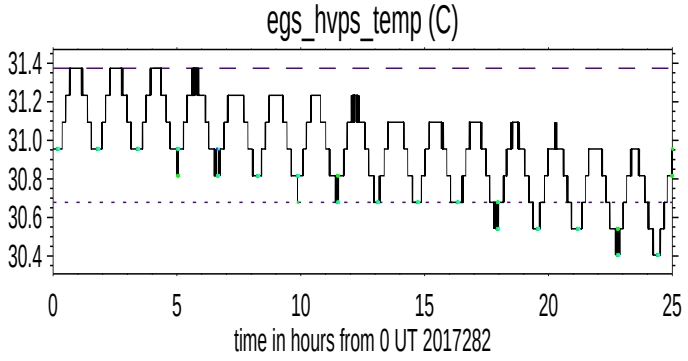

egs_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017282

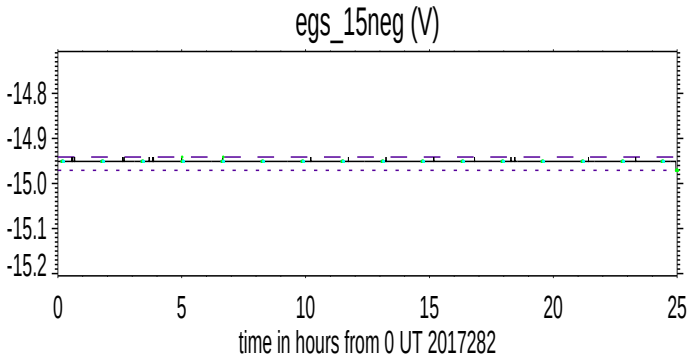

egs_case_mid_temp (C)
27.2
27.0
26.8
26.6
26.4
26.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

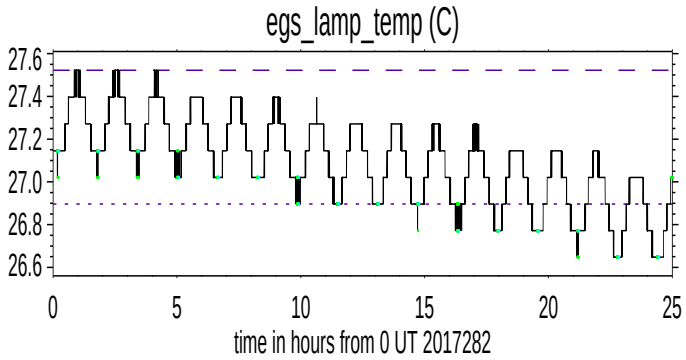

egs_15pos (V)
15.10
15.05
15.00
14.95
14.90
14.85
14.80
0
5
10
15
20
25
time in hours from 0 UT 2017282

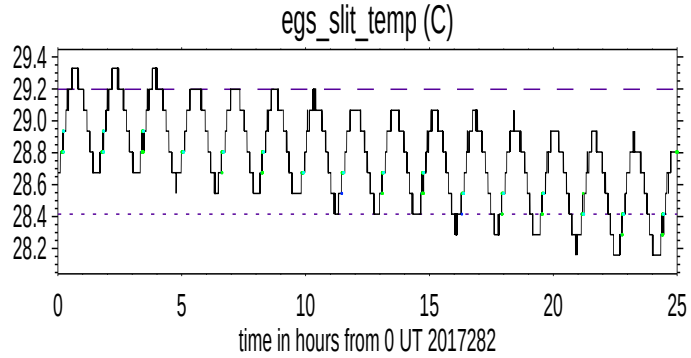

egs_5_conv_temp (C)
33.8
33.6
33.4
33.2
33.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

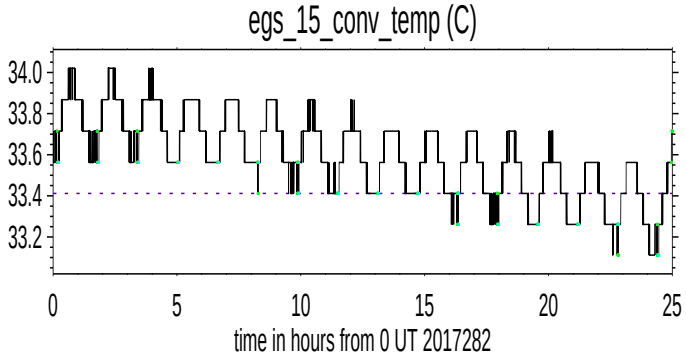

egs_sas_temp (C)
35.5
35.0
34.5
34.0
33.5
33.0
32.5
0
5
10
15
20
25
time in hours from 0 UT 2017282

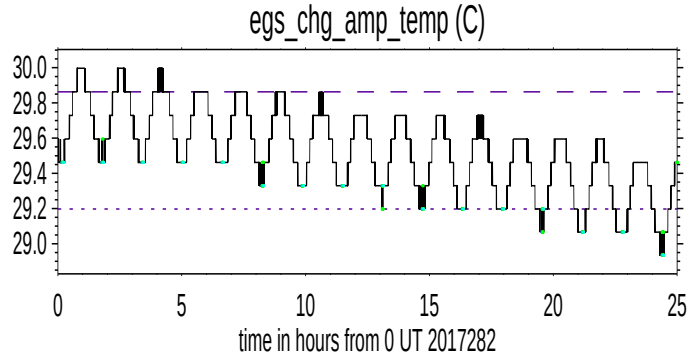

egs_hvps_temp (C)
31.4
31.2
31.0
30.8
30.6
30.4
0
5
10
15
20
25
time in hours from 0 UT 2017282

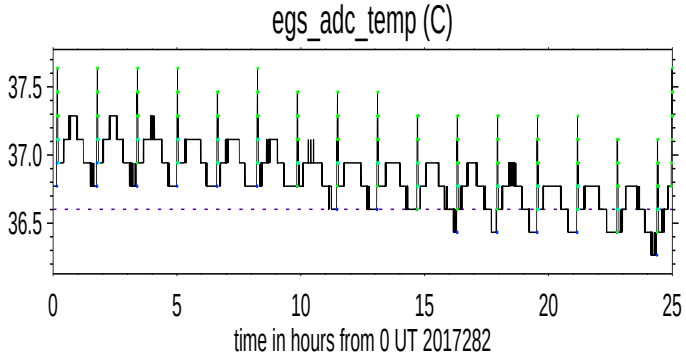

egs_15neg (V)
-14.8
-14.9
-15.0
-15.1
-15.2
0 5 10 15 20 25
time in hours from 0 UT 2017282

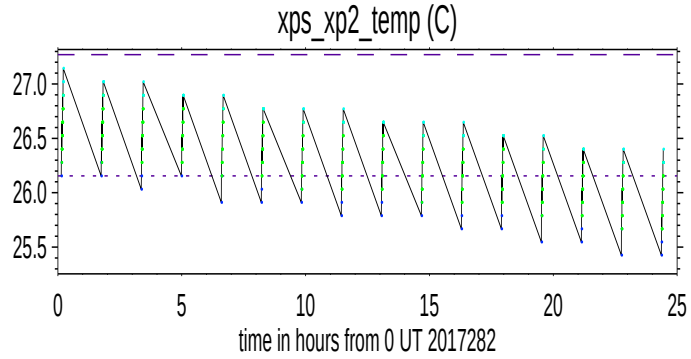

egs_lamp_temp (C)
27.6
27.4
27.2
27.0
26.8
26.6
0 5 10 15 20 25
time in hours from 0 UT 2017282

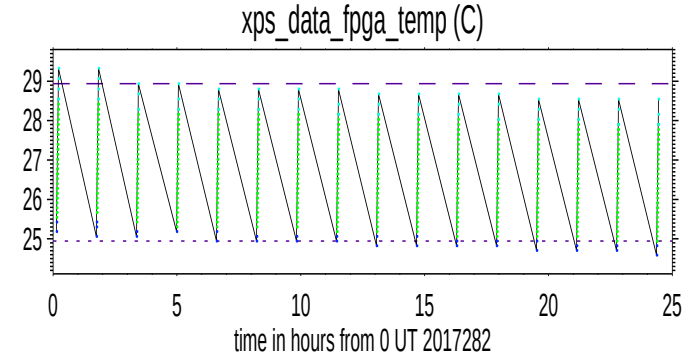

egs_slit_temp (C)
29.4
29.2
29.0
28.8
28.6
28.4
28.2
0 5 10 15 20 25
time in hours from 0 UT 2017282

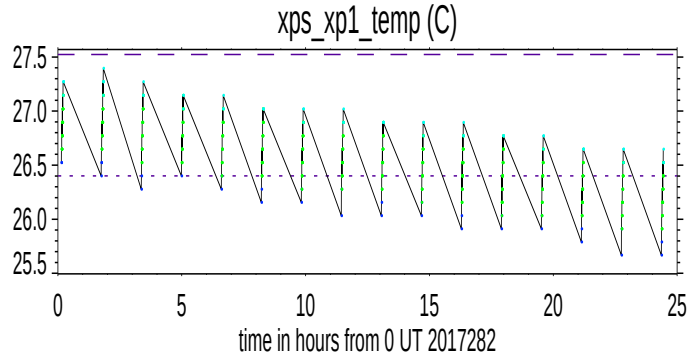

egs_15_conv_temp (C)
34.0
33.8
33.6
33.4
33.2
0 5 10 15 20 25
time in hours from 0 UT 2017282

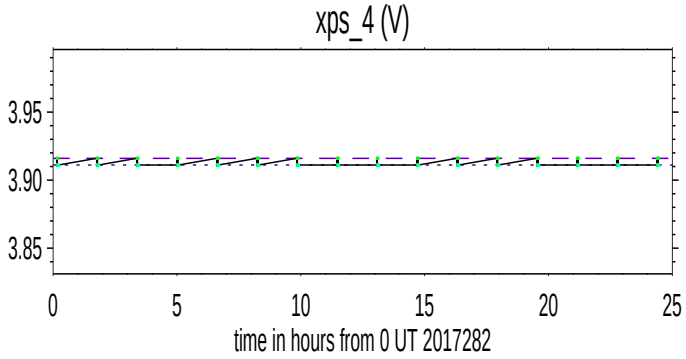

egs_chg_amp_temp (C)
30.0
29.8
29.6
29.4
29.2
29.0
0 5 10 15 20 25
time in hours from 0 UT 2017282

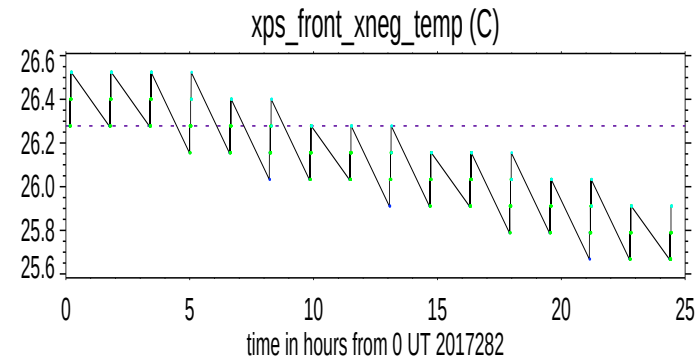

egs_adc_temp (C)
37.5
37.0
36.5
0 5 10 15 20 25
time in hours from 0 UT 2017282

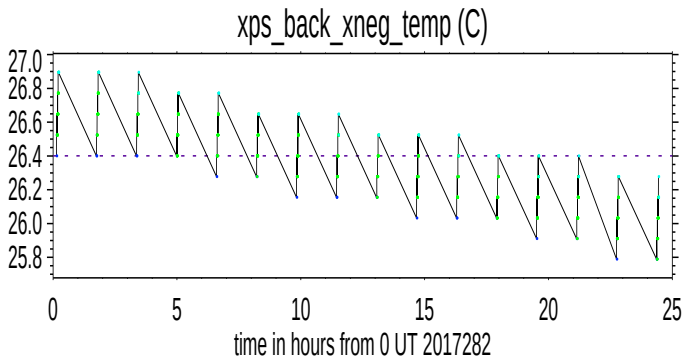

xps_xp2_temp (C)
27.0
26.5
26.0
25.5
0 5 10 15 20 25
time in hours from 0 UT 2017282

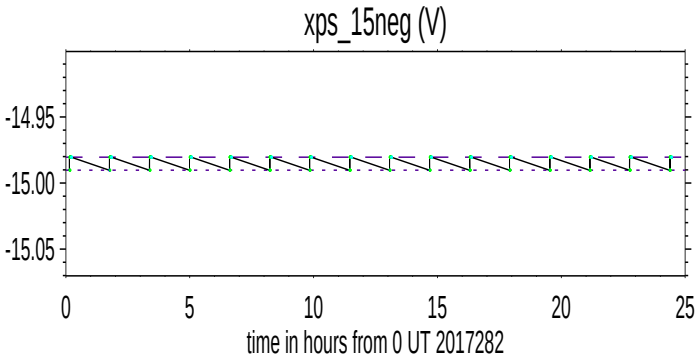

xps_data_fpga_temp (C)
29
28
27
26
25
0 5 10 15 20 25
time in hours from 0 UT 2017282

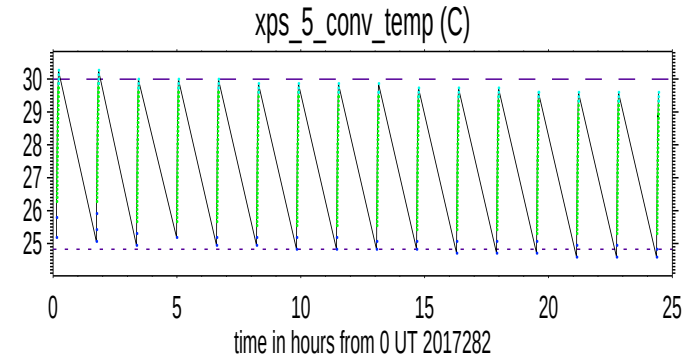

xps_xp1_temp (C)
27.5
27.0
26.5
26.0
25.5
0 5 10 15 20 25
time in hours from 0 UT 2017282

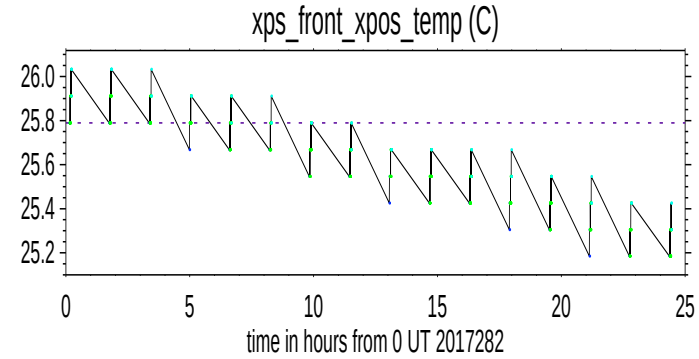

xps_4 (V)
3.95
3.90
3.85
0 5 10 15 20 25
time in hours from 0 UT 2017282

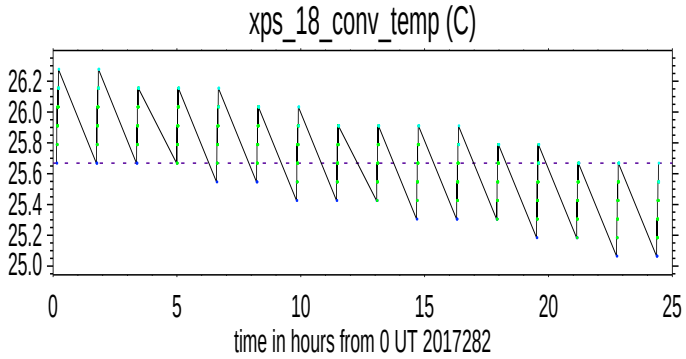

xps_front_xneg_temp (C)
26.6
26.4
26.2
26.0
25.8
25.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

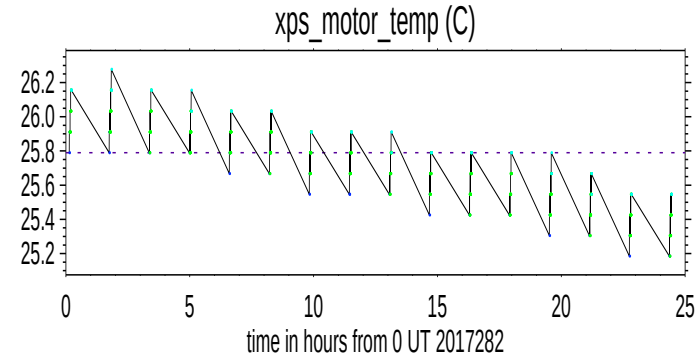

xps_back_xneg_temp (C)
27.0
26.8
26.6
26.4
26.2
26.0
25.8
0
5
10
15
20
25
time in hours from 0 UT 2017282

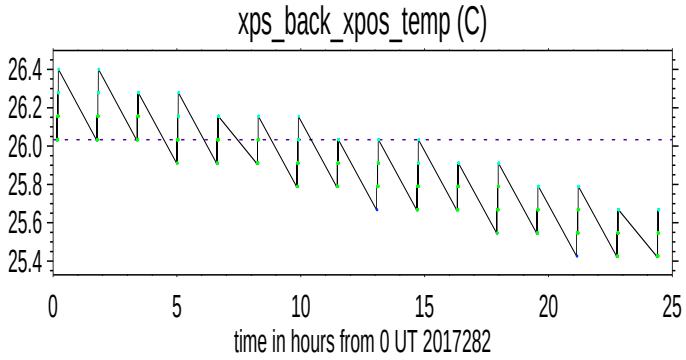

xps_15neg (V)
-14.95
-15.00
-15.05
0
5
10
15
20
25
time in hours from 0 UT 2017282

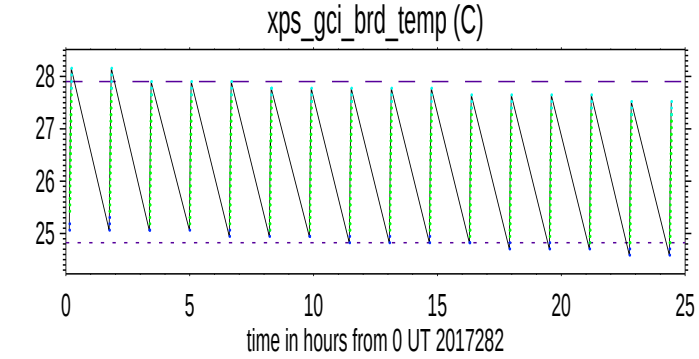

xps_5_conv_temp (C)
30
29
28
27
26
25
0
5
10
15
20
25
time in hours from 0 UT 2017282

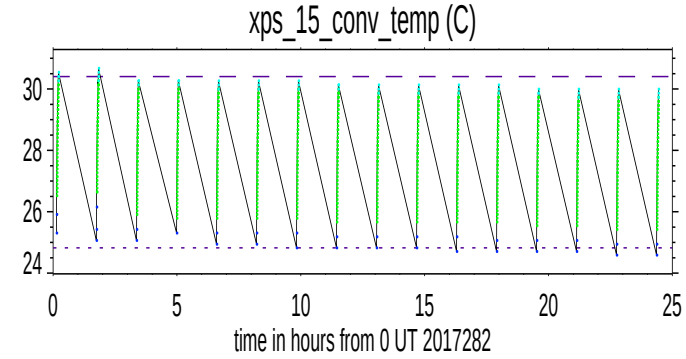

xps_front_xpos_temp (C)
26.0
25.8
25.6
25.4
25.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

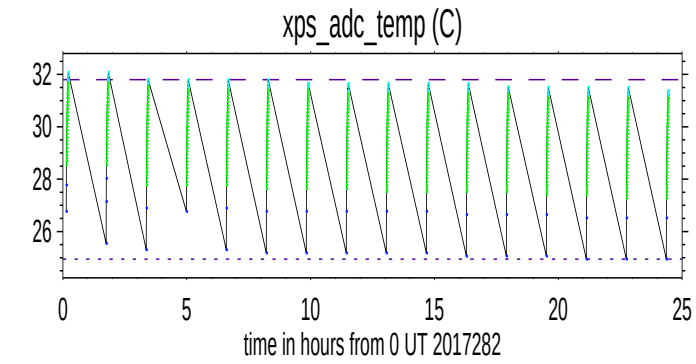

xps_18_conv_temp (C)
26.2
26.0
25.8
25.6
25.4
25.2
25.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

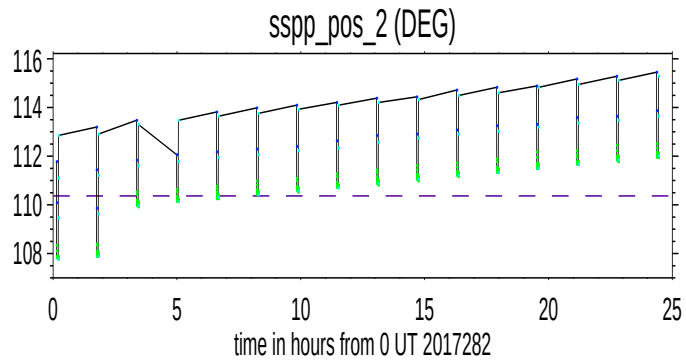

xps_motor_temp (C)
26.2
26.0
25.8
25.6
25.4
25.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

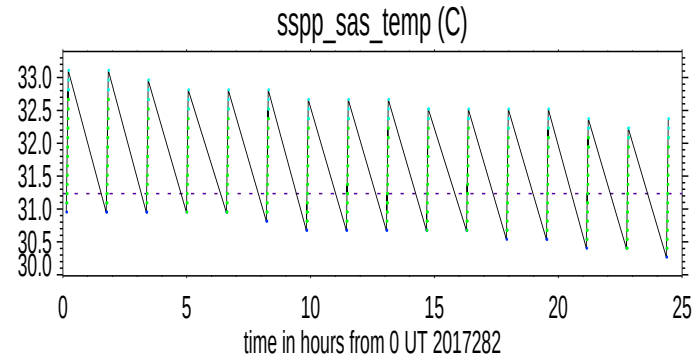

xps_back_xpos_temp (C)
26.4
26.2
26.0
25.8
25.6
25.4
0
5
10
15
20
25
time in hours from 0 UT 2017282

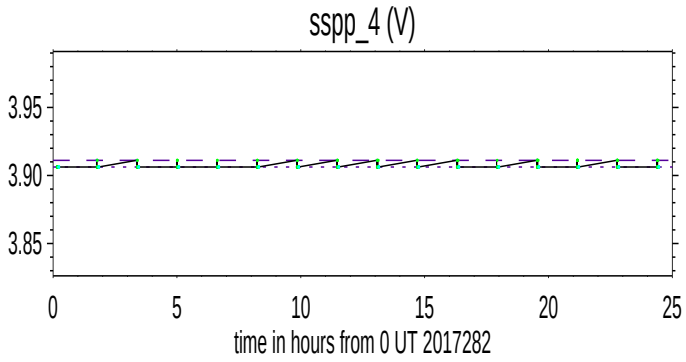

xps_gci_brd_temp (C)
28
27
26
25
0
5
10
15
20
25
time in hours from 0 UT 2017282

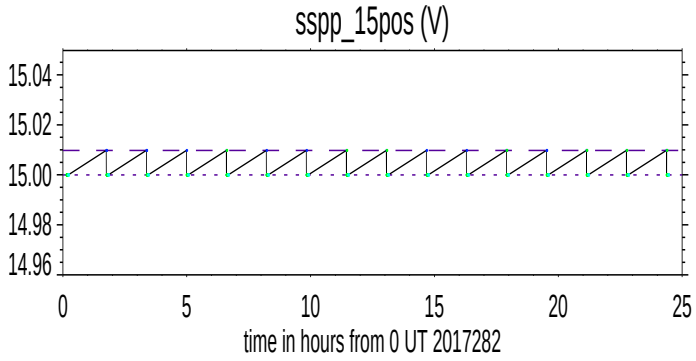

xps_15_conv_temp (C)
30
28
26
24
0
5
10
15
20
25
time in hours from 0 UT 2017282

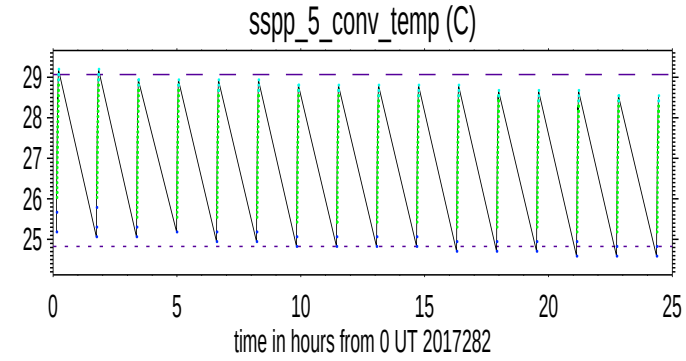

xps_adc_temp (C)
32
30
28
26
0
5
10
15
20
25
time in hours from 0 UT 2017282

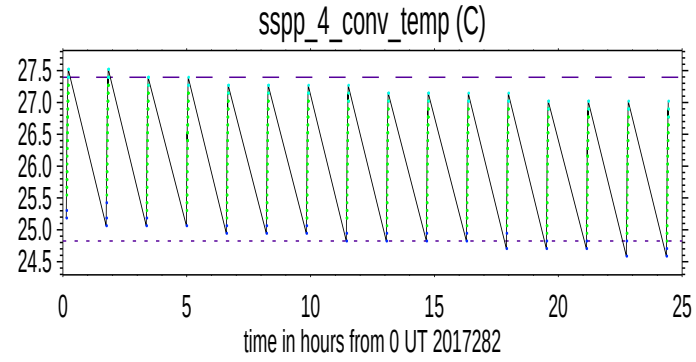

sspp_pos_2 (DEG)
116
114
112
110
108
0
5
10
15
20
25
time in hours from 0 UT 2017282

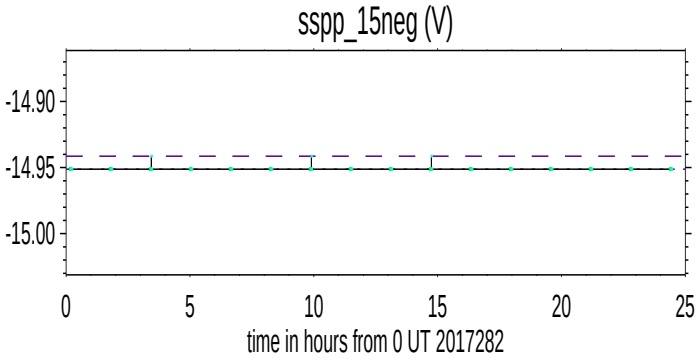

sspp_sas_temp (C)
33.0
32.5
32.0
31.5
31.0
30.5
30.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

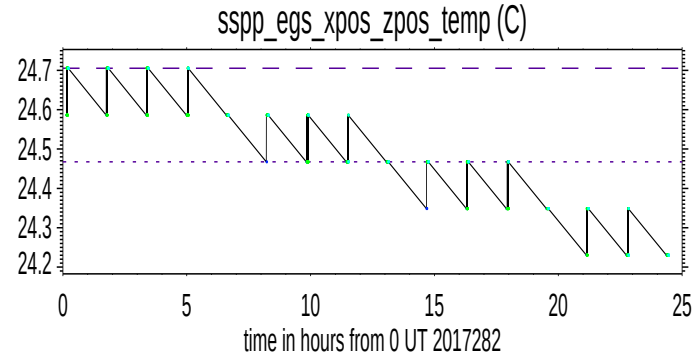

sspp_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017282

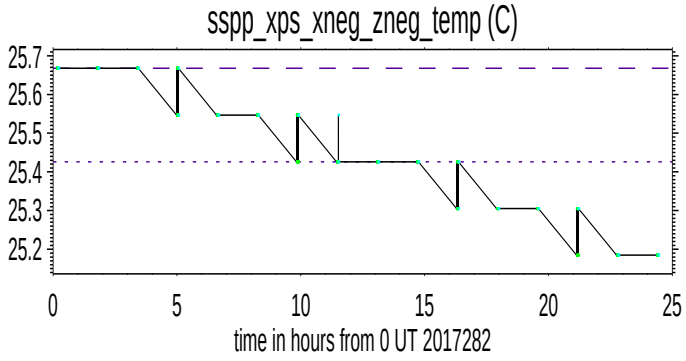

sspp_15pos (V)
15.04
15.02
15.00
14.98
14.96
0
5
10
15
20
25
time in hours from 0 UT 2017282

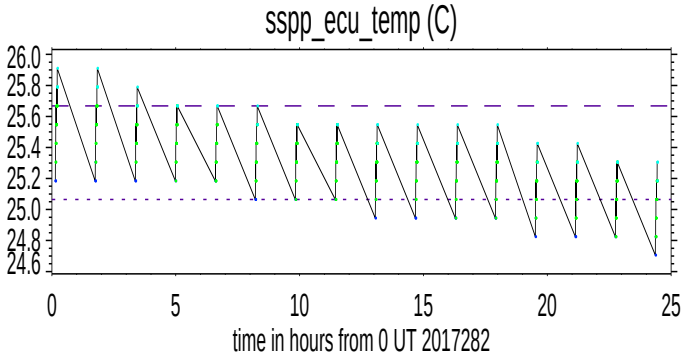

sspp_5_conv_temp (C)
29
28
27
26
25
0
5
10
15
20
25
time in hours from 0 UT 2017282

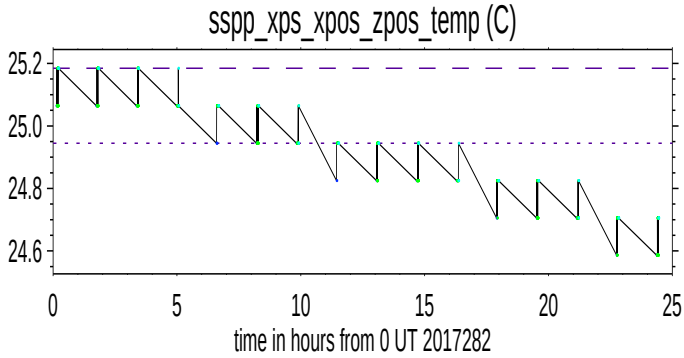

sspp_4_conv_temp (C)
27.5
27.0
26.5
26.0
25.5
25.0
24.5
0
5
10
15
20
25
time in hours from 0 UT 2017282

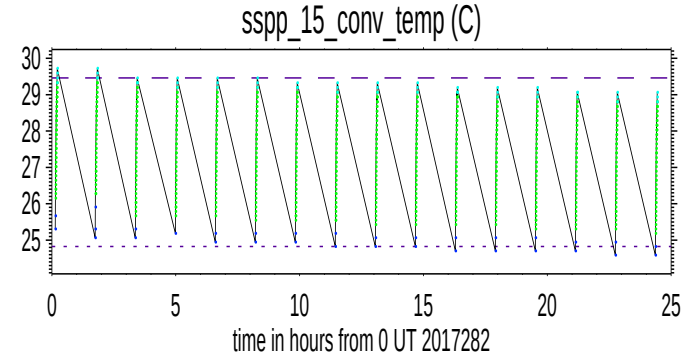

sspp_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2017282

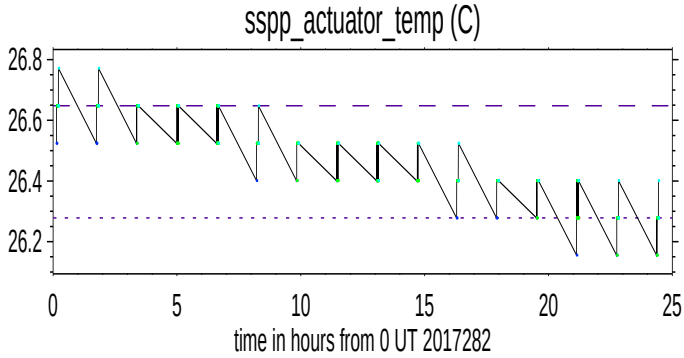

sspp_egs_xpos_zpos_temp (C)
24.7
24.6
24.5
24.4
24.3
24.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

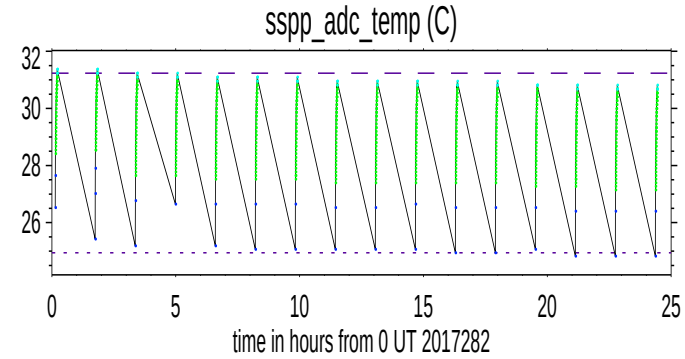

sspp_xps_xneg_zneg_temp (C)
25.7
25.6
25.5
25.4
25.3
25.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

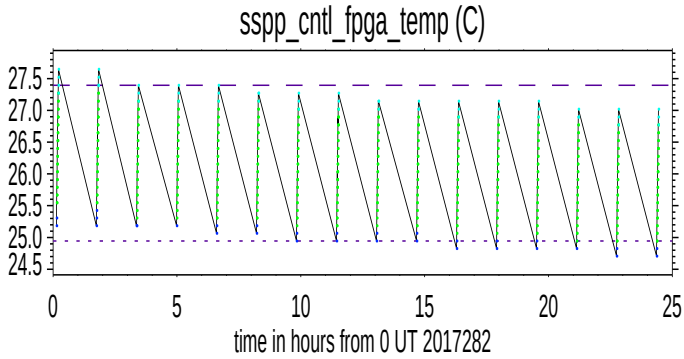

sspp_ecu_temp (C)
26.0
25.8
25.6
25.4
25.2
25.0
24.8
24.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

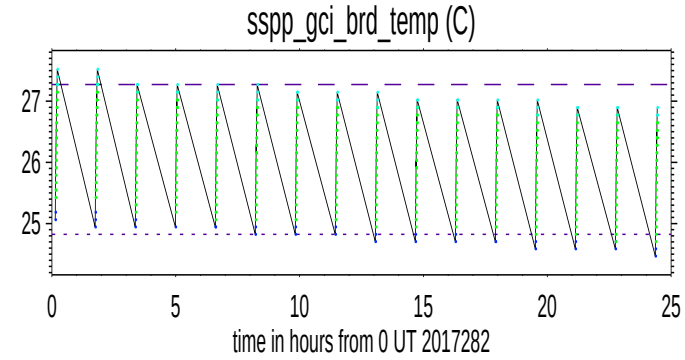

sspp_xps_xpos_zpos_temp (C)
25.2
25.0
24.8
24.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

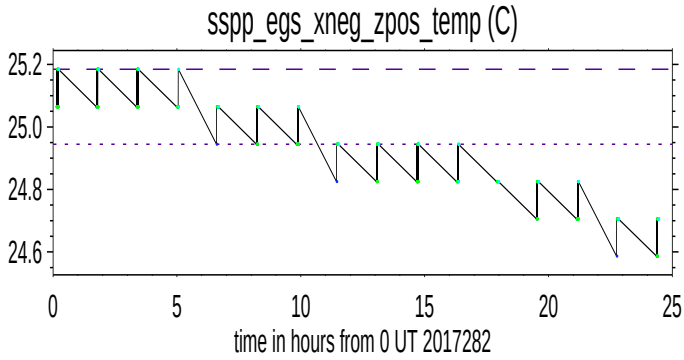

sspp_15_conv_temp (C)
30
29
28
27
26
25
0
5
10
15
20
25
time in hours from 0 UT 2017282

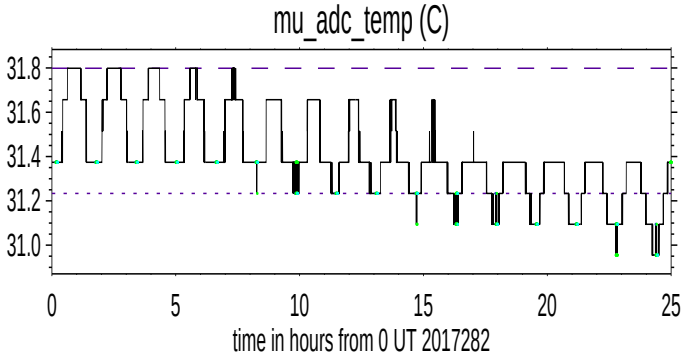

sspp_actuator_temp (C)
26.8
26.6
26.4
26.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

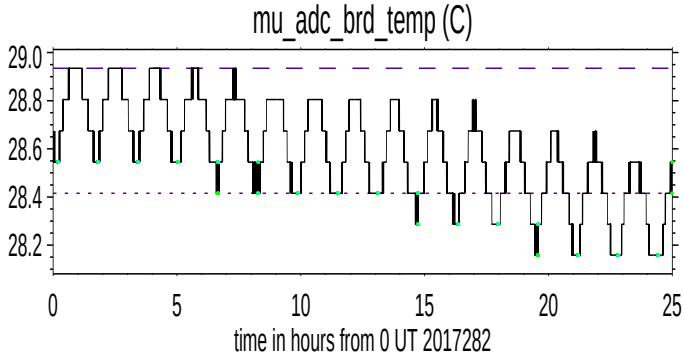

sspp_adc_temp (C)
32
30
28
26
0
5
10
15
20
25
time in hours from 0 UT 2017282

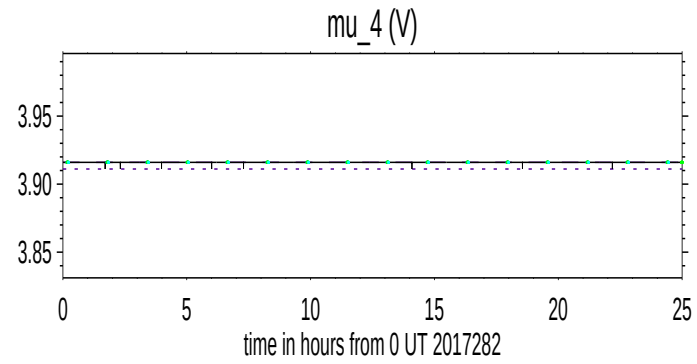

sspp_cntl_fpga_temp (C)
27.5
27.0
26.5
26.0
25.5
25.0
24.5
0
5
10
15
20
25
time in hours from 0 UT 2017282

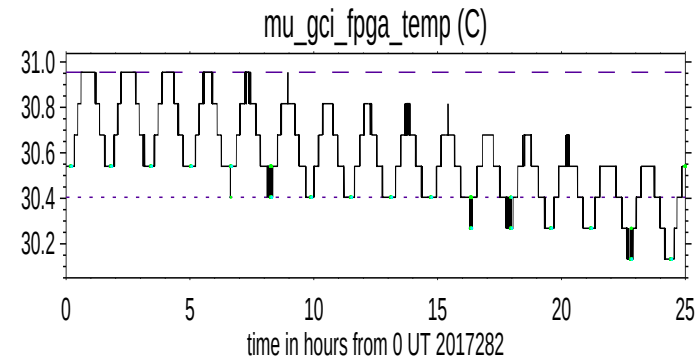

sspp_gci_brd_temp (C)
27
26
25
0
5
10
15
20
25
time in hours from 0 UT 2017282

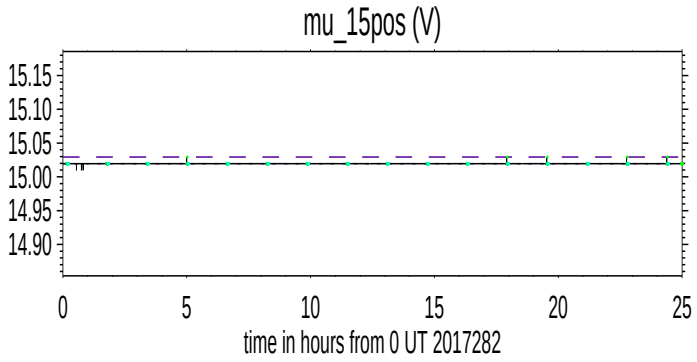

sspp_egs_xneg_zpos_temp (C)
25.2
25.0
24.8
24.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

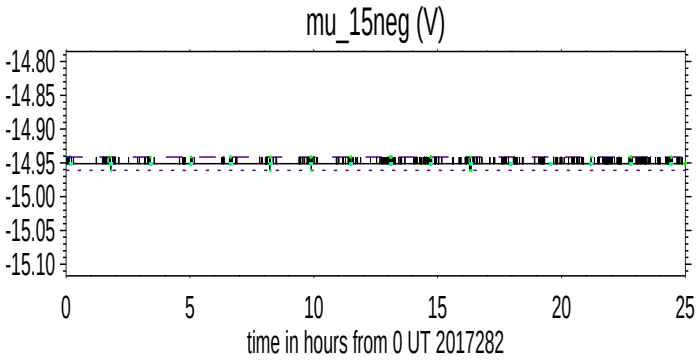

mu_adc_temp (C)
31.8
31.6
31.4
31.2
31.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

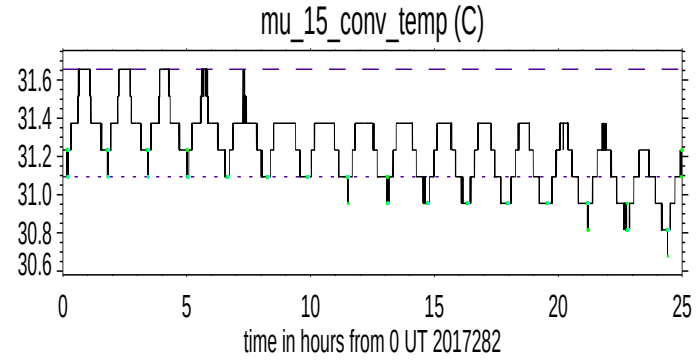

mu_adc_brd_temp (C)
29.0
28.8
28.6
28.4
28.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

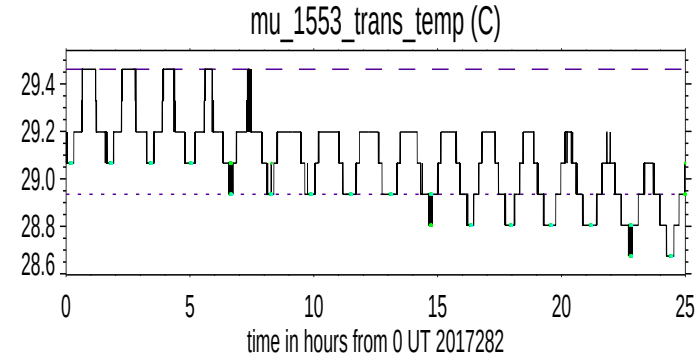

mu_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017282

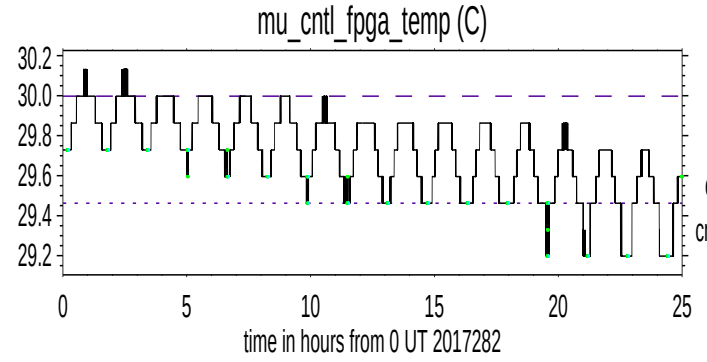

mu_gci_fpga_temp (C)
31.0
30.8
30.6
30.4
30.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

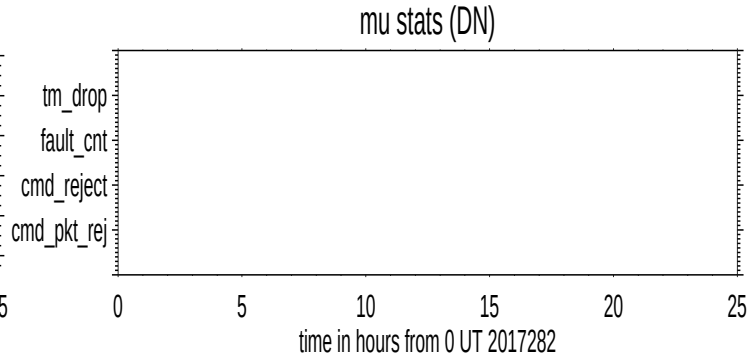

mu_15pos (V)
15.15
15.10
15.05
15.00
14.95
14.90
0
5
10
15
20
25
time in hours from 0 UT 2017282

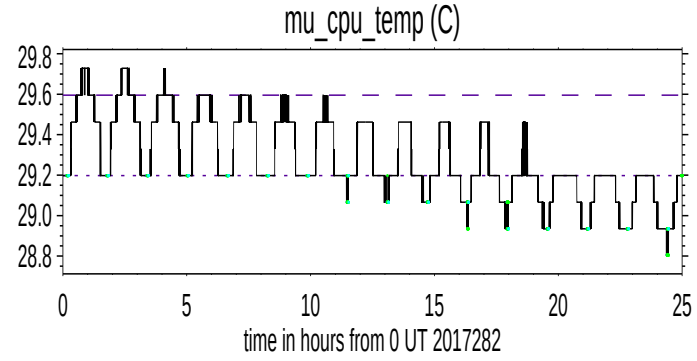

mu_15neg (V)
-14.80
-14.85
-14.90
-14.95
-15.00
-15.05
-15.10
0
5
10
15
20
25
time in hours from 0 UT 2017282

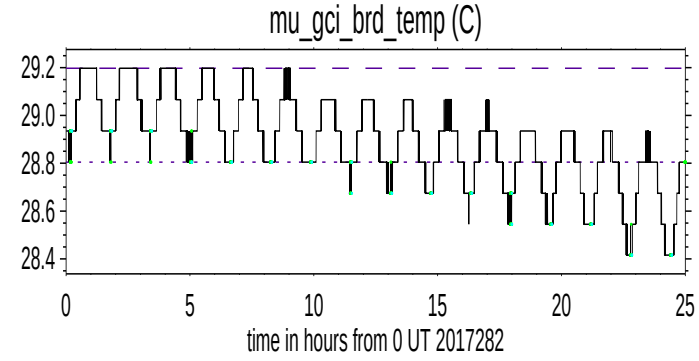

mu_15_conv_temp (C)
31.6
31.4
31.2
31.0
30.8
30.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

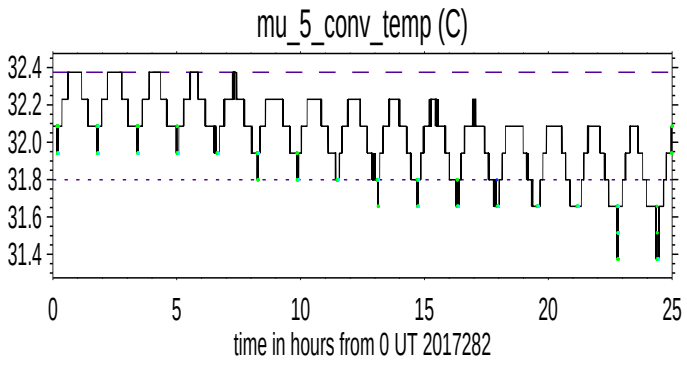

mu_1553_trans_temp (C)
29.4
29.2
29.0
28.8
28.6
0
5
10
15
20
25
time in hours from 0 UT 2017282

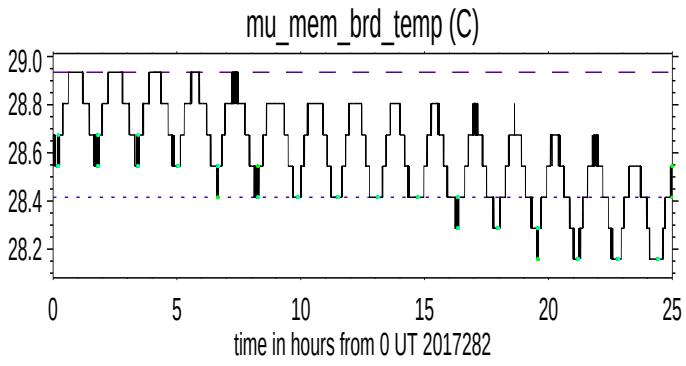

mu_cntl_fpga_temp (C)
30.2
30.0
29.8
29.6
29.4
29.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

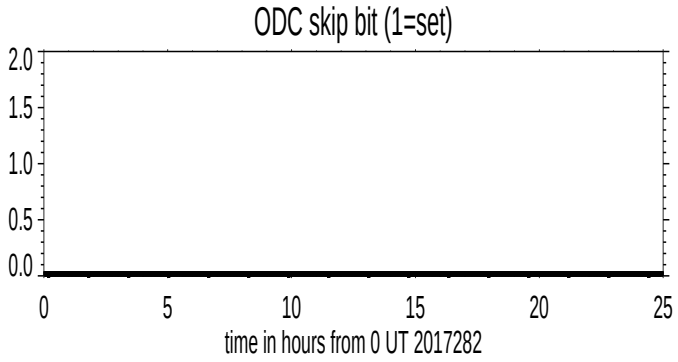

mu stats (DN)
tm_drop
fault_cnt
cmd_reject
cmd_pkt_rej
0
5
10
15
20
25
time in hours from 0 UT 2017282

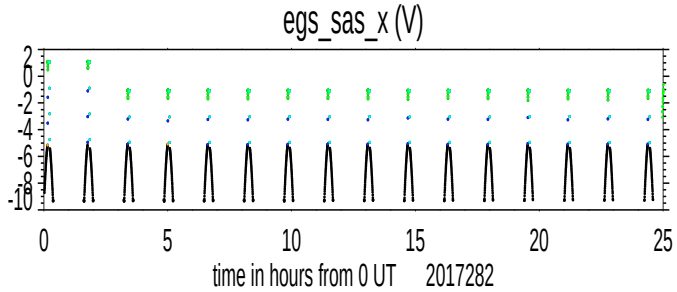

mu_cpu_temp (C)
29.8
29.6
29.4
29.2
29.0
28.8
0
5
10
15
20
25
time in hours from 0 UT 2017282

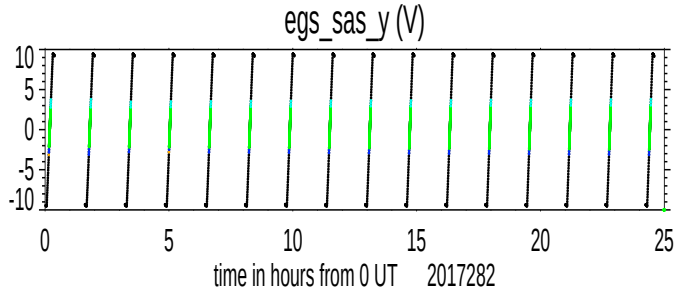

mu_gci_brd_temp (C)
29.2
29.0
28.8
28.6
28.4
0
5
10
15
20
25
time in hours from 0 UT 2017282

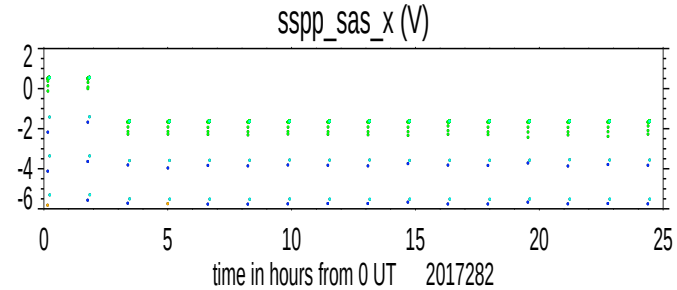

mu_5_conv_temp (C)
32.4
32.2
32.0
31.8
31.6
31.4
0
5
10
15
20
25
time in hours from 0 UT 2017282

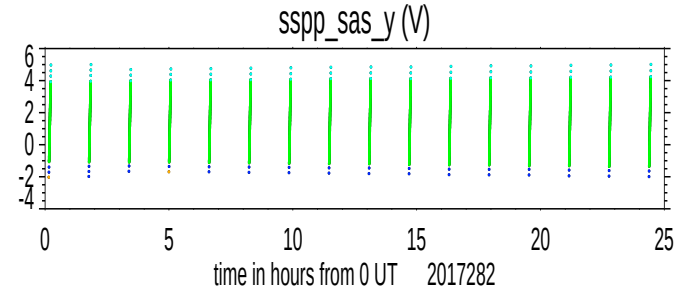

mu_mem_brd_temp (C)
29.0
28.8
28.6
28.4
28.2
0
5
10
15
20
25
time in hours from 0 UT 2017282

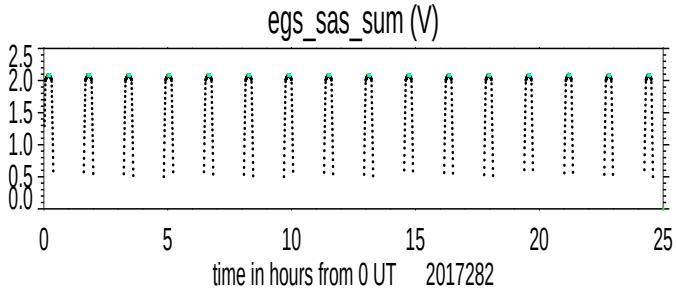

ODC skip bit (1=set)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017282

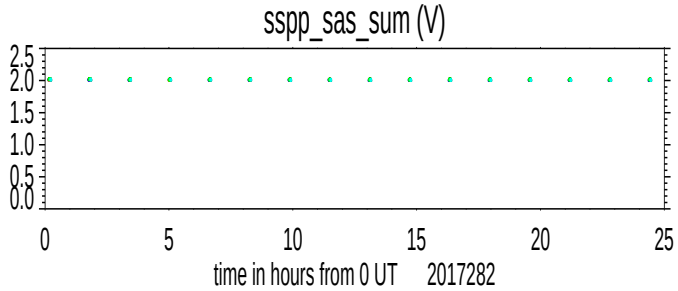

egs_sas_x (V)
time in hours from 0 UT 2017282

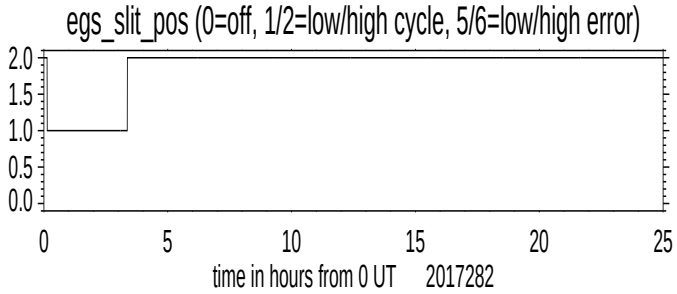

egs_sas_y (V)
time in hours from 0 UT 2017282

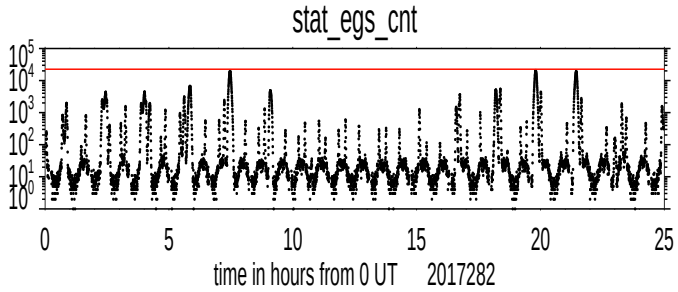

sspp_sas_x (V)
time in hours from 0 UT 2017282

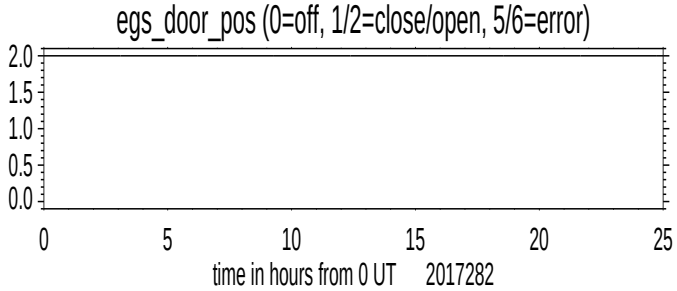

sspp_sas_y (V)
time in hours from 0 UT 2017282

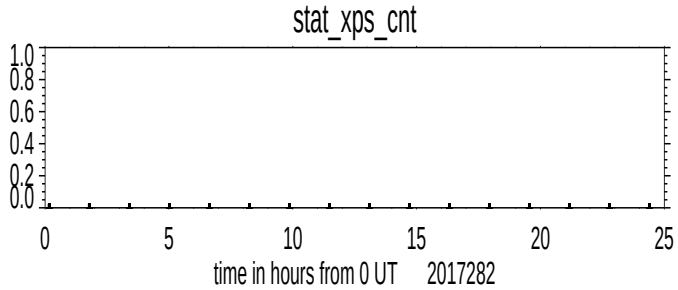

egs_sas_sum (V)
time in hours from 0 UT 2017282

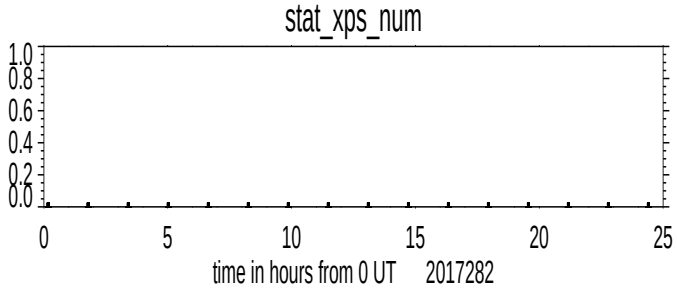

sspp_sas_sum (V)
time in hours from 0 UT 2017282

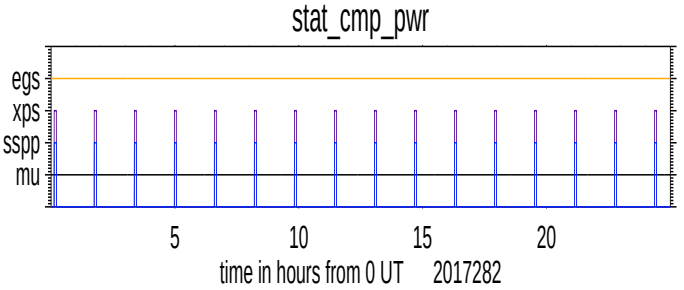

egs_slit_pos (0=off, 1/2=low/high cycle, 5/6=low/high error)
time in hours from 0 UT 2017282


stat_egs_cnt
time in hours from 0 UT 2017282

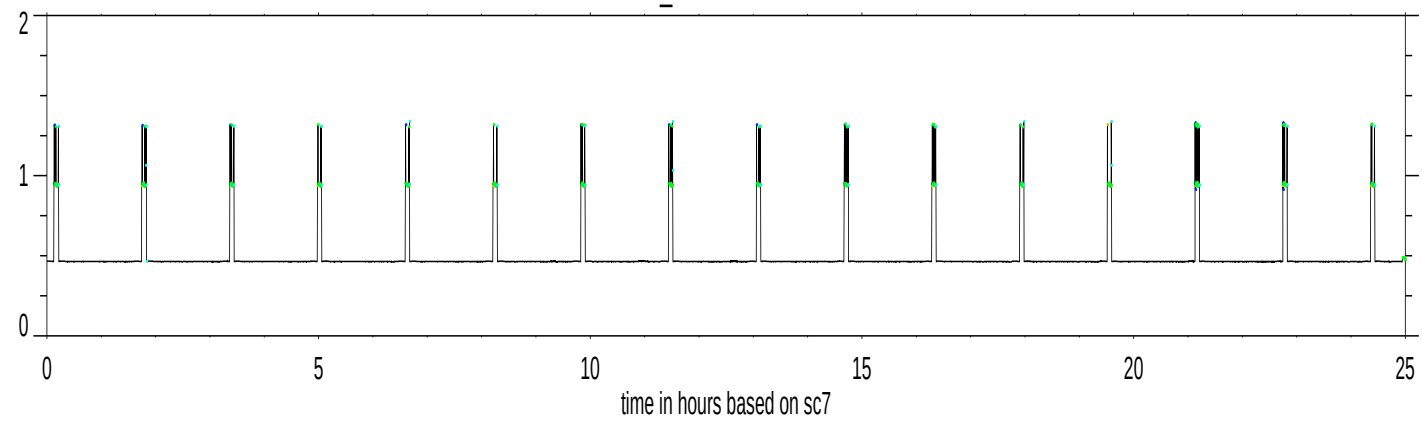

egs_door_pos (0=off, 1/2=close/open, 5/6=error)
time in hours from 0 UT 2017282


stat_xps_cnt
time in hours from 0 UT 2017282

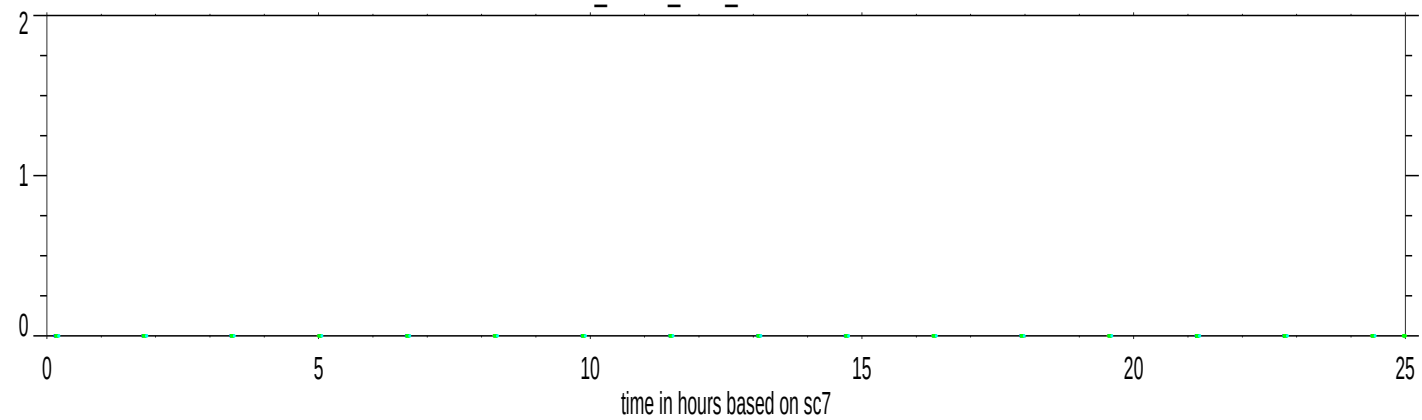

stat_xps_num
time in hours from 0 UT 2017282


stat_cmp_pwr
egs
xps
sspp
mu
time in hours from 0 UT 2017282

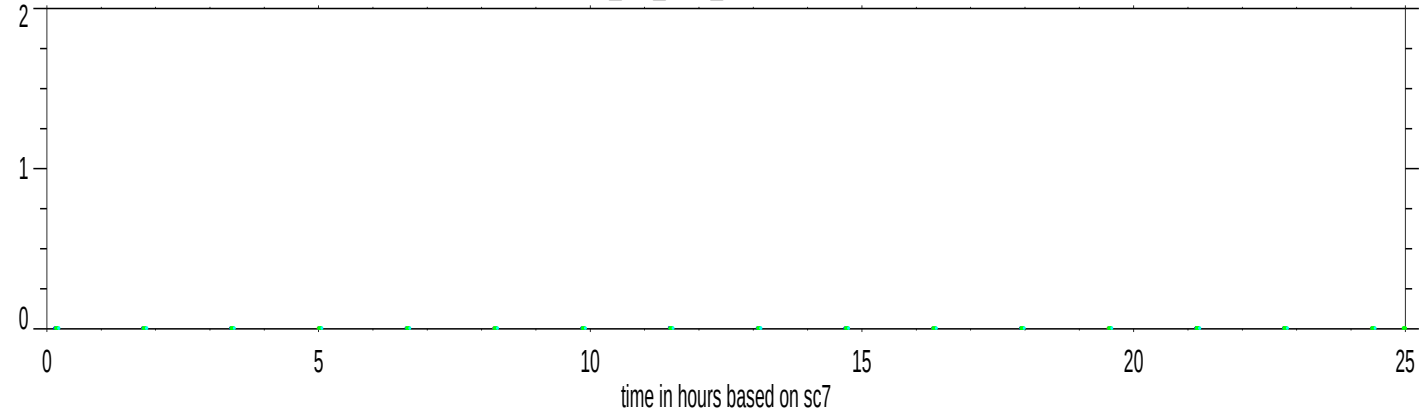
SEE_CUR 2017282



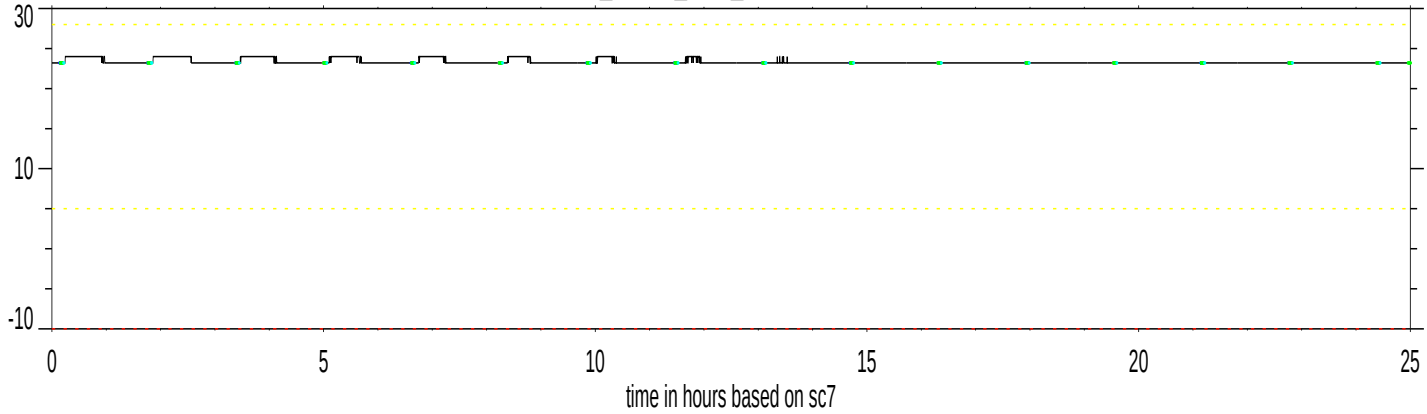
SEE_SURV_HTR_CUR 2017282

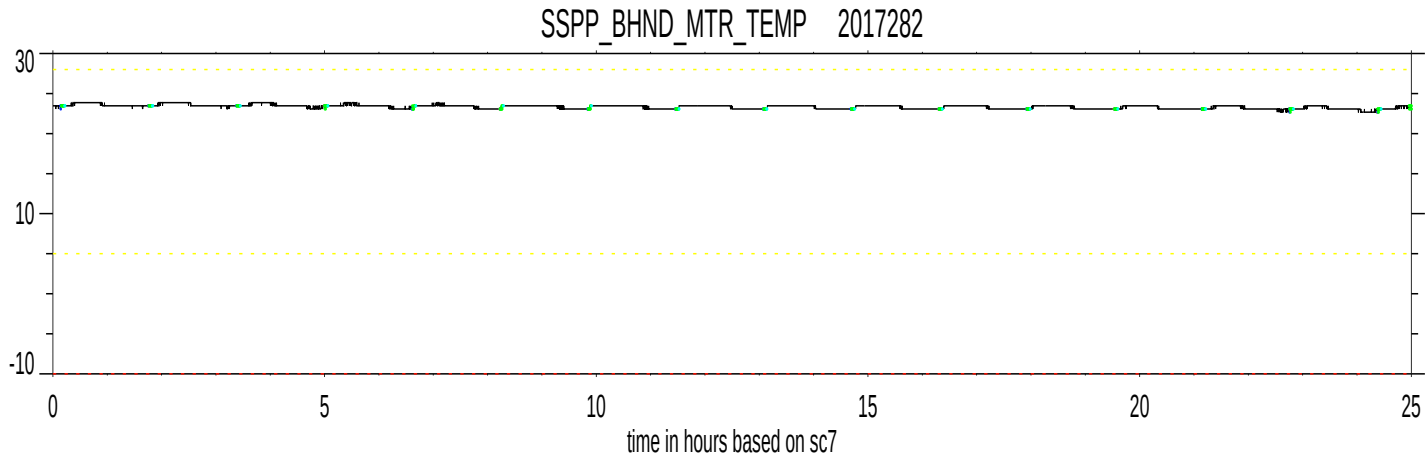


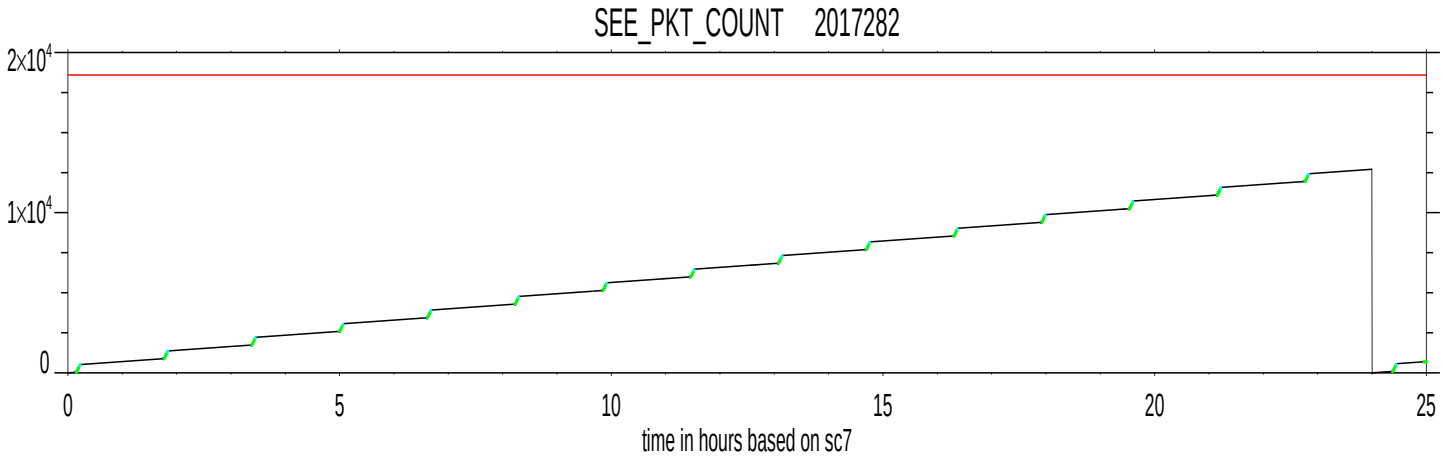
SEE_OP_HTR_CUR 2017282

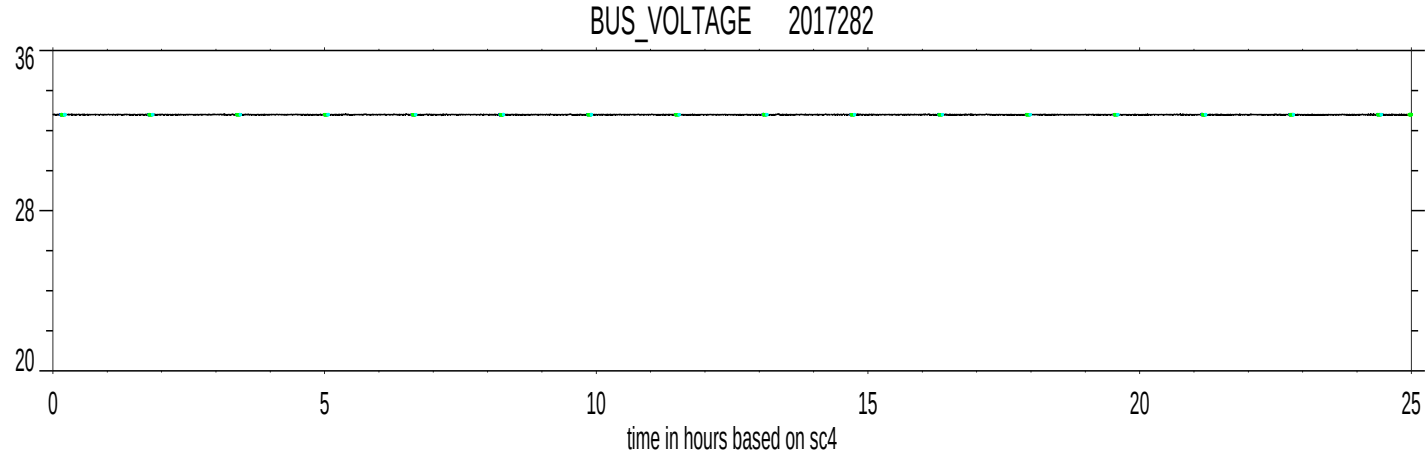


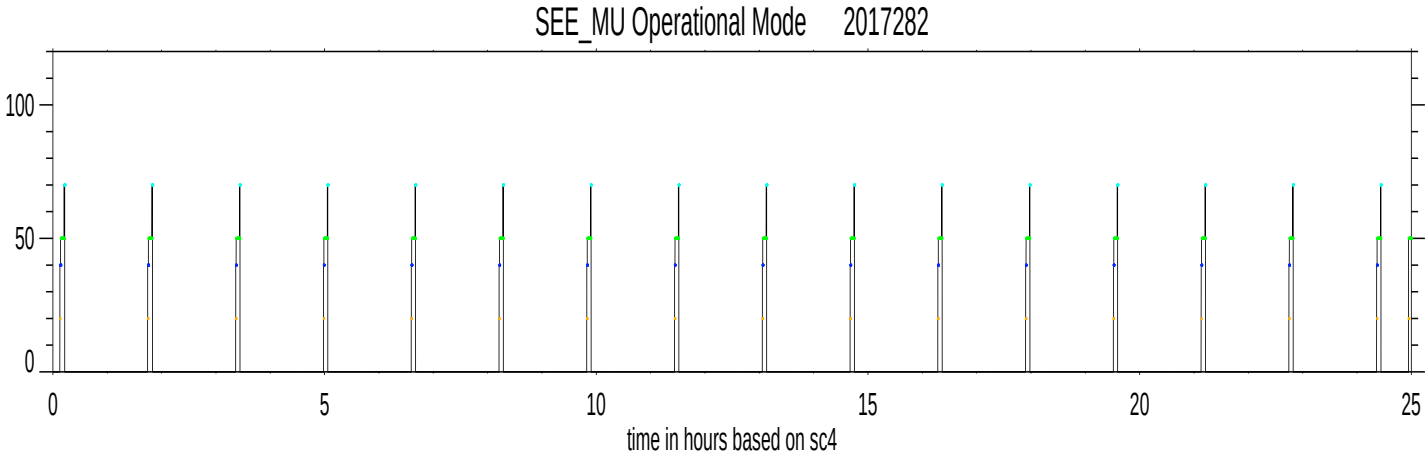
SSPP_BHND_XPS_TEMP 2017282

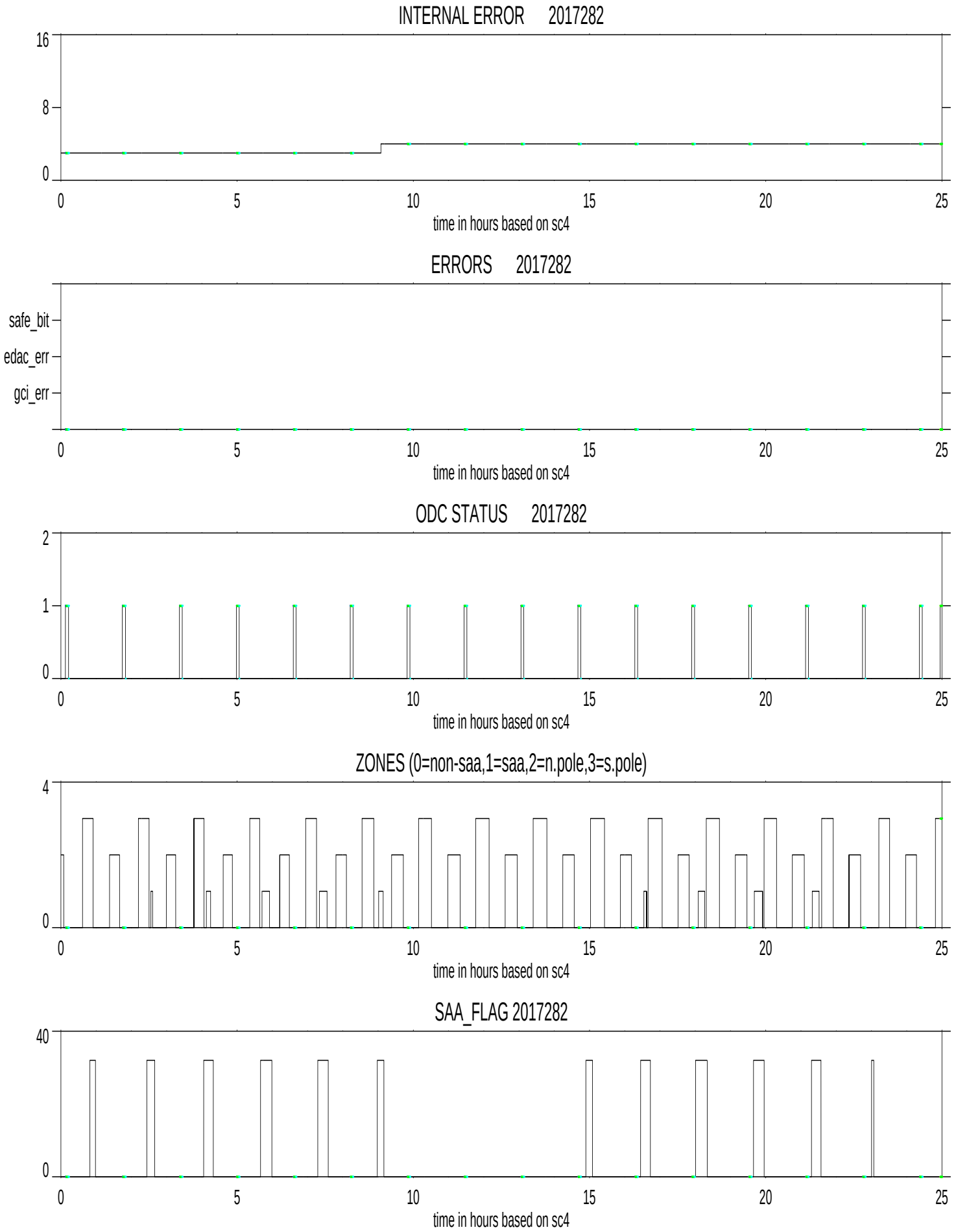


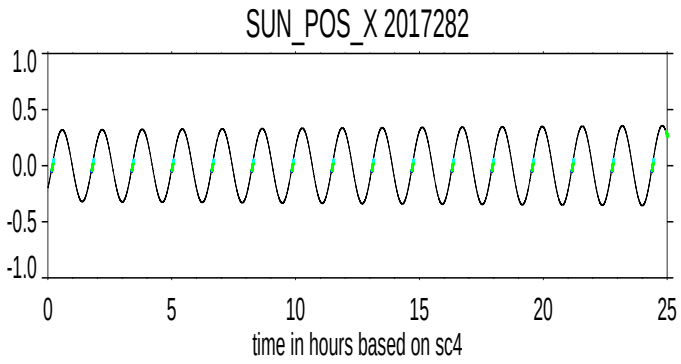

SSPP_BHND_MTR_TEMP 2017282
30
10
-10
0
5
10
15
20
25
time in hours based on sc7

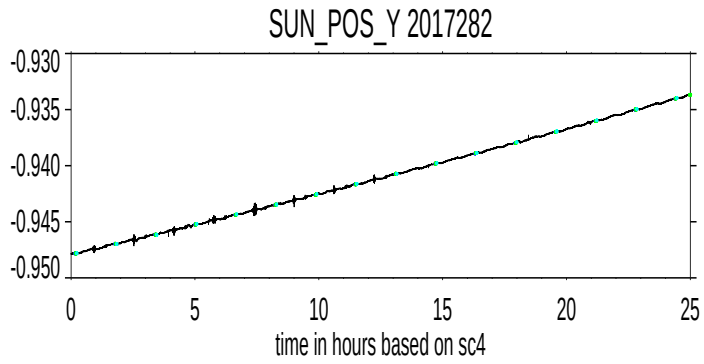

SEE_PKT_COUNT 2017282
2x10^4
1x10^4
0
0
5
10
15
20
25
time in hours based on sc7

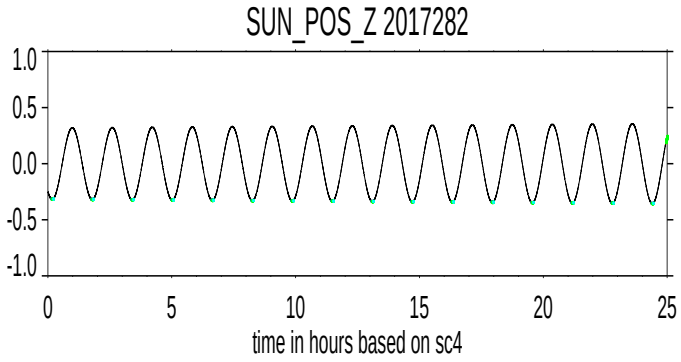

BUS_VOLTAGE 2017282
36
28
20
0
5
10
15
20
25
time in hours based on sc4

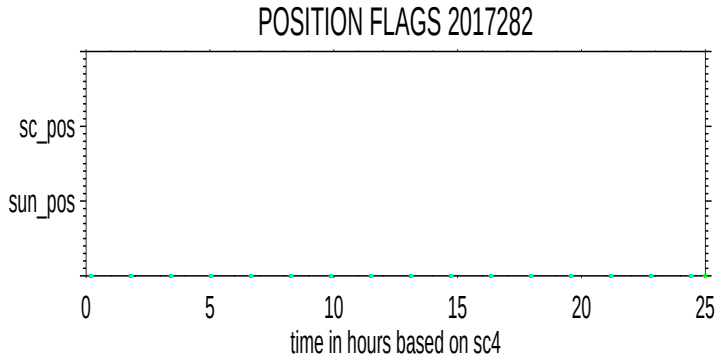

SEE_MU Operational Mode 2017282
100
50
0
0
5
10
15
20
25
time in hours based on sc4

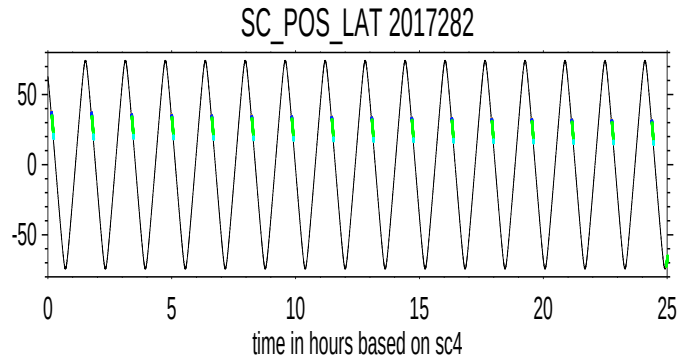

INTERNAL ERROR 2017282
16
8
0
0
5
10
15
20
25
time in hours based on sc4
ERRORS 2017282
safe_bit
edac_err
gci_err
0
5
10
15
20
25
time in hours based on sc4
ODC STATUS 2017282
2
1
0
0
5
10
15
20
25
time in hours based on sc4
ZONES (0=non-saa,1=saa,2=n.pole,3=s.pole)
4
0
0
5
10
15
20
25
time in hours based on sc4
SAA_FLAG 2017282
40
0
0
5
10
15
20
25
time in hours based on sc4

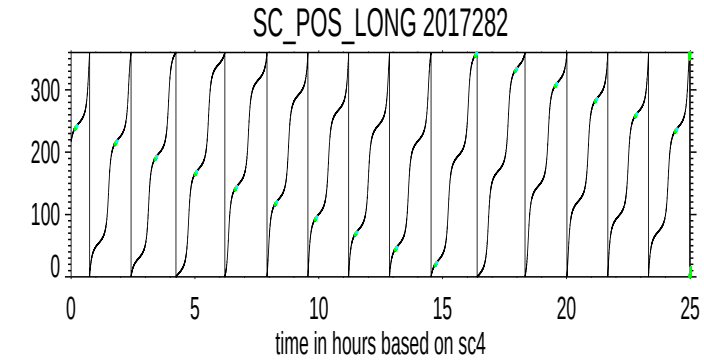

SUN_POS_X 2017282
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4

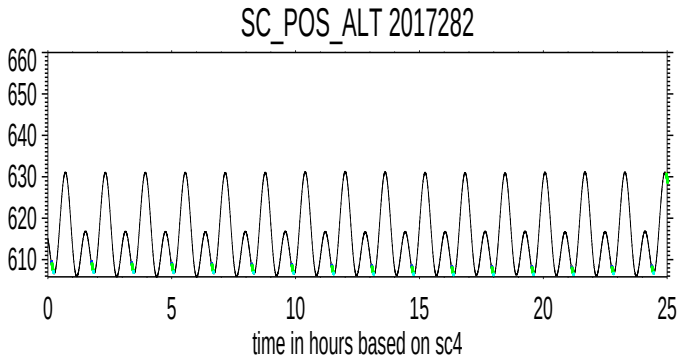

SUN_POS_Y 2017282
-0.930
-0.935
-0.940
-0.945
-0.950
0
5
10
15
20
25
time in hours based on sc4

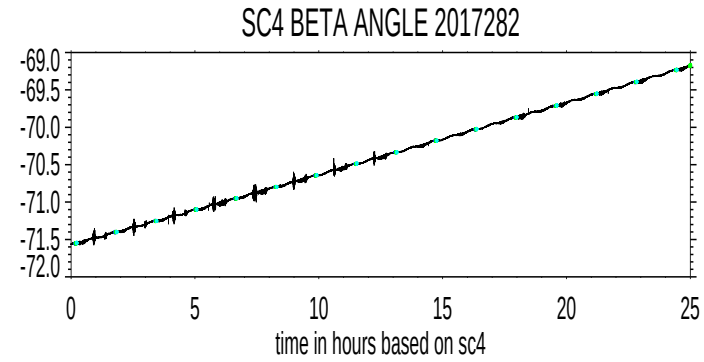

SUN_POS_Z 2017282
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4


POSITION FLAGS 2017282
sc_pos
sun_pos
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LAT 2017282
50
0
-50
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LONG 2017282
300
200
100
0
0
5
10
15
20
25
time in hours based on sc4


SC_POS_ALT 2017282
660
650
640
630
620
610
0
5
10
15
20
25
time in hours based on sc4


SC4 BETA ANGLE 2017282
-69.0
-69.5
-70.0
-70.5
-71.0
-71.5
-72.0
0
5
10
15
20
25
time in hours based on sc4