```
Information for      2017320
23:11:59.002: see_stat[1]: 5 internal error
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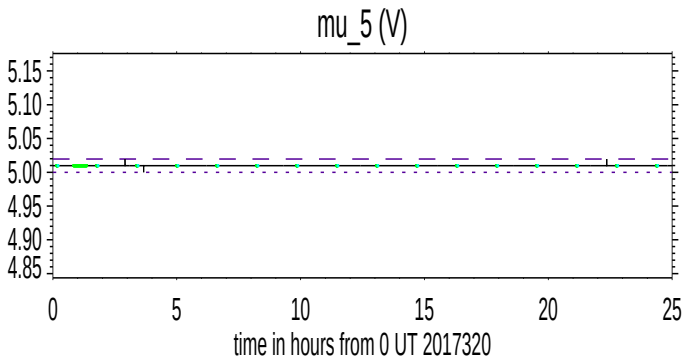
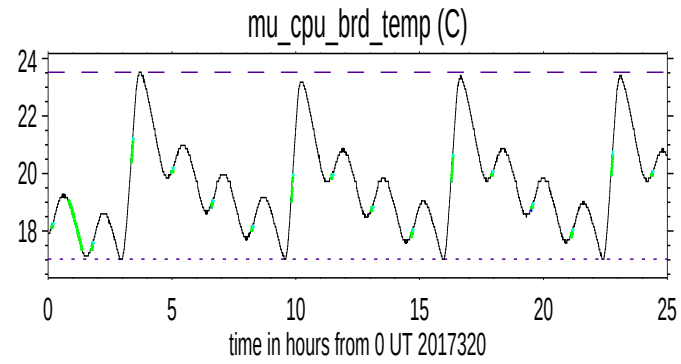

mu_5 (V)
5.15
5.10
5.05
5.00
4.95
4.90
4.85
0
5
10
15
20
25
time in hours from 0 UT 2017320


mu_cpu_brd_temp (C)
24
22
20
18
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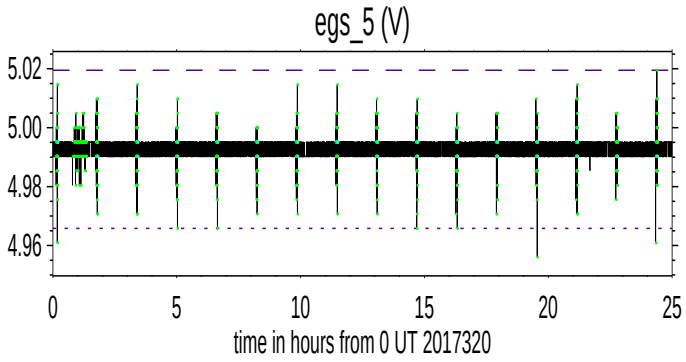
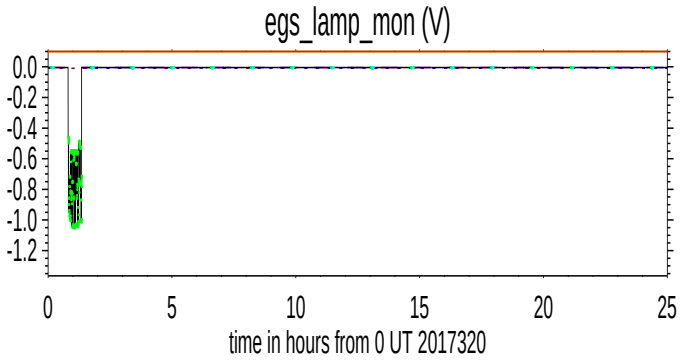
5
10
15
20
25
time in hours from 0 UT 2017320


egs_5 (V)
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017320


egs_lamp_mon (V)
0.0
-0.2
-0.4
-0.6
-0.8
-1.0
-1.2
0
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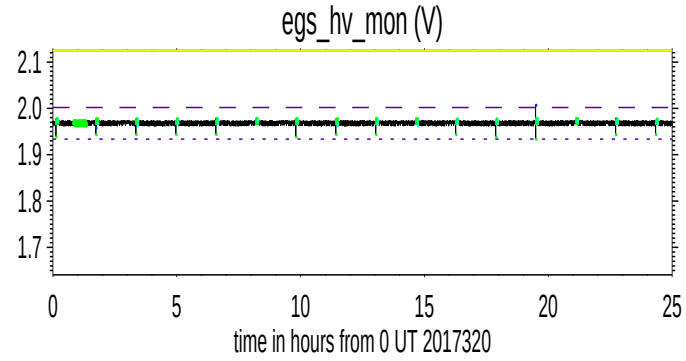
10
15
20
25
time in hours from 0 UT 2017320


egs_hv_mon (V)
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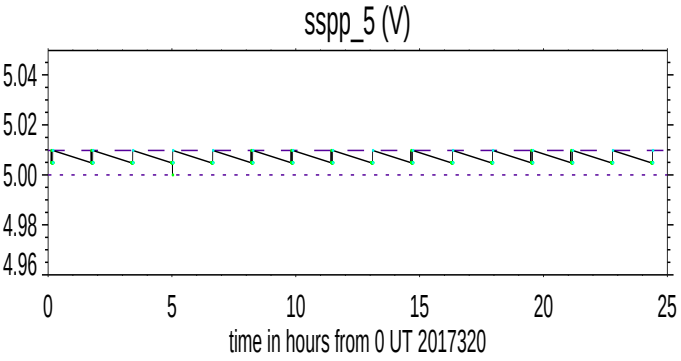
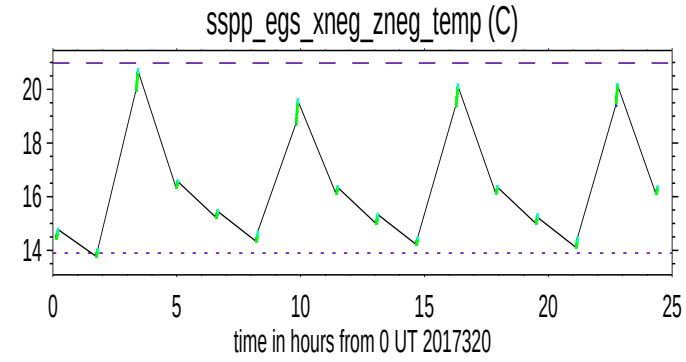
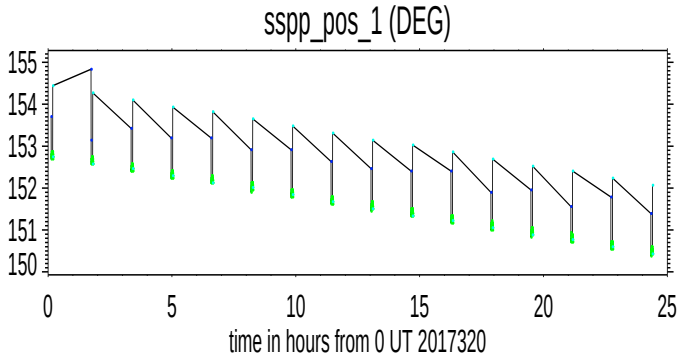
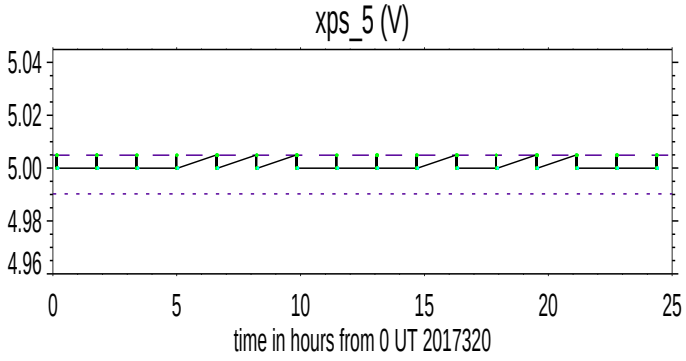
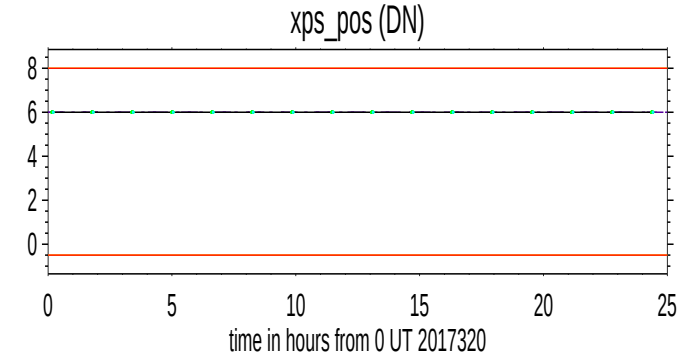
2.0
1.9
1.8
1.7
0
5
10
15
20
25
time in hours from 0 UT 2017320

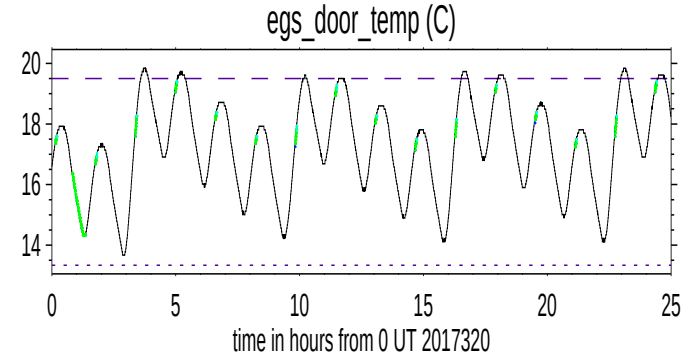

sspp_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017320


sspp_egs_xneg_zneg_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320


sspp_pos_1 (DEG)
155
154
153
152
151
150
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_pos (DN)
8
6
4
2
0
0
5
10
15
20
25
time in hours from 0 UT 2017320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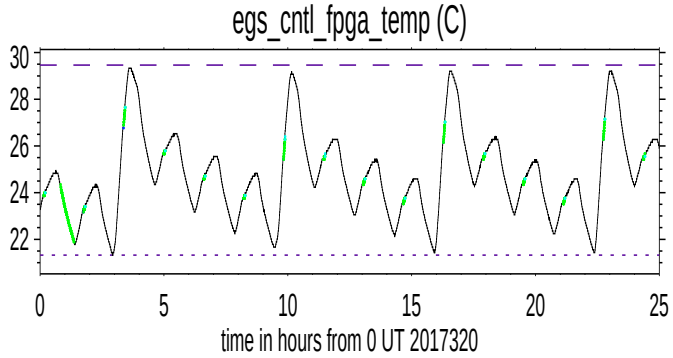
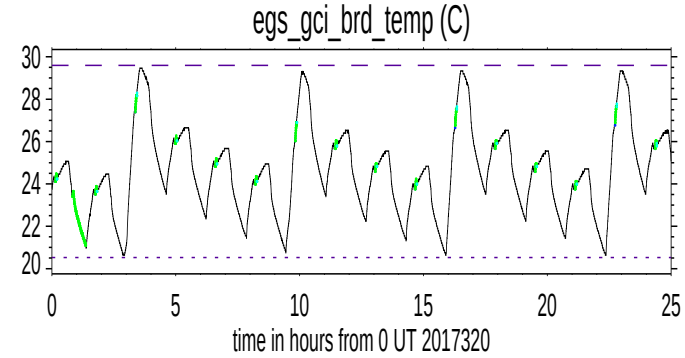
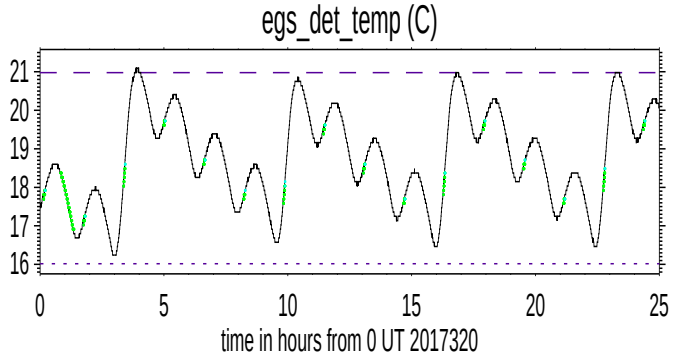
time in hours from 0 UT 2017320


egs_cntl_fpga_temp (C)
time in hours from 0 UT 2017320


egs_gci_brd_temp (C)
time in hours from 0 UT 2017320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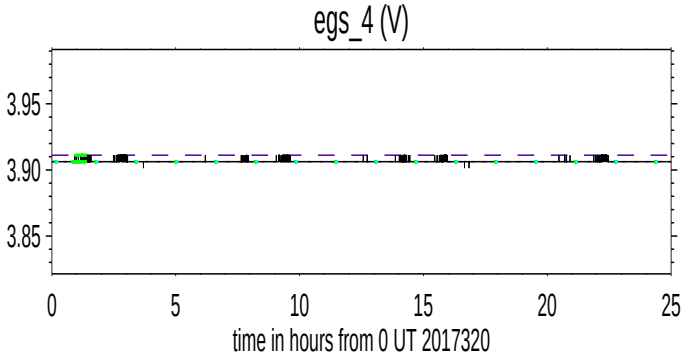
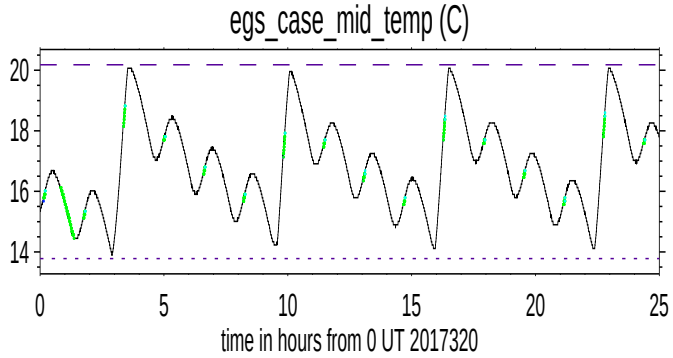
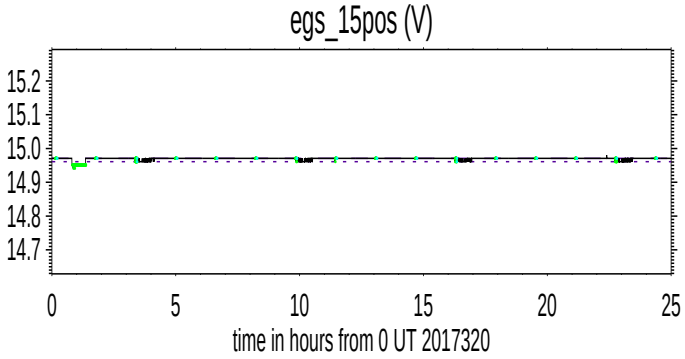
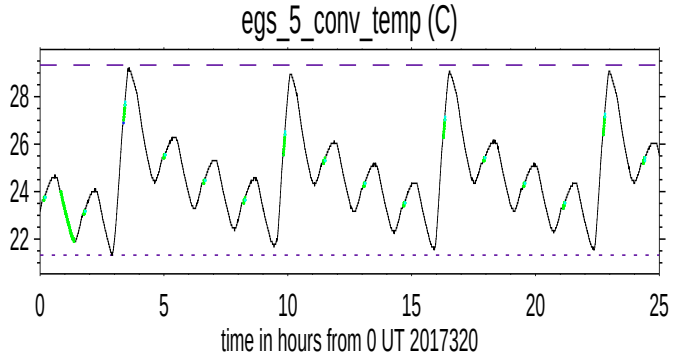
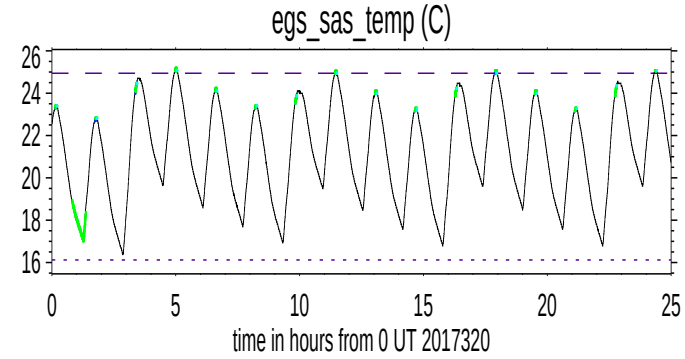
time in hours from 0 UT 2017320


egs_4 (V)
time in hours from 0 UT 2017320


egs_case_mid_temp (C)
time in hours from 0 UT 2017320


egs_15pos (V)
time in hours from 0 UT 2017320


egs_5_conv_temp (C)
time in hours from 0 UT 2017320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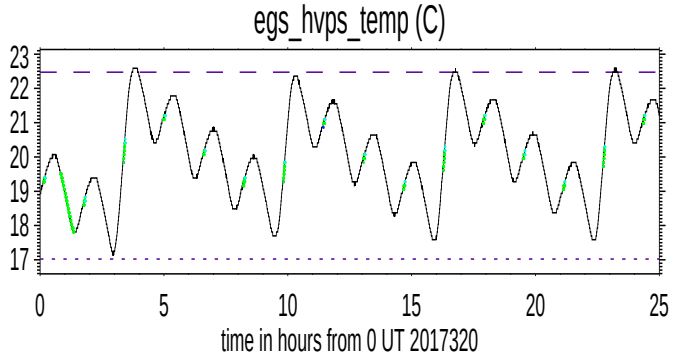
time in hours from 0 UT 2017320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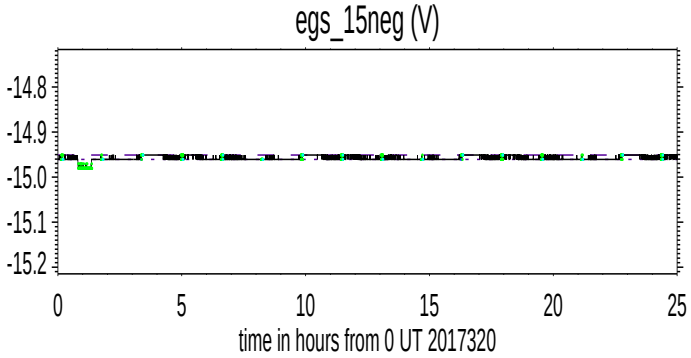
time in hours from 0 UT 2017320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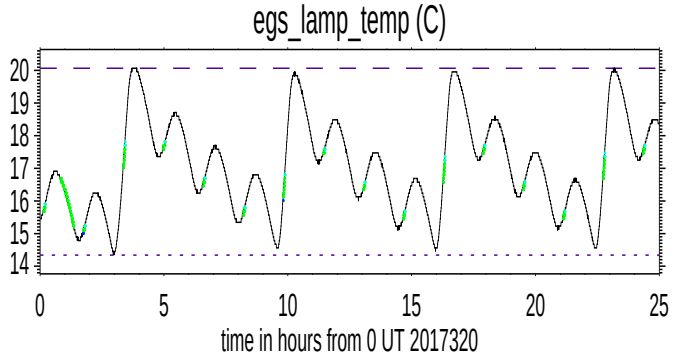
-14.8
-14.9
-15.0
-15.1
-15.2
0 5 10 15 20 25
time in hours from 0 UT 2017320


egs_lamp_temp (C)
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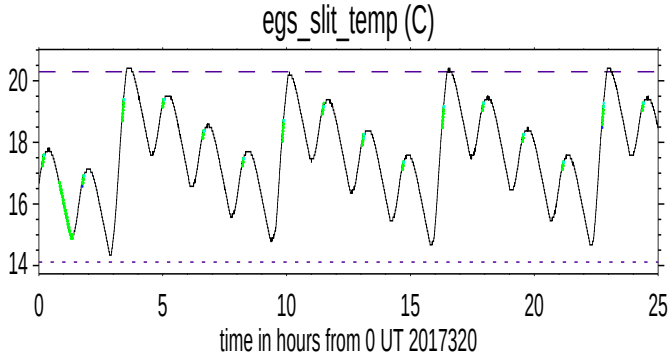
0 5 10 15 20 25
time in hours from 0 UT 2017320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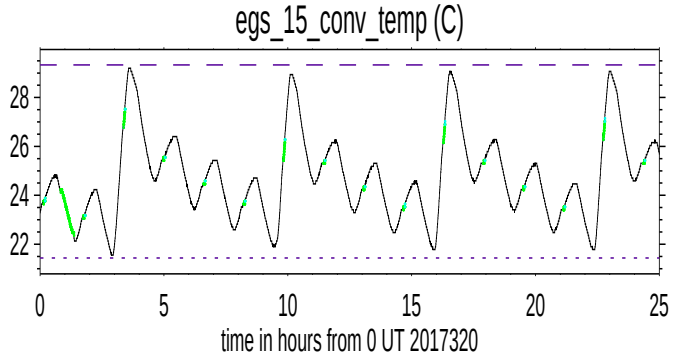
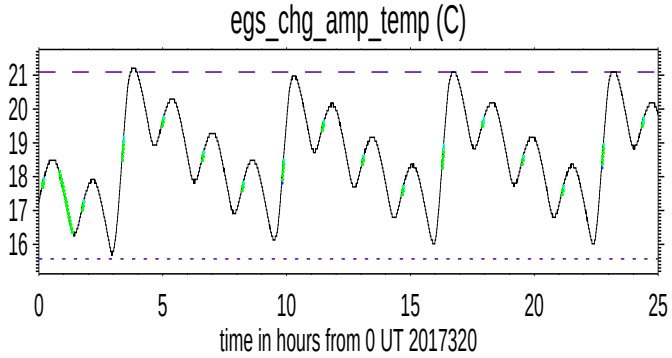
0 5 10 15 20 25
time in hours from 0 UT 2017320


egs_15_conv_temp (C)
28 26 24 22
0 5 10 15 20 25
time in hours from 0 UT 2017320


egs_chg_amp_temp (C)
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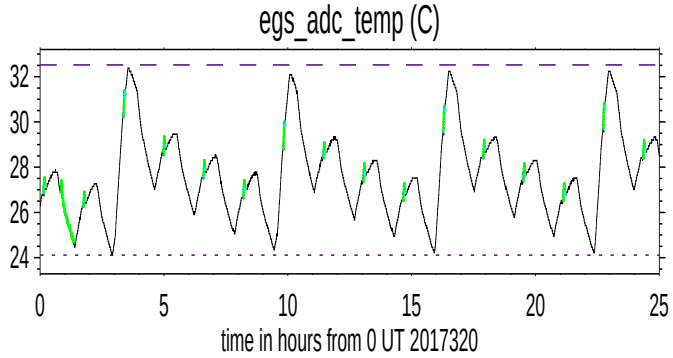
0 5 10 15 20 25
time in hours from 0 UT 2017320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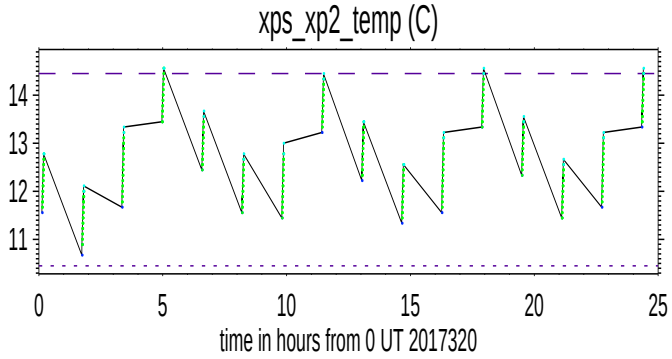
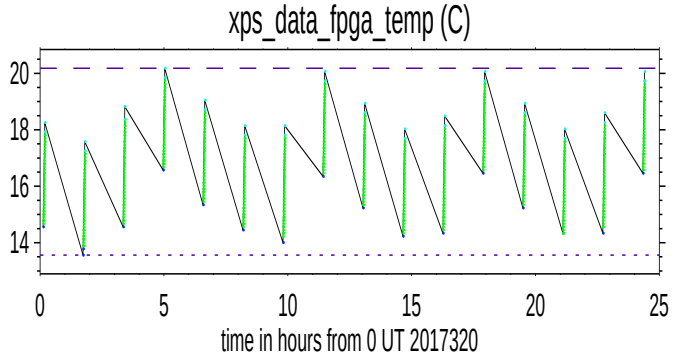
0 5 10 15 20 25
time in hours from 0 UT 2017320


xps_xp2_temp (C)
14 13 12 11
0 5 10 15 20 25
time in hours from 0 UT 2017320


xps_data_fpga_temp (C)
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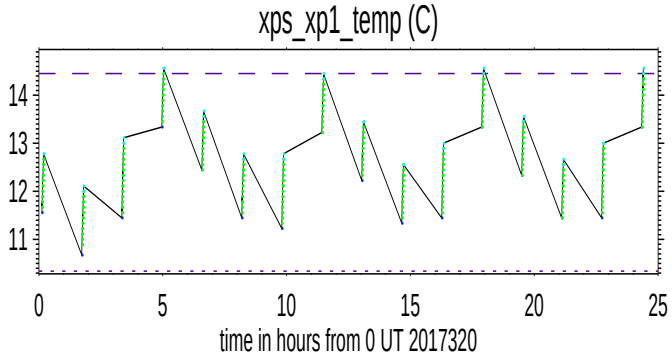
0 5 10 15 20 25
time in hours from 0 UT 2017320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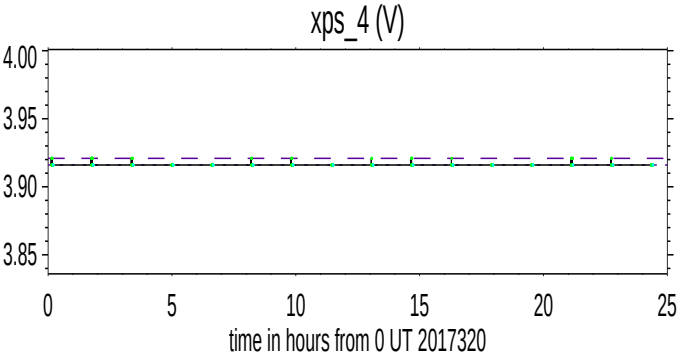
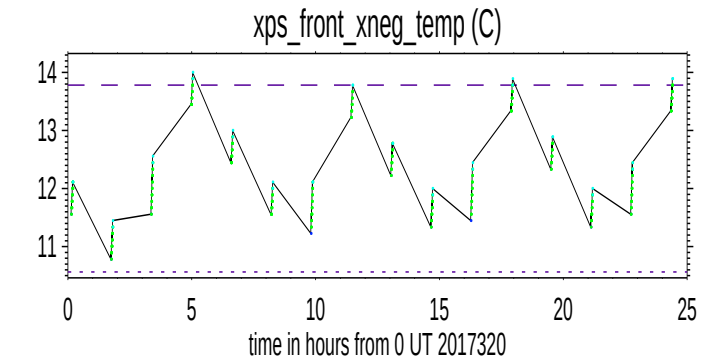
0 5 10 15 20 25
time in hours from 0 UT 2017320


xps_4 (V)
4.00 3.95 3.90 3.85
0 5 10 15 20 25
time in hours from 0 UT 2017320


xps_front_xneg_temp (C)
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13
12
11
0
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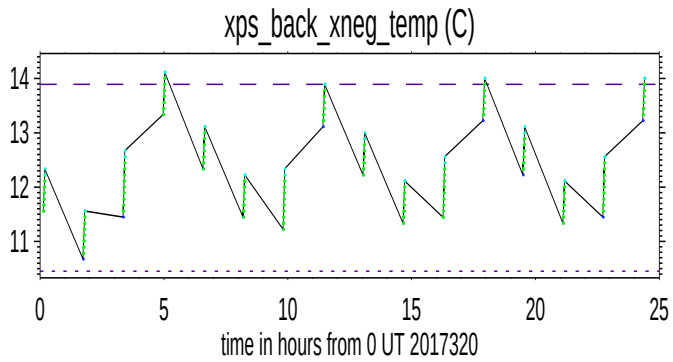
10
15
20
25
time in hours from 0 UT 2017320


xps_back_xneg_temp (C)
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12
11
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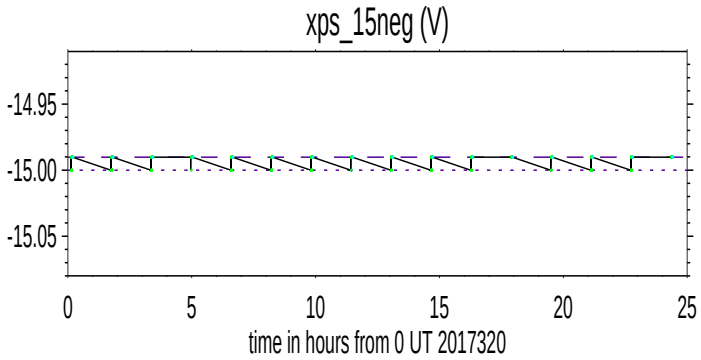
5
10
15
20
25
time in hours from 0 UT 2017320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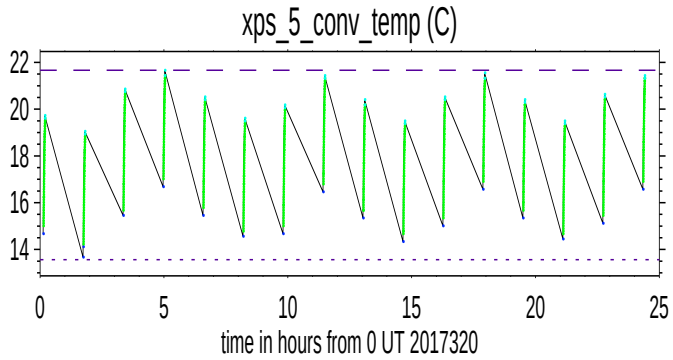
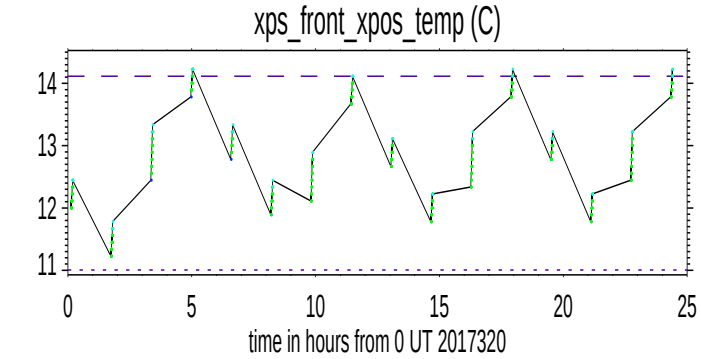
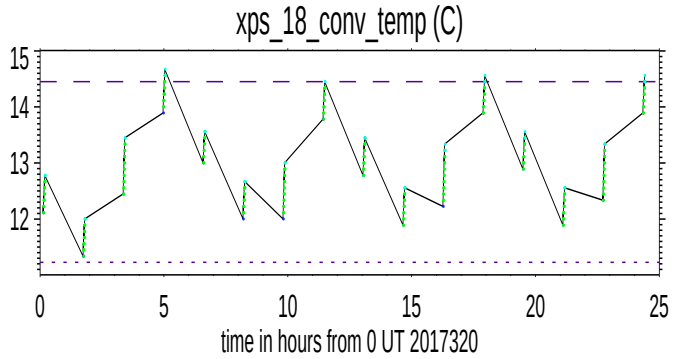
-15.00
-15.05
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_5_conv_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_front_xpos_temp (C)
14
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2017320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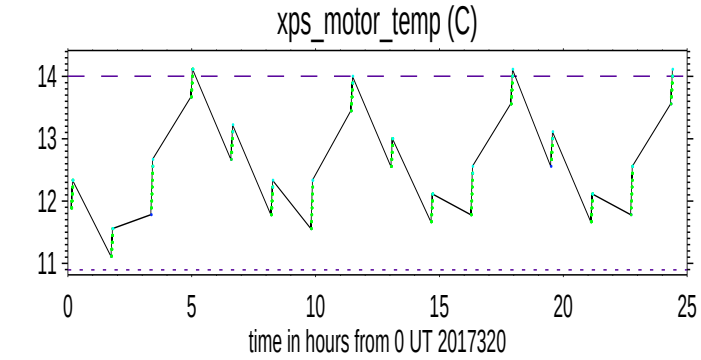
15
14
13
12
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_motor_temp (C)
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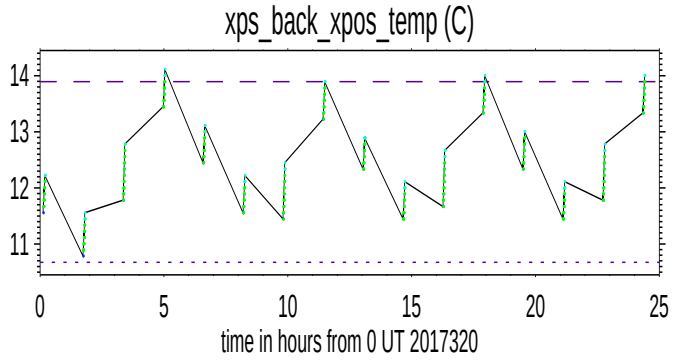
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_back_xpos_temp (C)
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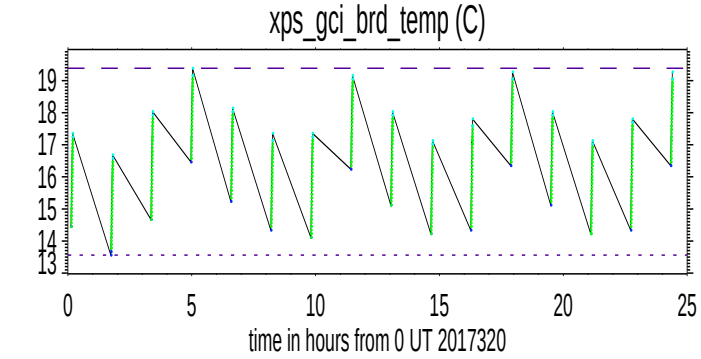
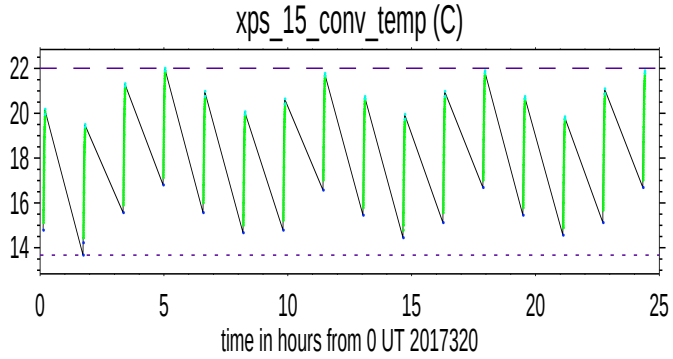
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_gci_brd_temp (C)
19
18
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320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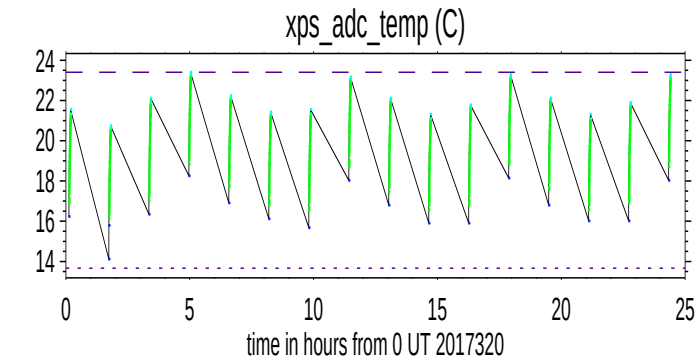
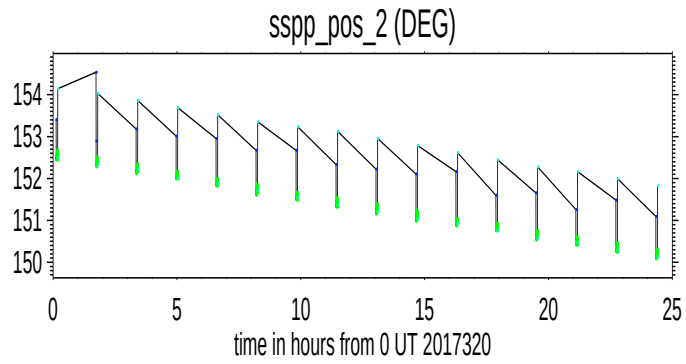
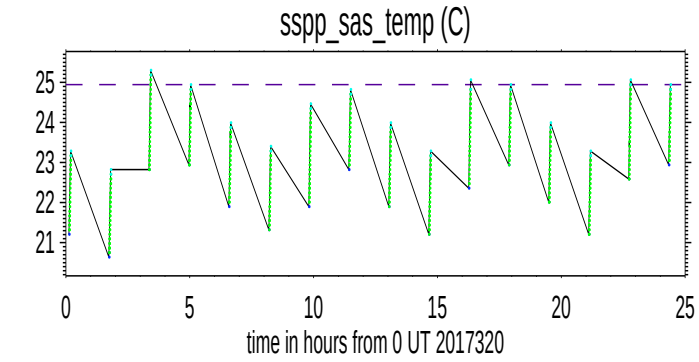
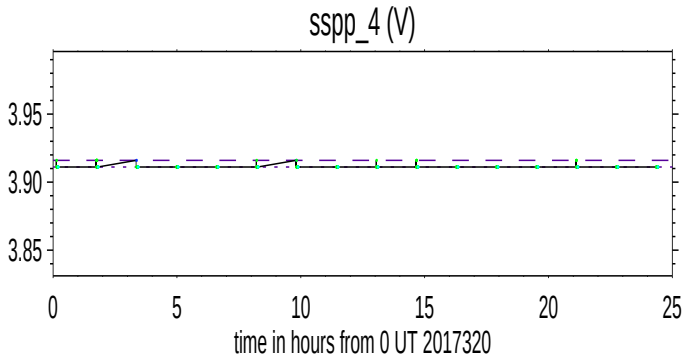
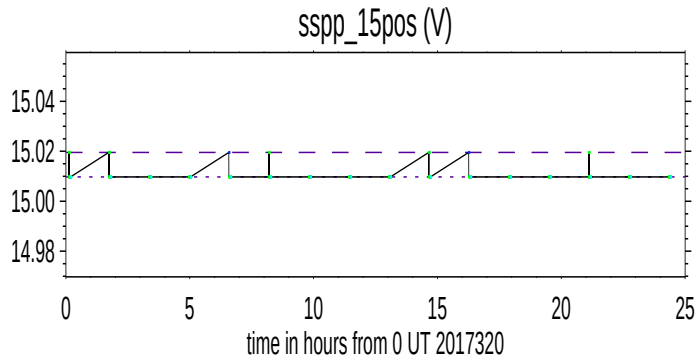
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320


xps_adc_temp (C)
24
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320


sspp_pos_2 (DEG)
154
153
152
151
150
0
5
10
15
20
25
time in hours from 0 UT 2017320


sspp_sas_temp (C)
25
24
23
22
21
0
5
10
15
20
25
time in hours from 0 UT 2017320


sspp_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017320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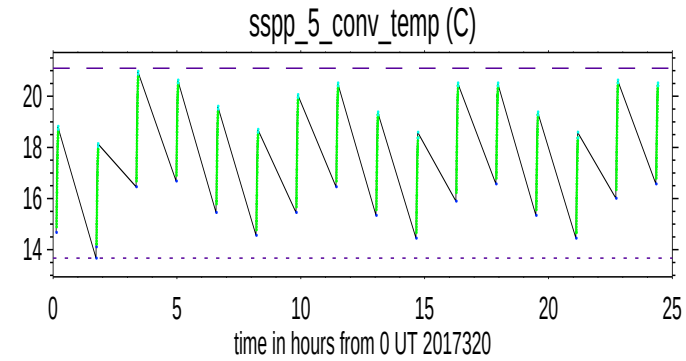
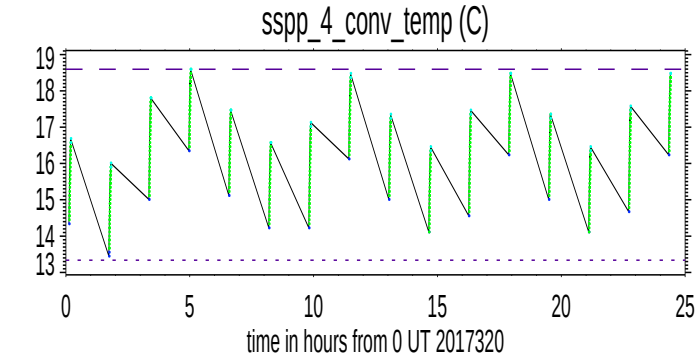
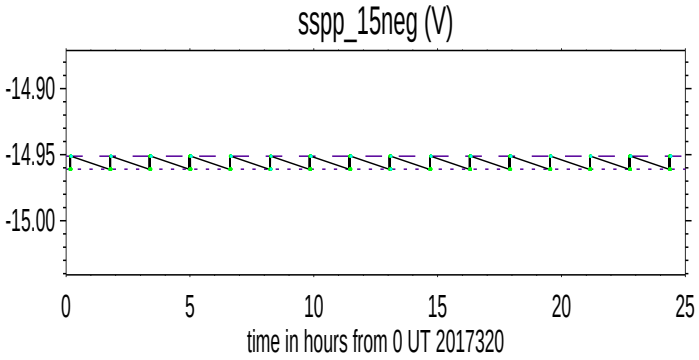
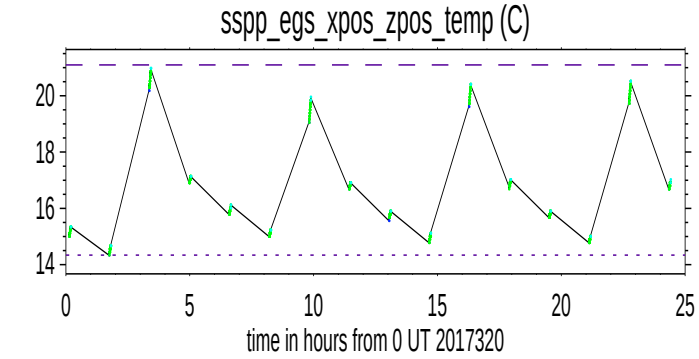
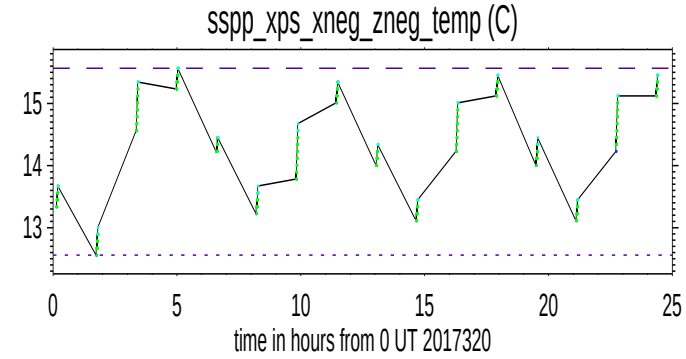
15.04
15.02
15.00
14.98
0
5
10
15
20
25
time in hours from 0 UT 2017320

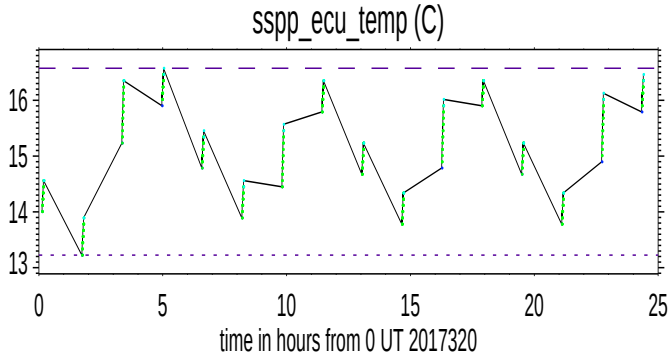

sspp_5_conv_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

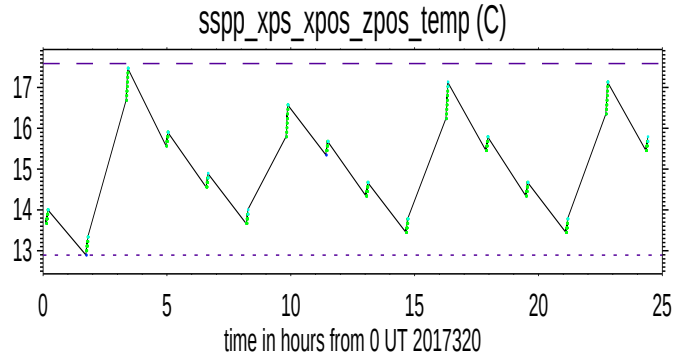

sspp_4_conv_temp (C)
19
18
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320

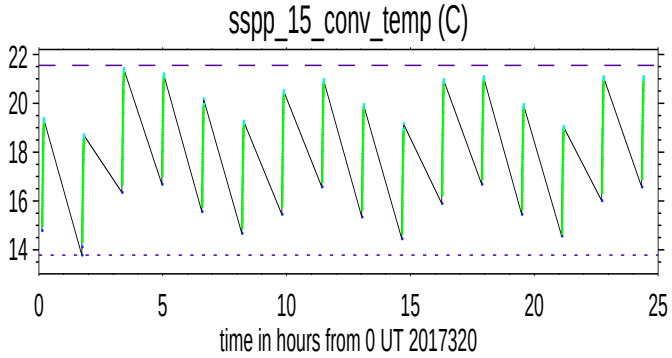

sspp_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2017320

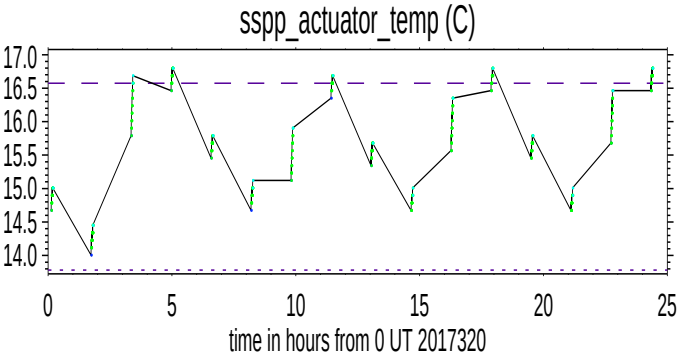

sspp_egs_xpos_zpos_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

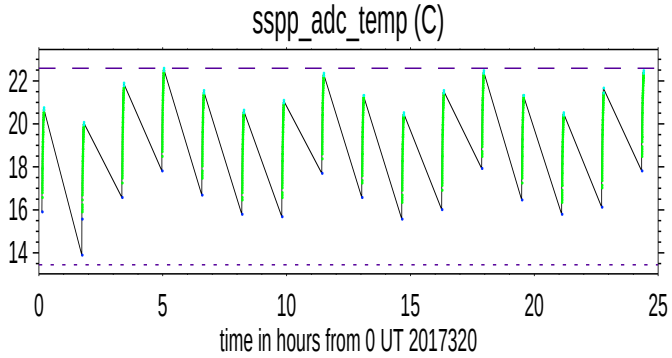

sspp_xps_xneg_zneg_temp (C)
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320

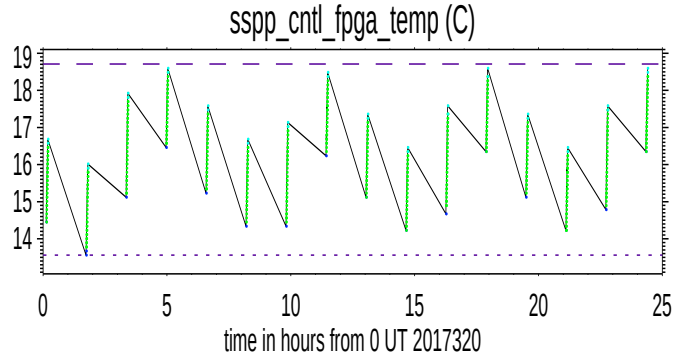

sspp_ecu_temp (C)
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320

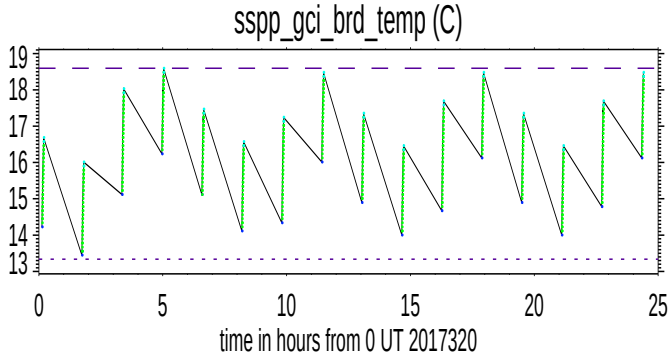

sspp_xps_xpos_zpos_temp (C)
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320

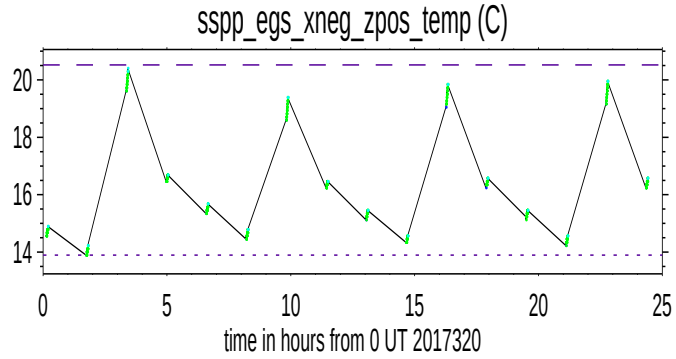

sspp_15_conv_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

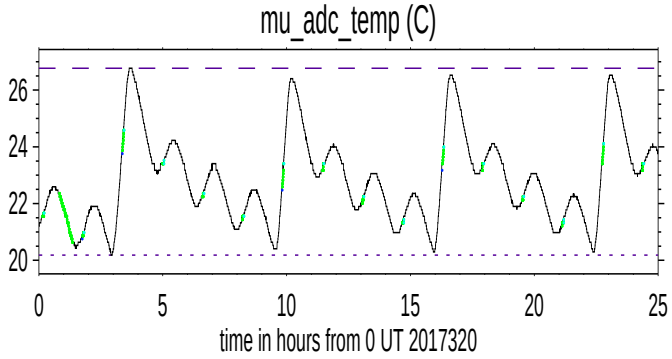

sspp_actuator_temp (C)
17.0
16.5
16.0
15.5
15.0
14.5
14.0
0
5
10
15
20
25
time in hours from 0 UT 2017320

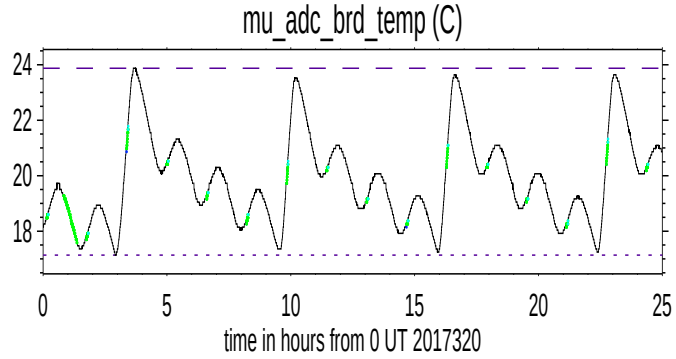

sspp_adc_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

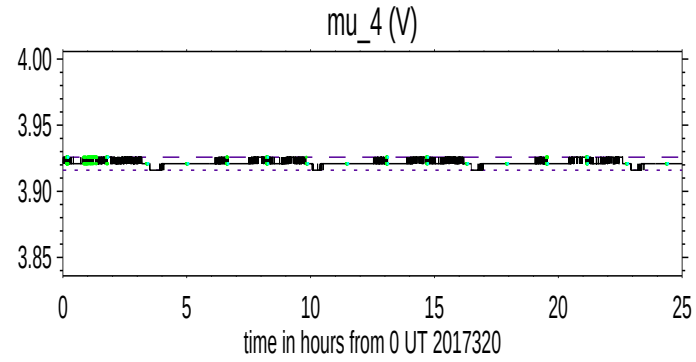

sspp_cntl_fpga_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

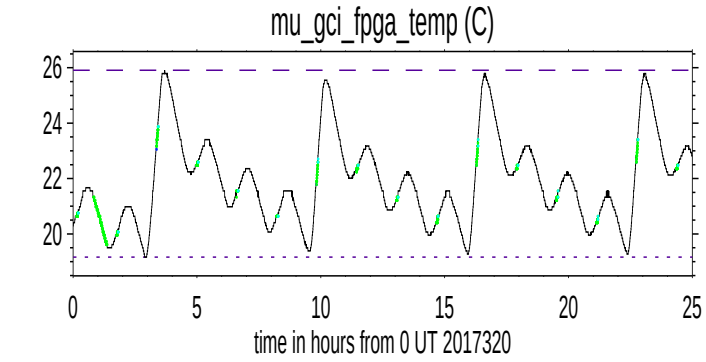

sspp_gci_brd_temp (C)
19
18
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2017320

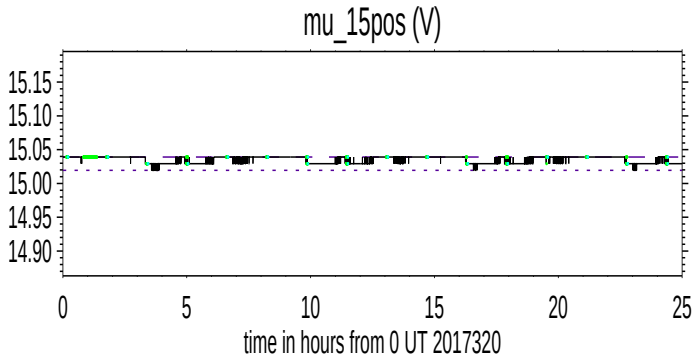

sspp_egs_xneg_zpos_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2017320

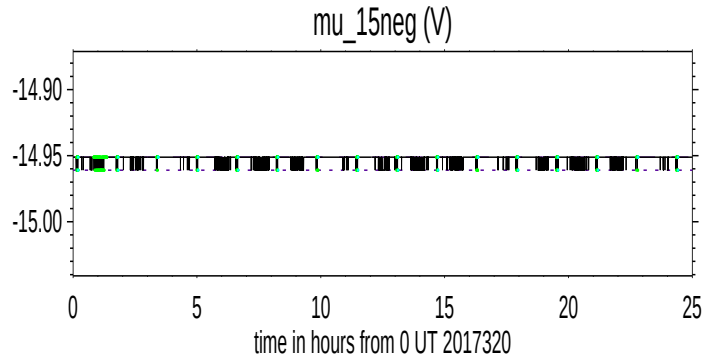

mu_adc_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017320

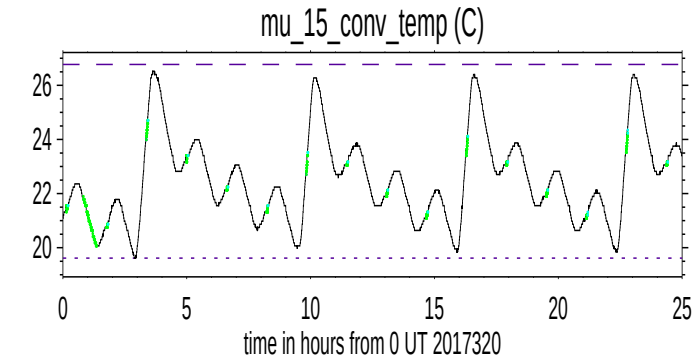

mu_adc_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

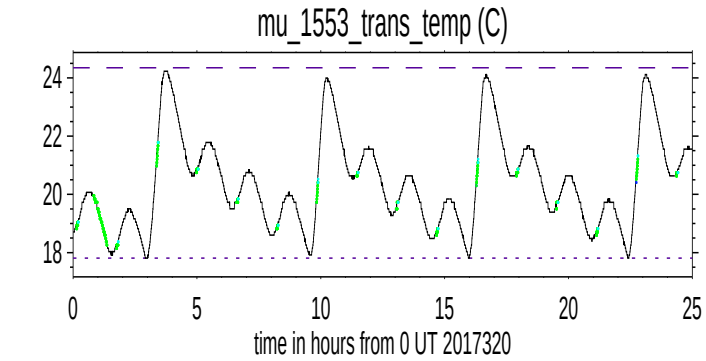

mu_4 (V)
4.00
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2017320

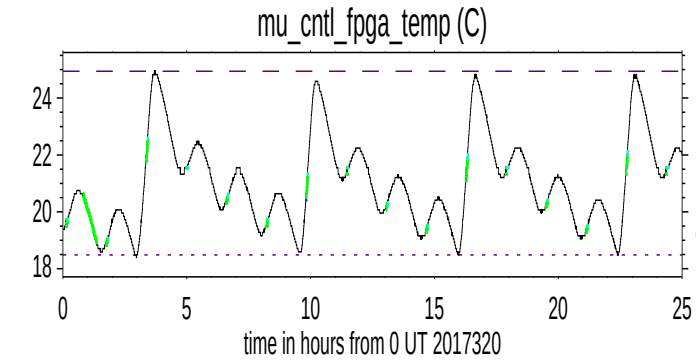

mu_gci_fpga_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017320

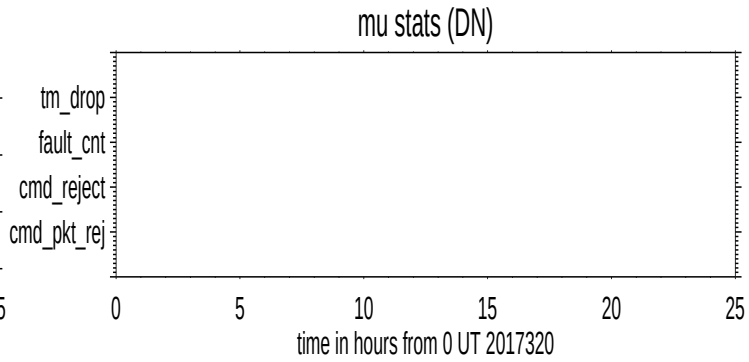

mu_15pos (V)
15.15
15.10
15.05
15.00
14.95
14.90
0
5
10
15
20
25
time in hours from 0 UT 2017320

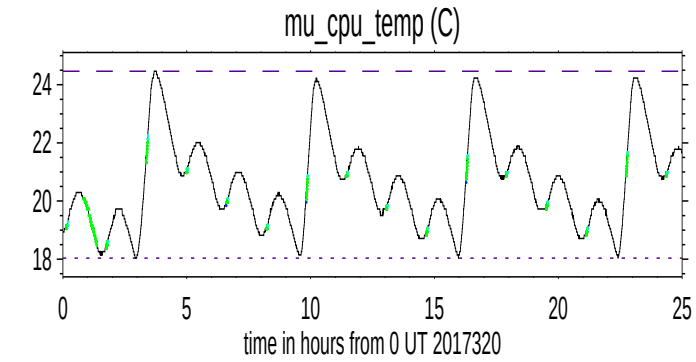

mu_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2017320

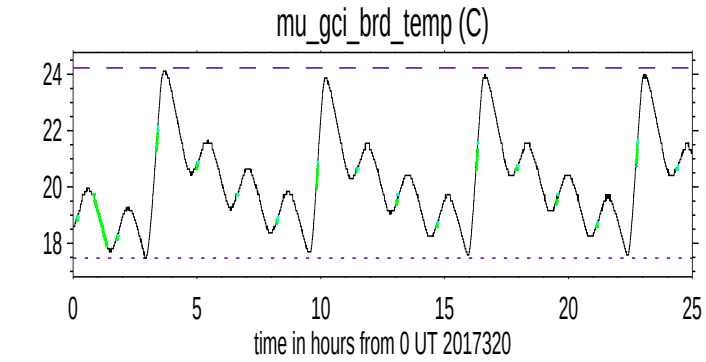

mu_15_conv_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017320

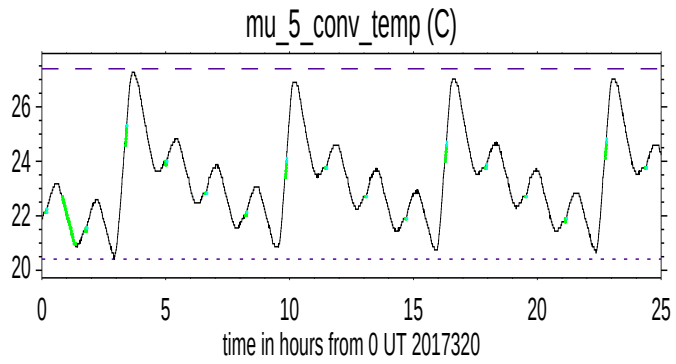

mu_1553_trans_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

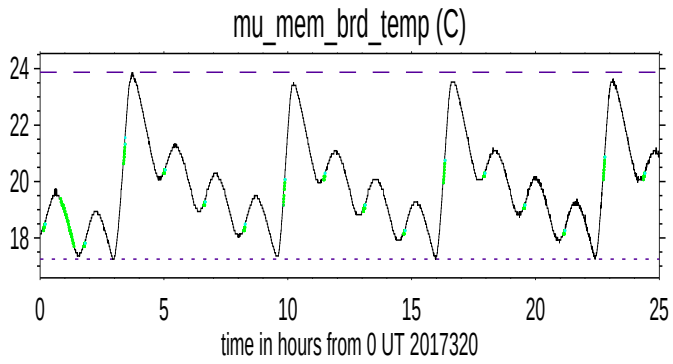

mu_cntl_fpga_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

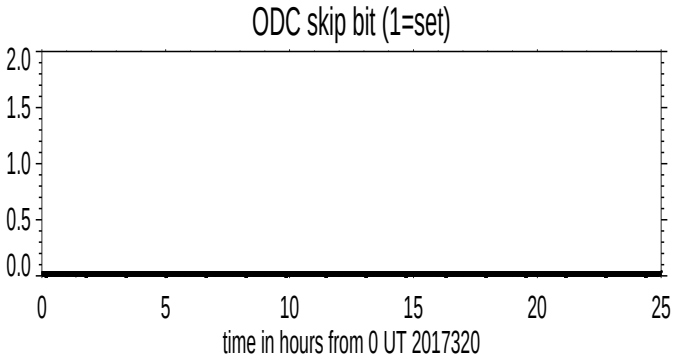

mu stats (DN)
tm_drop
fault_cnt
cmd_reject
cmd_pkt_rej
0
5
10
15
20
25
time in hours from 0 UT 2017320

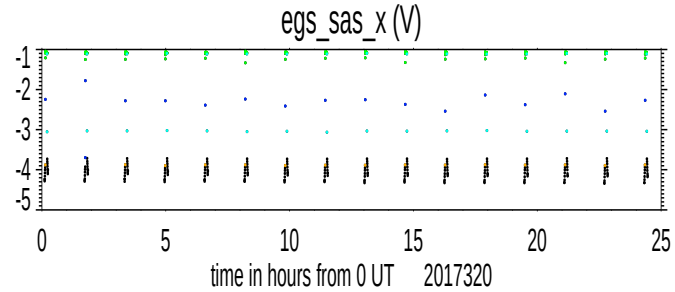

mu_cpu_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

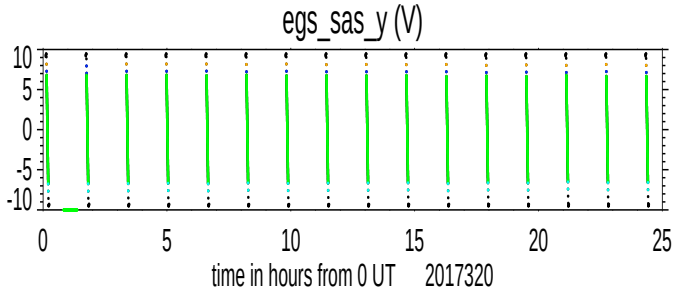

mu_gci_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

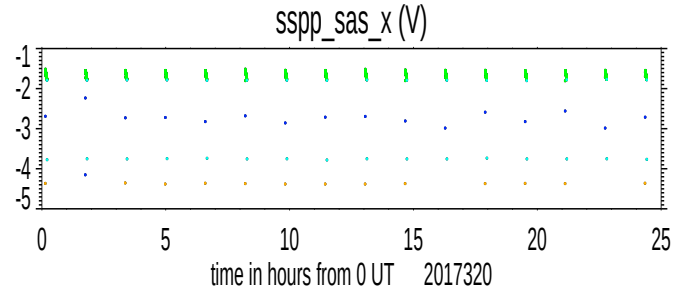

mu_5_conv_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2017320

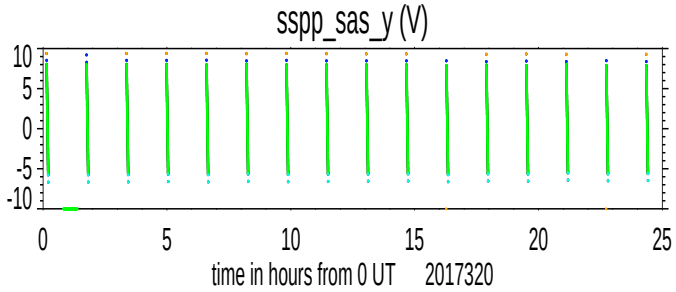

mu_mem_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2017320

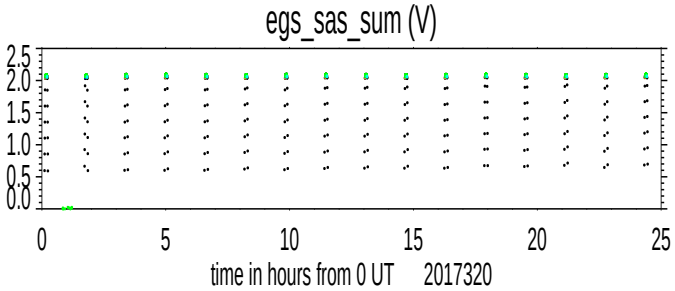

ODC skip bit (1=set)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320

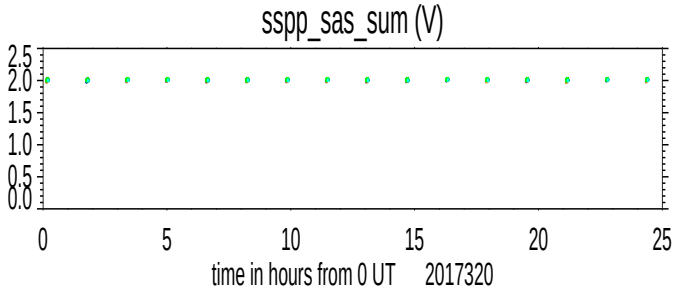

egs_sas_x (V)
-1
-2
-3
-4
-5
0
5
10
15
20
25
time in hours from 0 UT 2017320

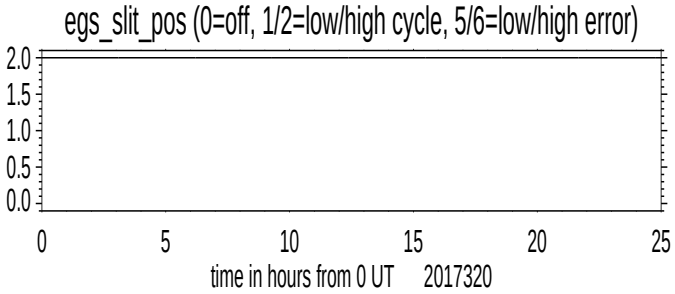

egs_sas_y (V)
10
5
0
-5
-10
0
5
10
15
20
25
time in hours from 0 UT 2017320

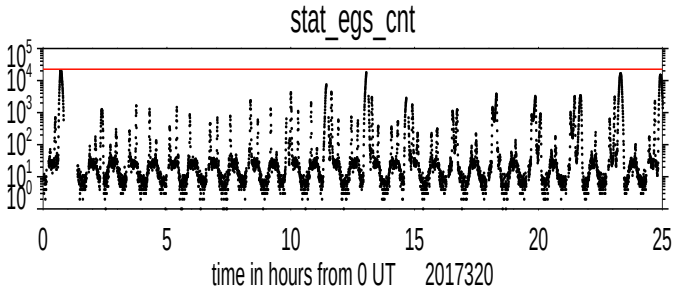

sspp_sas_x (V)
-1
-2
-3
-4
-5
0
5
10
15
20
25
time in hours from 0 UT 2017320

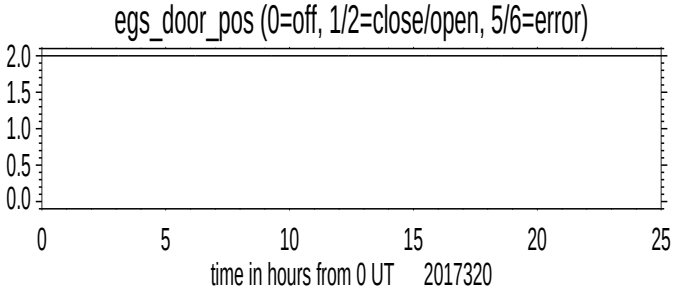

sspp_sas_y (V)
10
5
0
-5
-10
0
5
10
15
20
25
time in hours from 0 UT 2017320

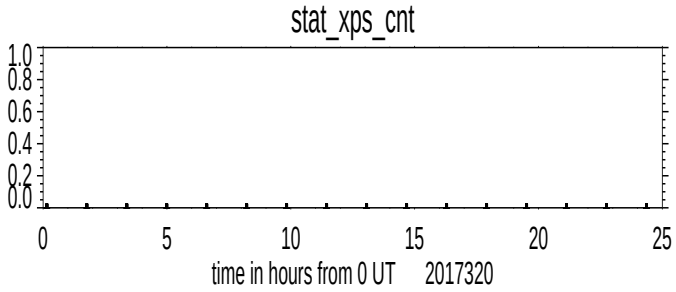

egs_sas_sum (V)
2.5
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320

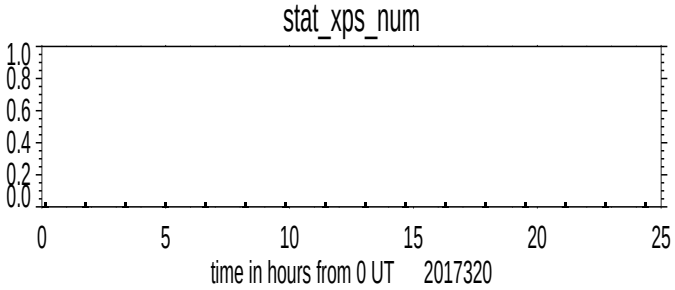

sspp_sas_sum (V)
2.5
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320

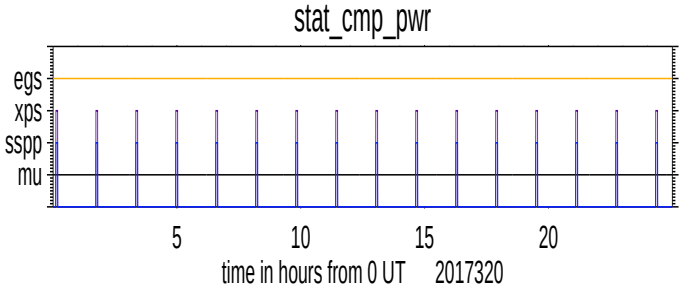

egs_slit_pos (0=off, 1/2=low/high cycle, 5/6=low/high error)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320


stat_egs_cnt
10^5
10^4
10^3
10^2
10^1
10^0
0
5
10
15
20
25
time in hours from 0 UT 2017320

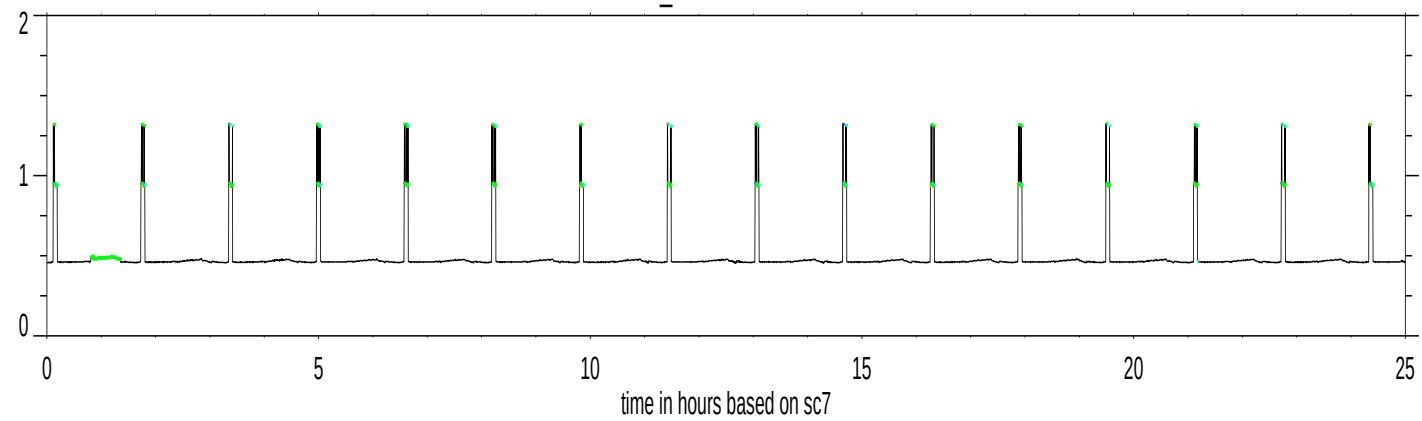

egs_door_pos (0=off, 1/2=close/open, 5/6=error)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320


stat_xps_cnt
1.0
0.8
0.6
0.4
0.2
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320

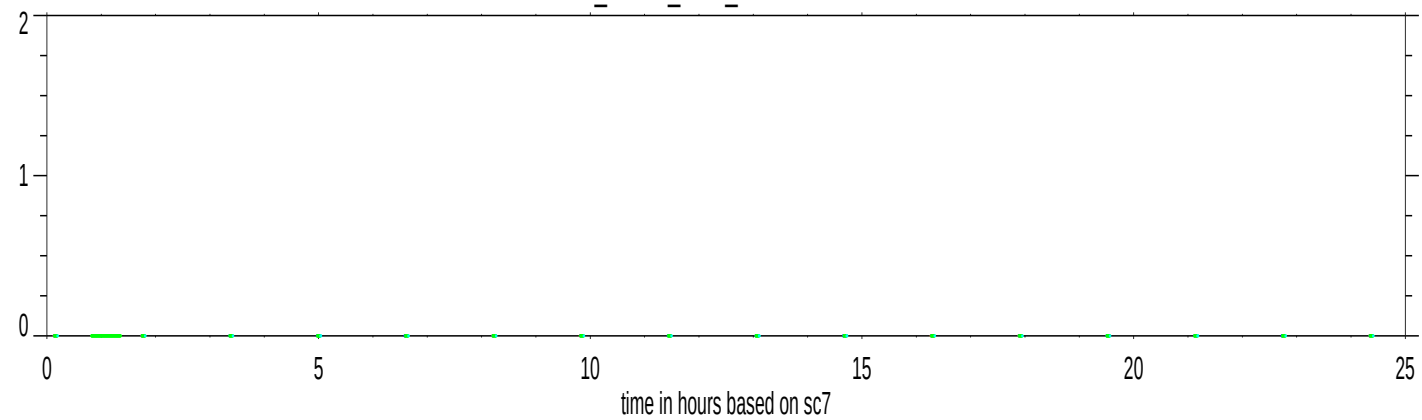

stat_xps_num
1.0
0.8
0.6
0.4
0.2
0.0
0
5
10
15
20
25
time in hours from 0 UT 2017320


stat_cmp_pwr
egs
xps
sspp
mu
5
10
15
20
time in hours from 0 UT 2017320

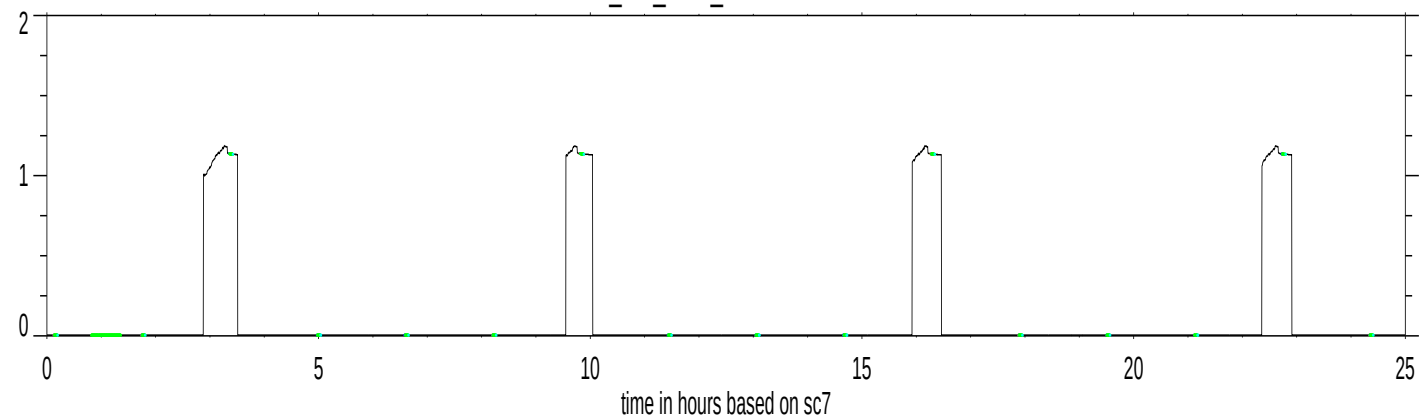
SEE_CUR 2017320



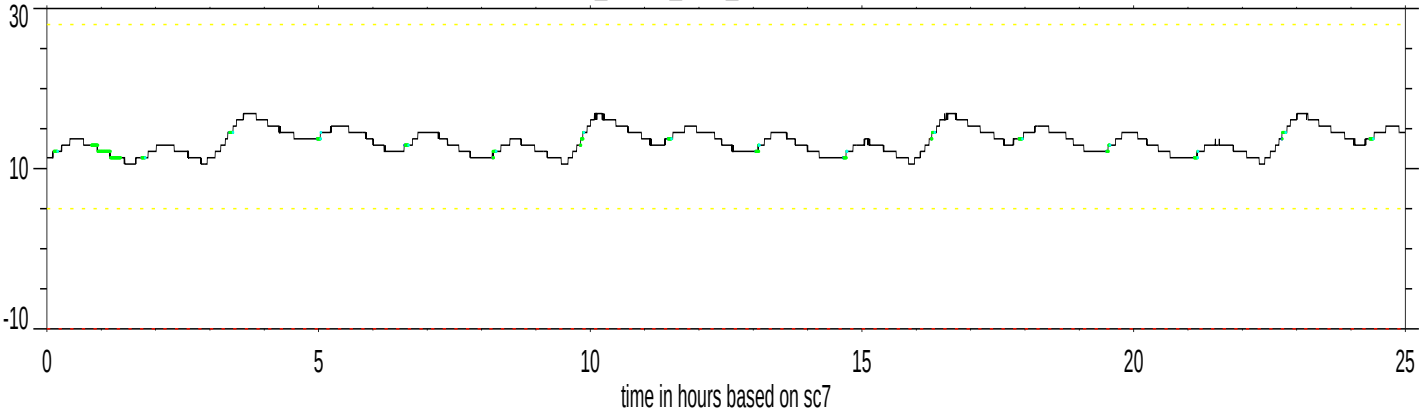
SEE_SURV_HTR_CUR 2017320



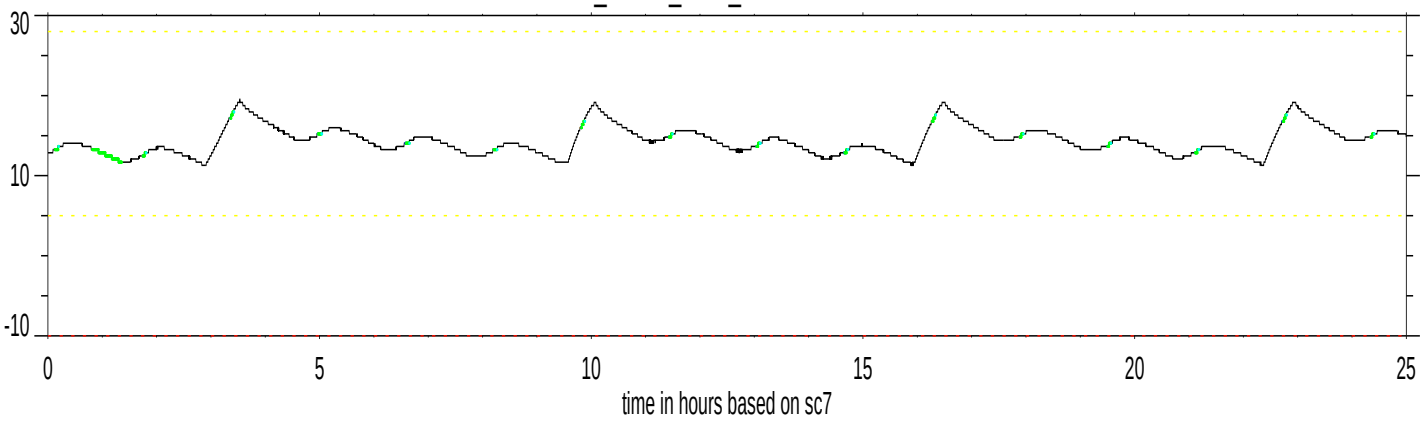
SEE_OP_HTR_CUR 2017320



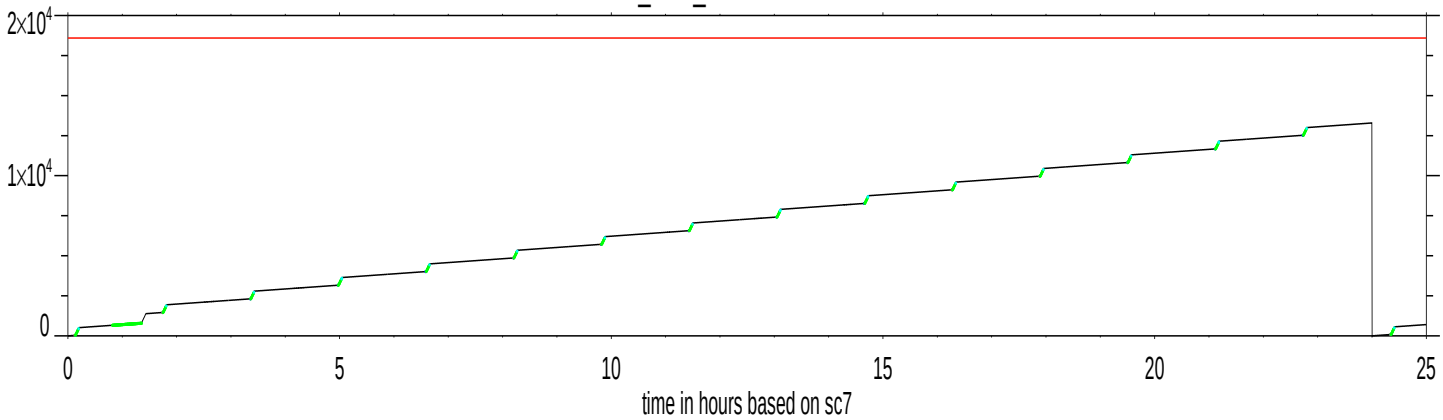
SSPP_BHND_XPS_TEMP 2017320



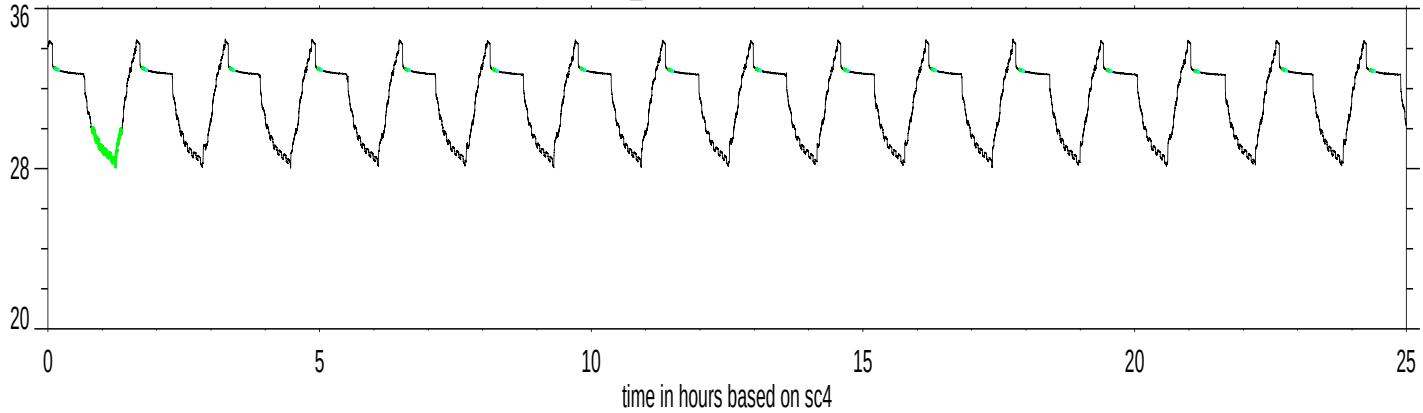
SSPP_BHND_MTR_TEMP 2017320



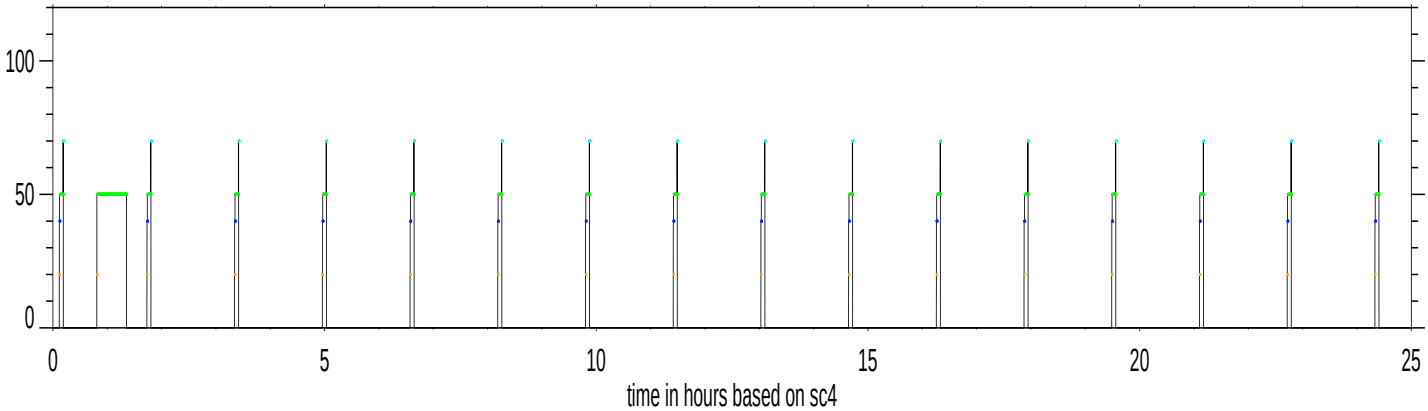
SEE_PKT_COUNT 2017320

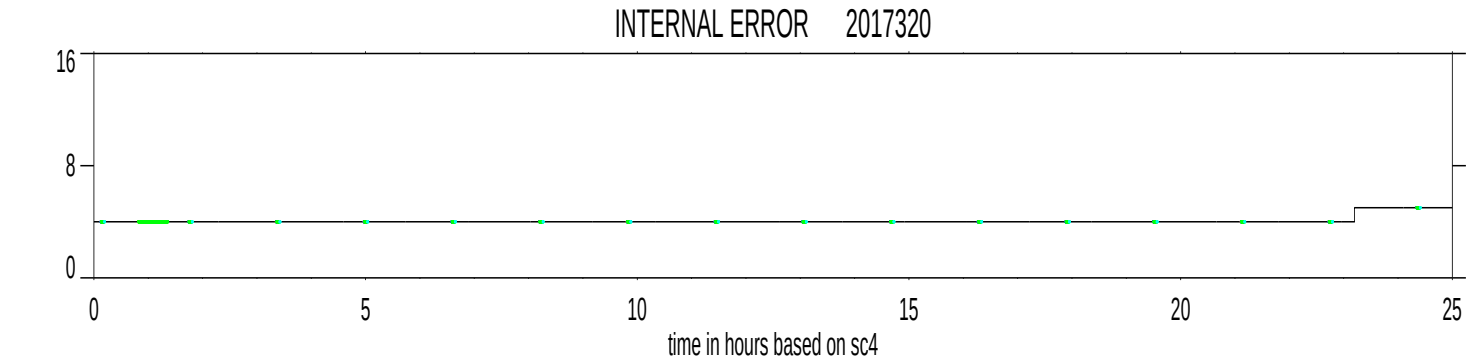


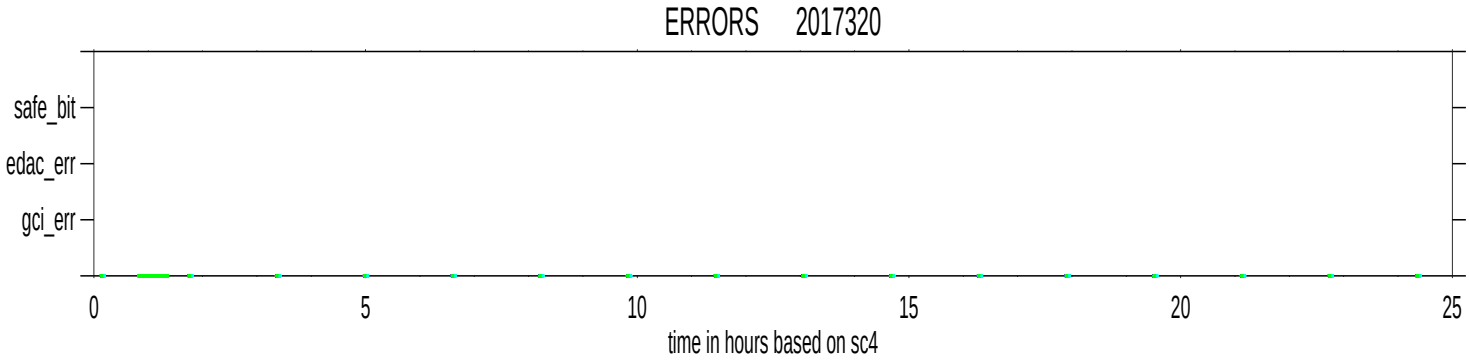
BUS_VOLTAGE 2017320

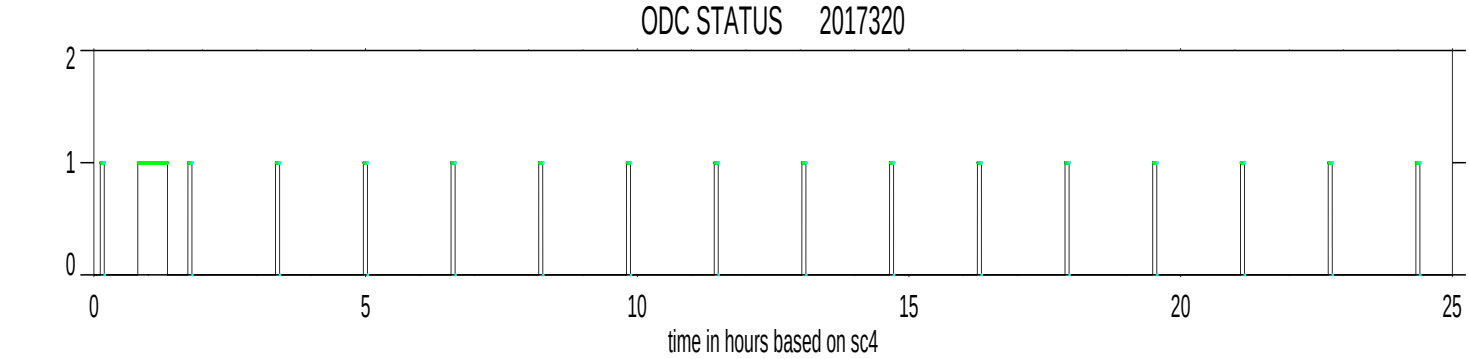


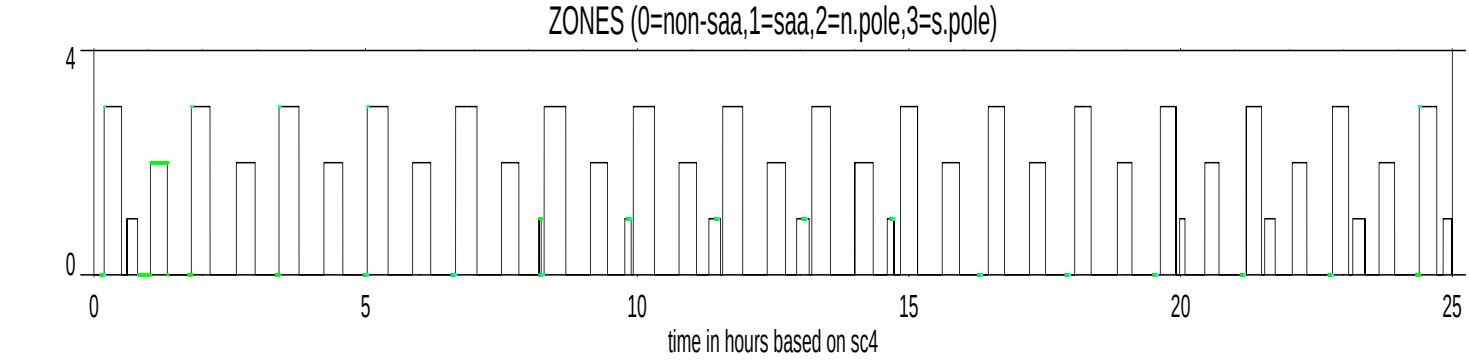
SEE_MU Operational Mode 2017320

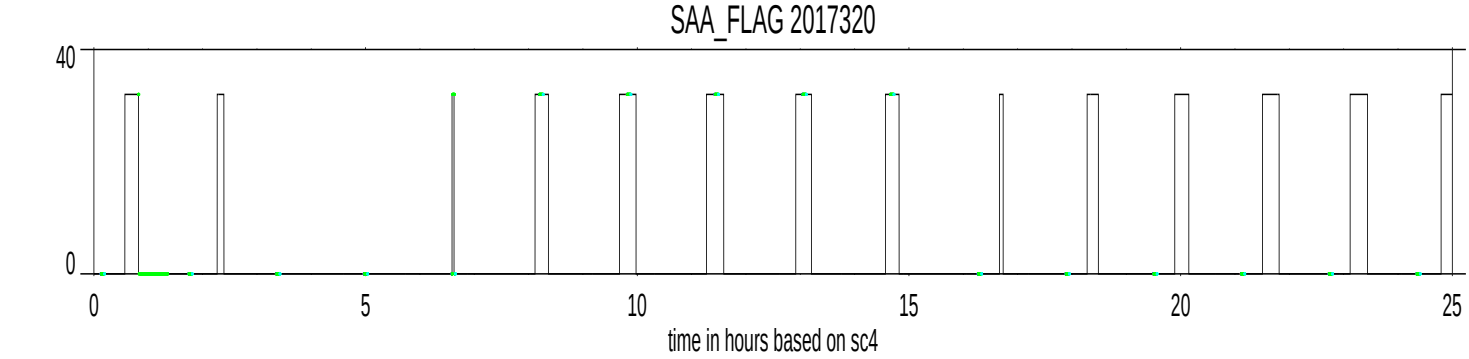


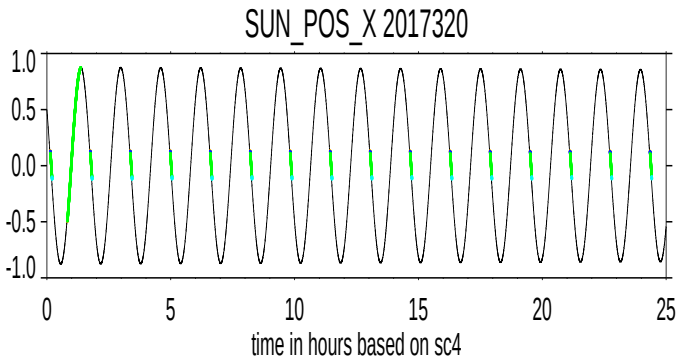

INTERNAL ERROR 2017320
16
8
0
0
5
10
15
20
25
time in hours based on sc4

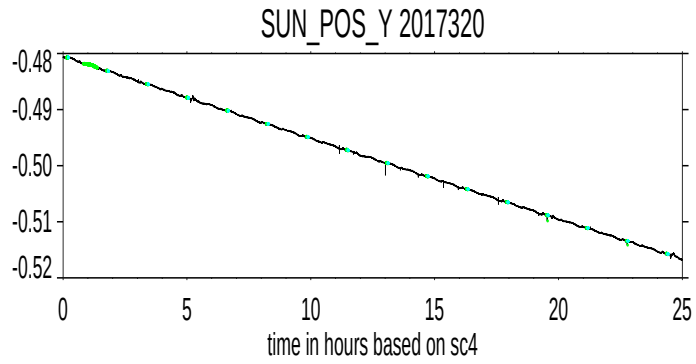

ERRORS 2017320
safe_bit
edac_err
gci_err
0
5
10
15
20
25
time in hours based on sc4

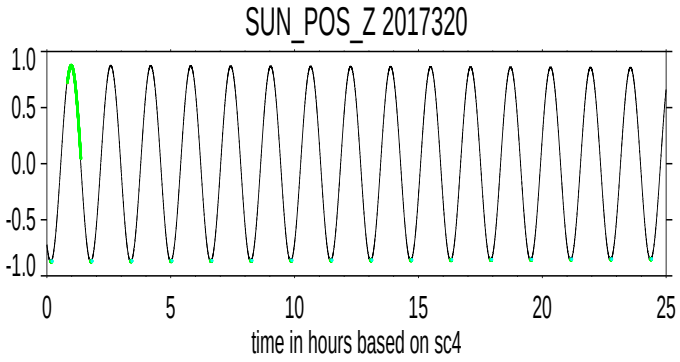

ODC STATUS 2017320
2
1
0
0
5
10
15
20
25
time in hours based on sc4

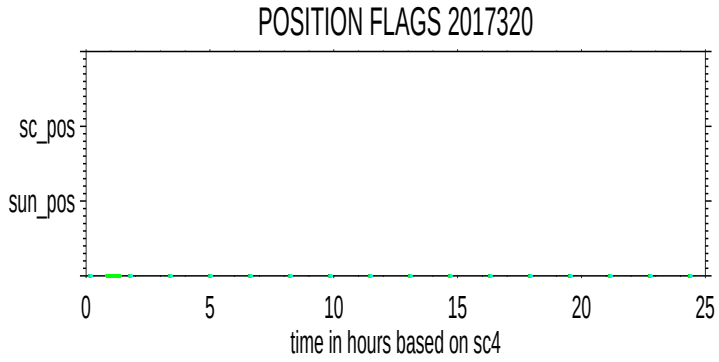

ZONES (0=non-saa,1=saa,2=n.pole,3=s.pole)
4
0
0
5
10
15
20
25
time in hours based on sc4

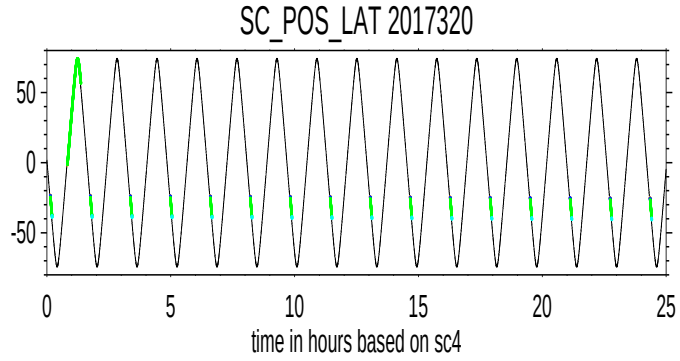

SAA_FLAG 2017320
40
0
0
5
10
15
20
25
time in hours based on sc4

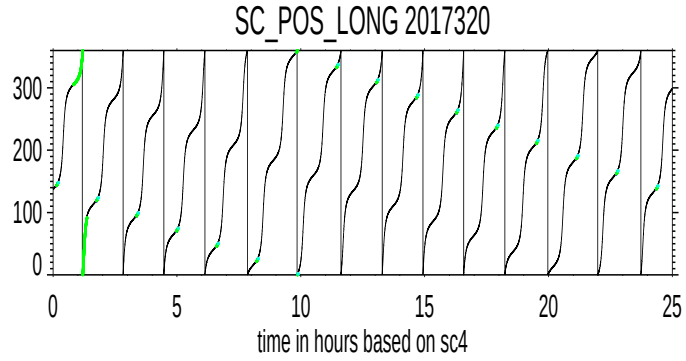

SUN_POS_X 2017320
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4

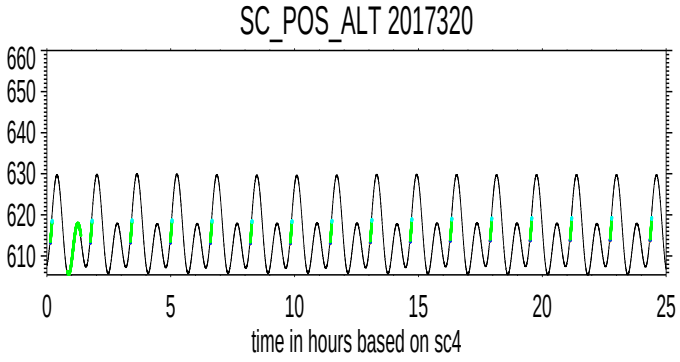

SUN_POS_Y 2017320
-0.48
-0.49
-0.50
-0.51
-0.52
0
5
10
15
20
25
time in hours based on sc4

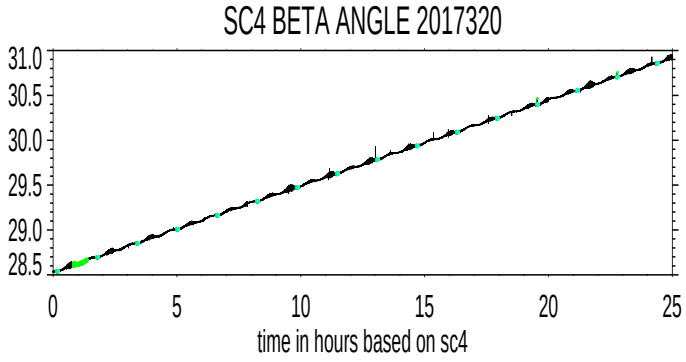

SUN_POS_Z 2017320
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4


POSITION FLAGS 2017320
sc_pos
sun_pos
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LAT 2017320
50
0
-50
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LONG 2017320
300
200
100
0
0
5
10
15
20
25
time in hours based on sc4


SC_POS_ALT 2017320
660
650
640
630
620
610
0
5
10
15
20
25
time in hours based on sc4


SC4 BETA ANGLE 2017320
31.0
30.5
30.0
29.5
29.0
28.5
0
5
10
15
20
25
time in hours based on sc4