```
Information for      2018312
24:57:12.002: see_stat[1]: 14 internal error
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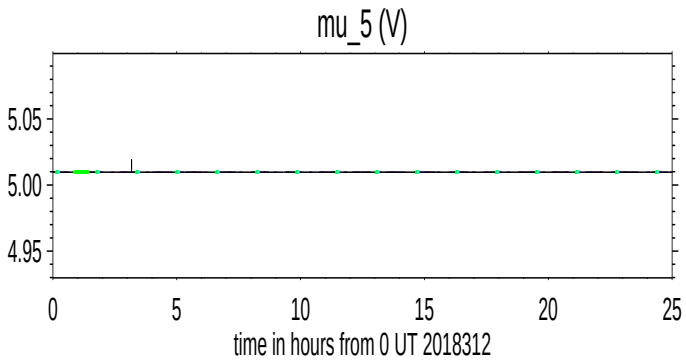
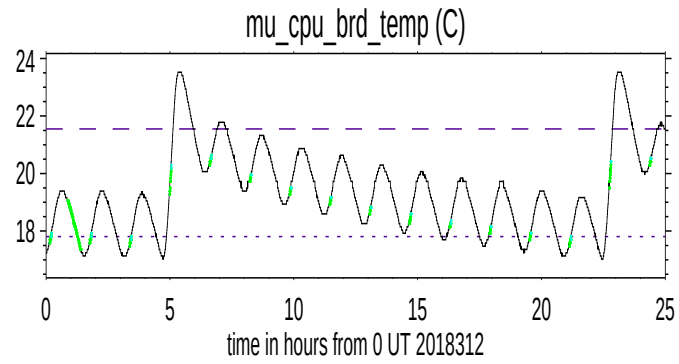

mu_5 (V)
5.05
5.00
4.95
0
5
10
15
20
25
time in hours from 0 UT 2018312


mu_cpu_brd_temp (C)
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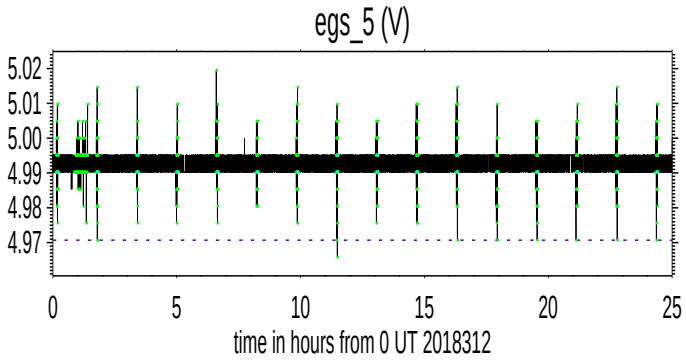
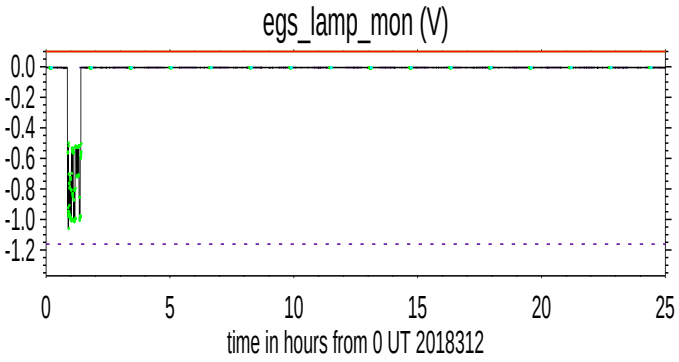
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_5 (V)
5.02
5.01
5.00
4.99
4.98
4.97
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_lamp_mon (V)
0.0
-0.2
-0.4
-0.6
-0.8
-1.0
-1.2
0
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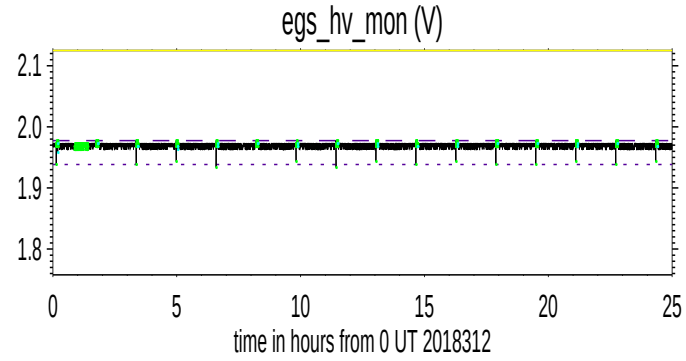
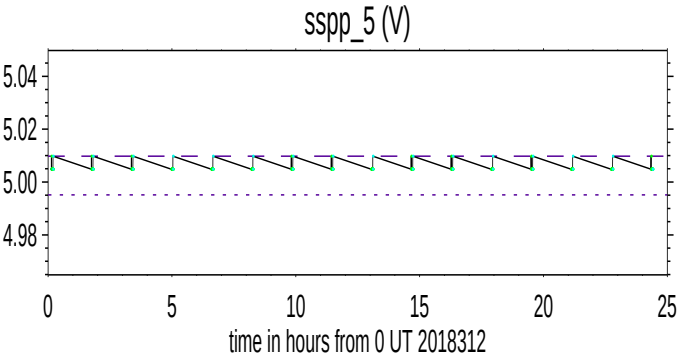
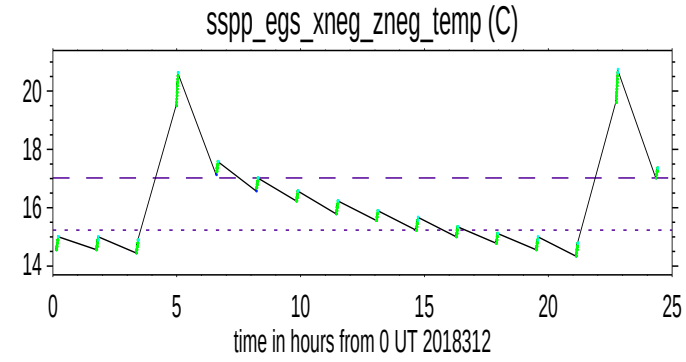
10
15
20
25
time in hours from 0 UT 2018312


egs_hv_mon (V)
2.1
2.0
1.9
1.8
0
5
10
15
20
25
time in hours from 0 UT 2018312


sspp_5 (V)
5.04
5.02
5.00
4.98
0
5
10
15
20
25
time in hours from 0 UT 2018312


sspp_egs_xneg_zneg_temp (C)
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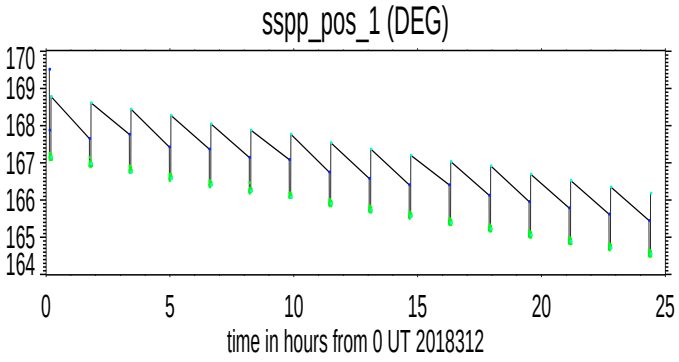
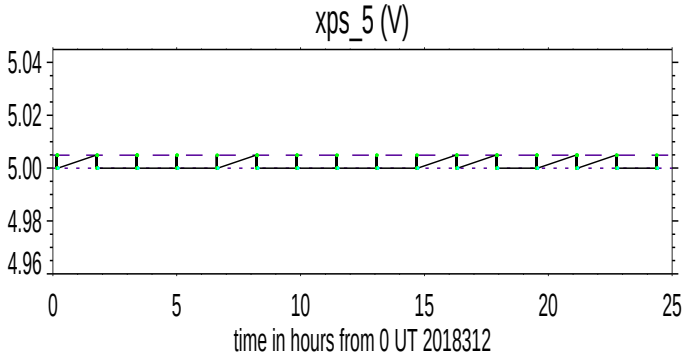
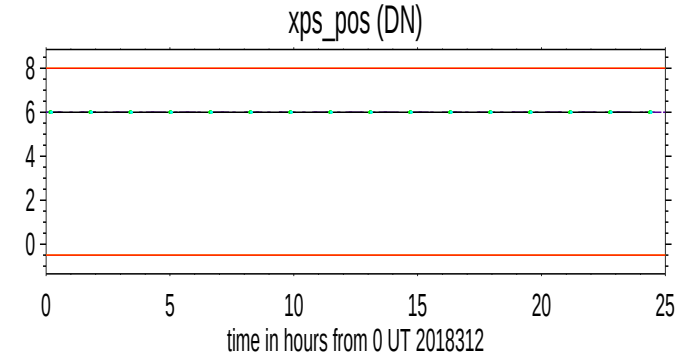
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312


sspp_pos_1 (DEG)
170
169
168
167
166
165
164
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_5 (V)
5.04
5.02
5.00
4.98
4.96
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_pos (DN)
8
6
4
2
0
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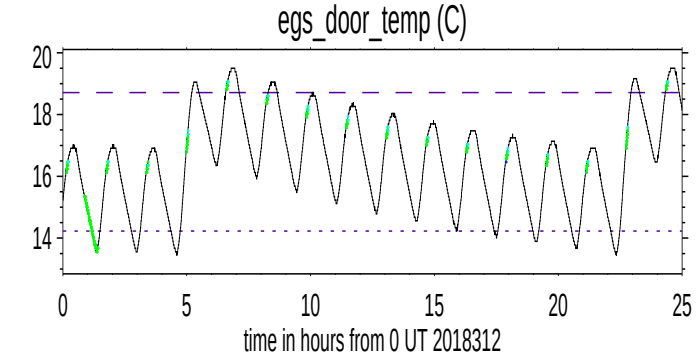
5
10
15
20
25
time in hours from 0 UT 2018312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18
16
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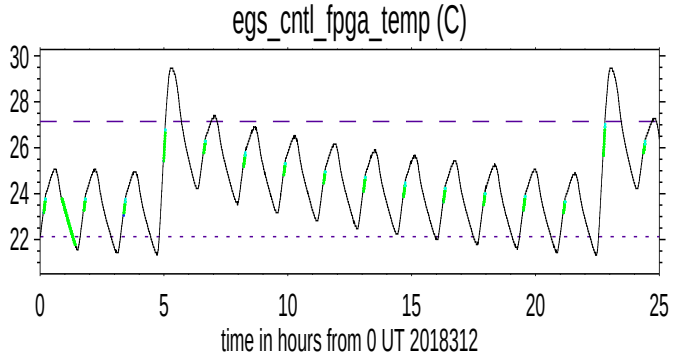
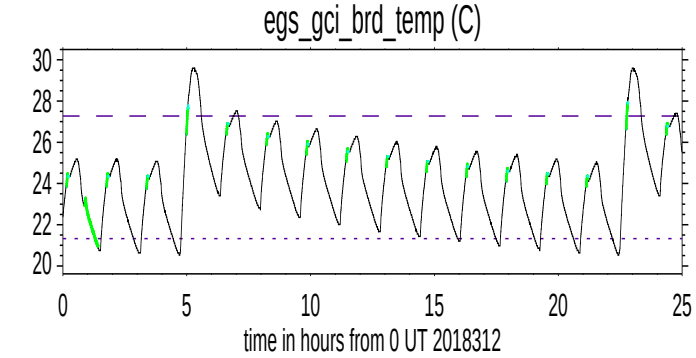
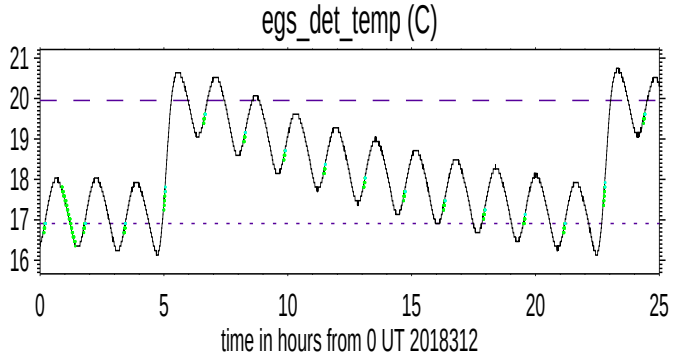
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_cntl_fpga_temp (C)
30
28
26
24
22
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_gci_brd_temp (C)
30
28
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2018312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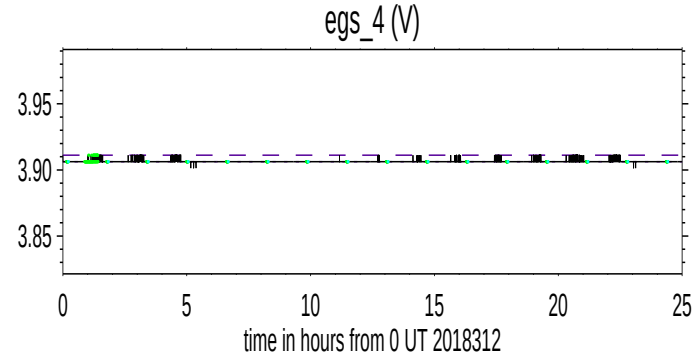
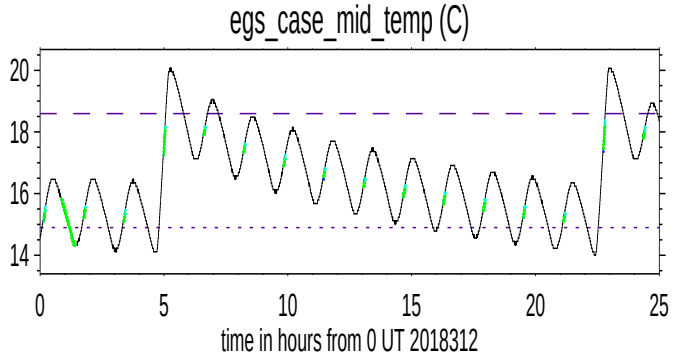
17
16
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2018312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18
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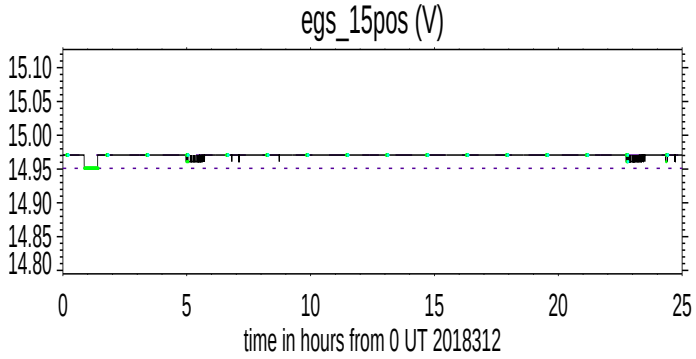
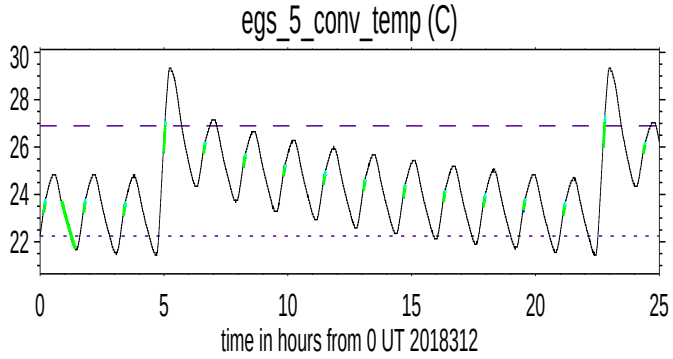
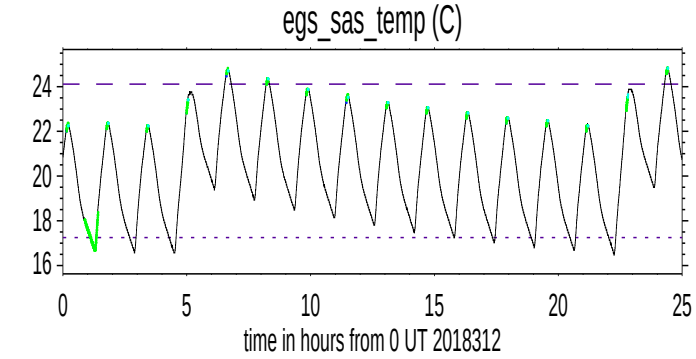
14
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_15pos (V)
15.10
15.05
15.00
14.95
14.90
14.85
14.80
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_5_conv_temp (C)
30
28
26
24
22
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_sas_temp (C)
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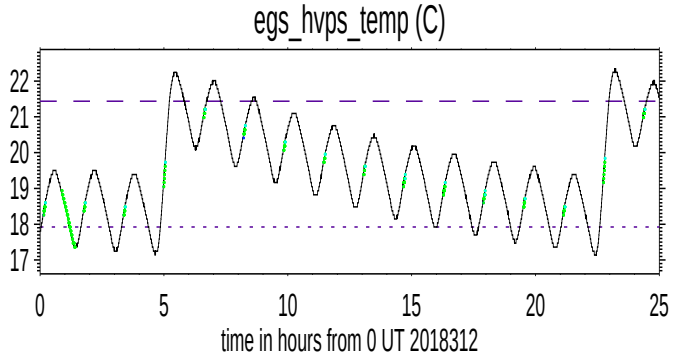
22
20
18
16
0
5
10
15
20
25
time in hours from 0 UT 2018312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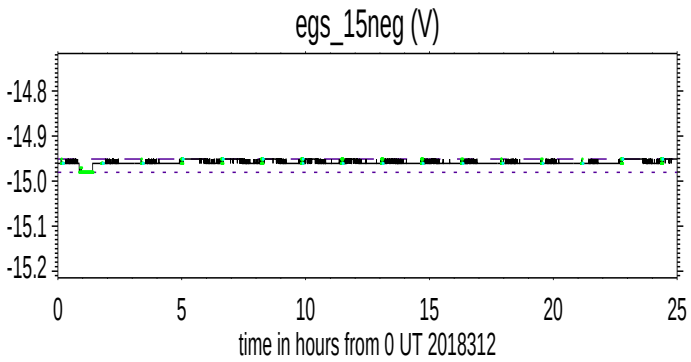
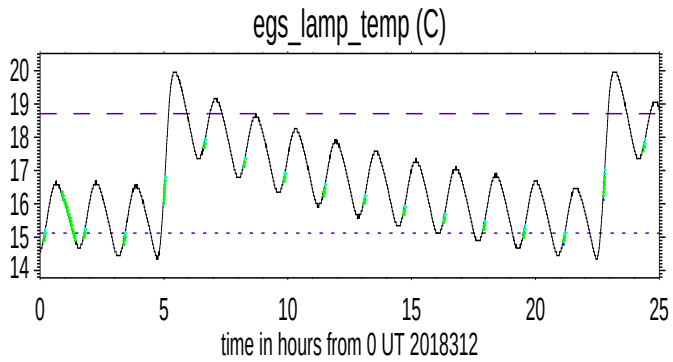
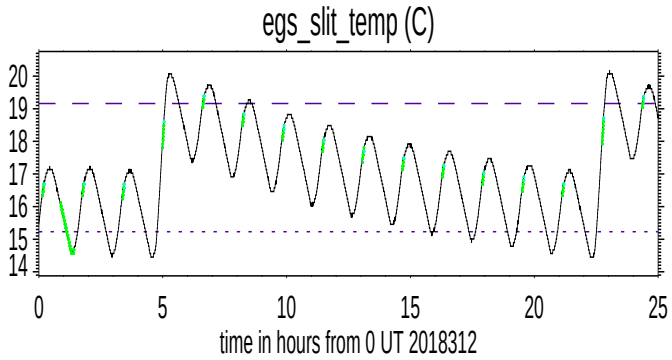
21
20
19
18
17
0
5
10
15
20
25
time in hours from 0 UT 2018312


egs_15neg (V)
-14.8
-14.9
-15.0
-15.1
-15.2
0 5 10 15 20 25
time in hours from 0 UT 2018312


egs_lamp_temp (C)
20 19 18 17 16 15 14
0 5 10 15 20 25
time in hours from 0 UT 2018312


egs_slit_temp (C)
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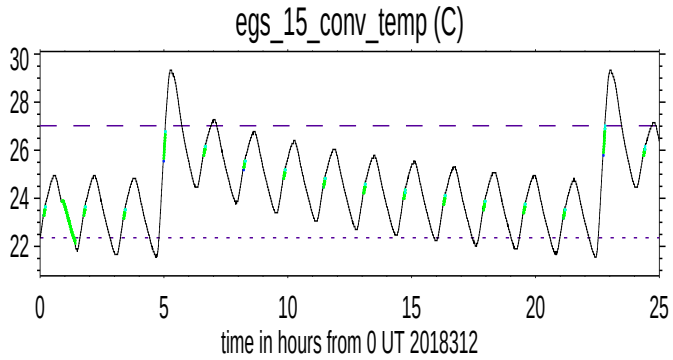
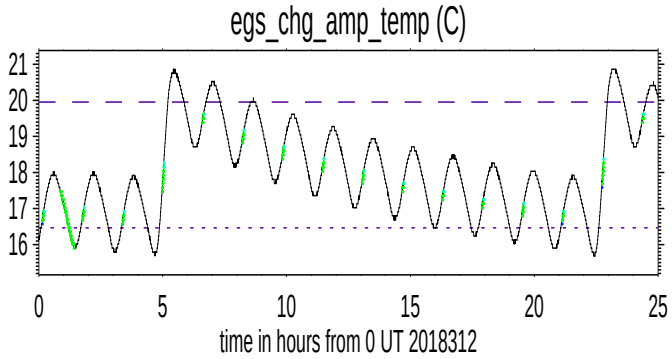
0 5 10 15 20 25
time in hours from 0 UT 2018312


egs_15_conv_temp (C)
30 28 26 24 22
0 5 10 15 20 25
time in hours from 0 UT 2018312


egs_chg_amp_temp (C)
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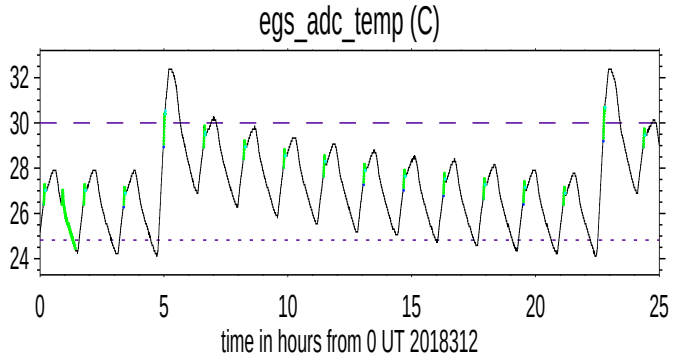
0 5 10 15 20 25
time in hours from 0 UT 2018312


egs_adc_temp (C)
32 30 28 26 24
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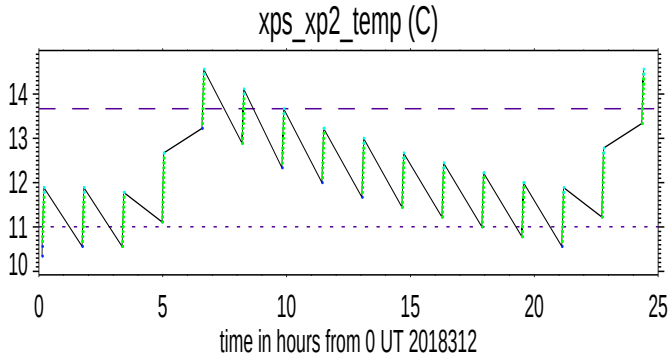
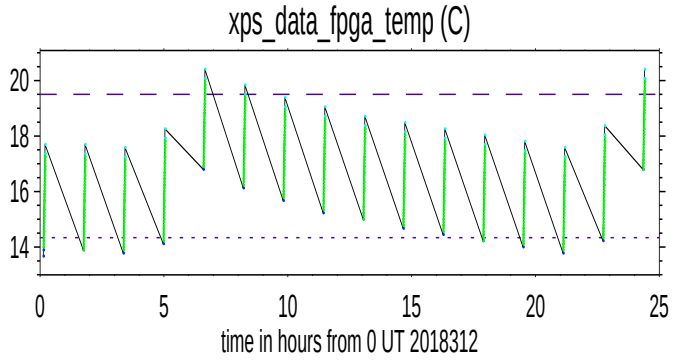
time in hours from 0 UT 2018312


xps_xp2_temp (C)
14 13 12 11 10
0 5 10 15 20 25
time in hours from 0 UT 2018312


xps_data_fpga_temp (C)
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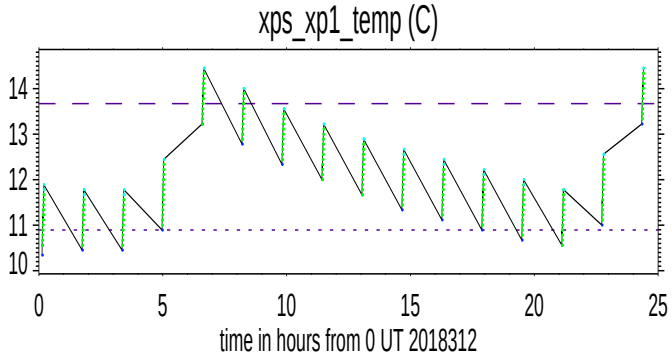
0 5 10 15 20 25
time in hours from 0 UT 2018312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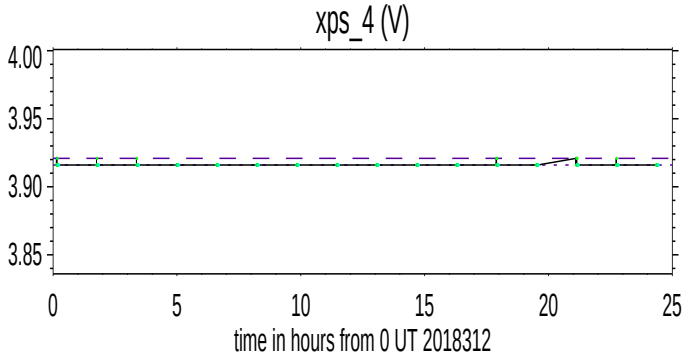
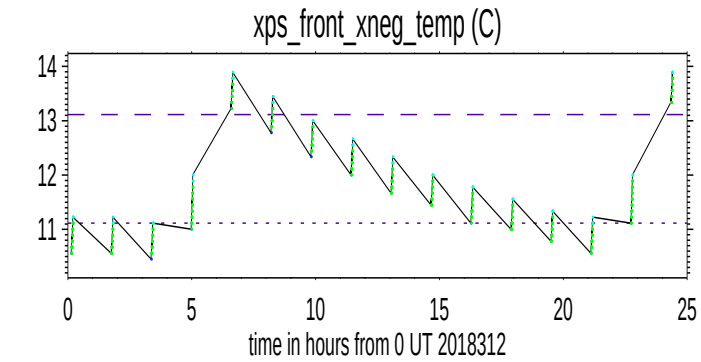
0 5 10 15 20 25
time in hours from 0 UT 2018312


xps_4 (V)
4.00 3.95 3.90 3.85
0 5 10 15 20 25
time in hours from 0 UT 2018312


xps_front_xneg_temp (C)
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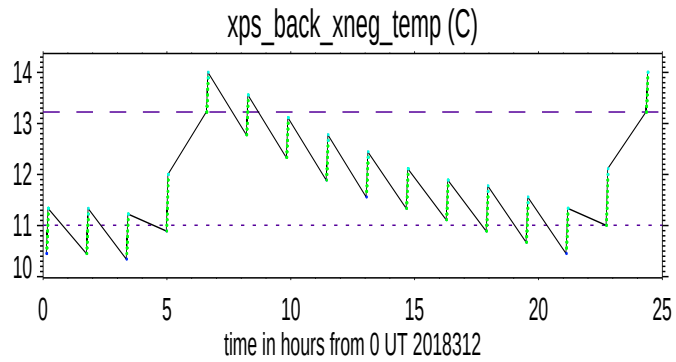
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_back_xneg_temp (C)
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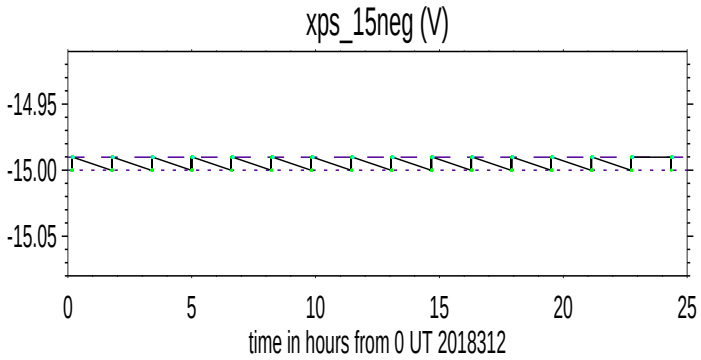
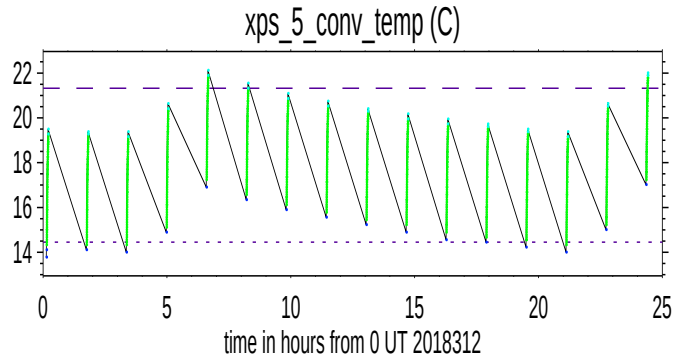
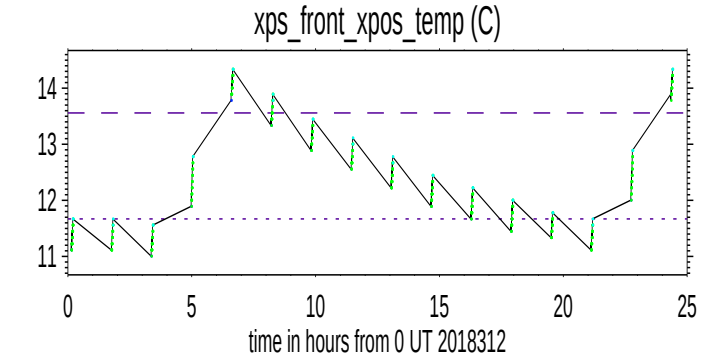
12
11
10
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_15neg (V)
-14.95
-15.00
-15.05
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_5_conv_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_front_xpos_temp (C)
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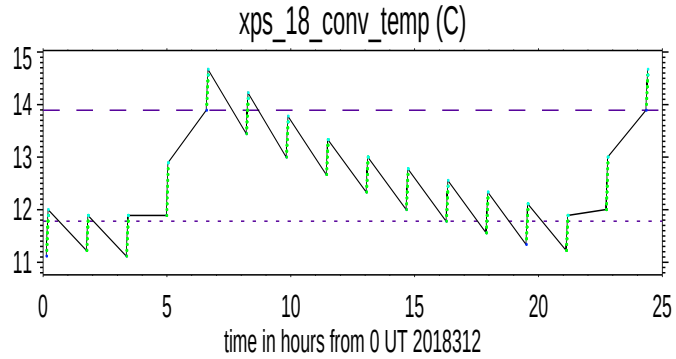
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2018312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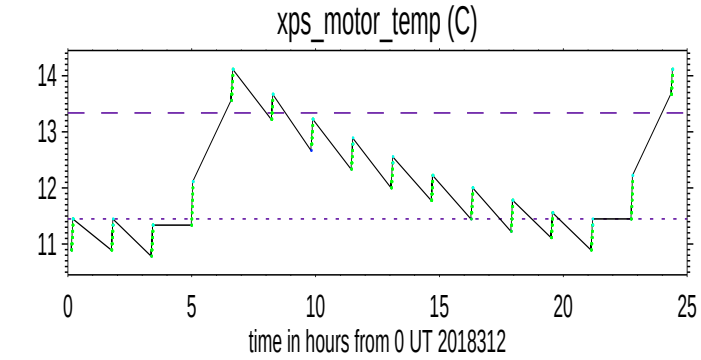
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2018312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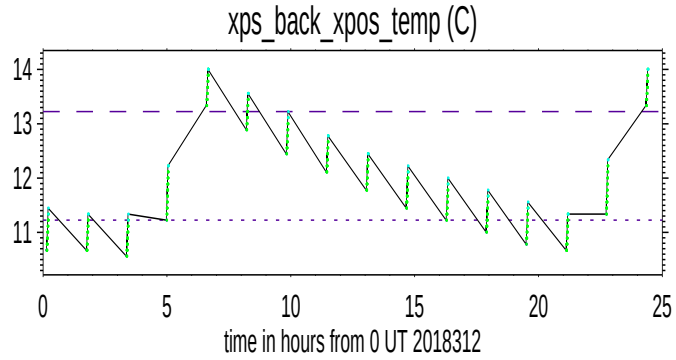
13
12
11
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_back_xpos_temp (C)
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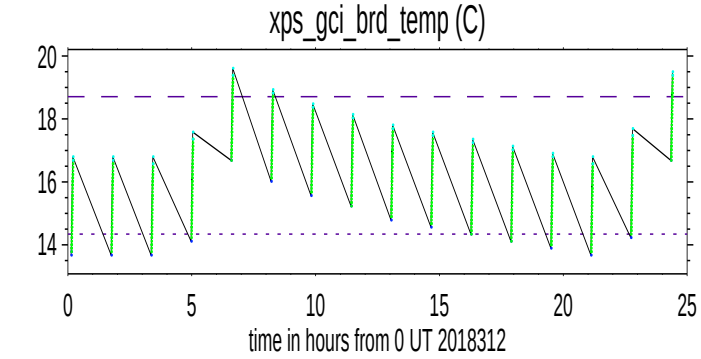
12
11
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_gci_brd_temp (C)
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18
16
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0
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10
15
20
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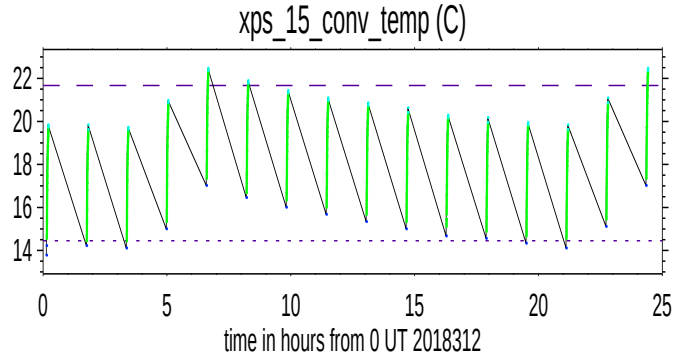
time in hours from 0 UT 2018312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22
20
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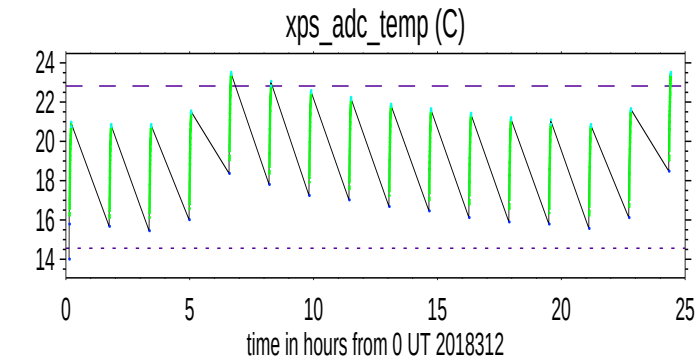
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312


xps_adc_temp (C)
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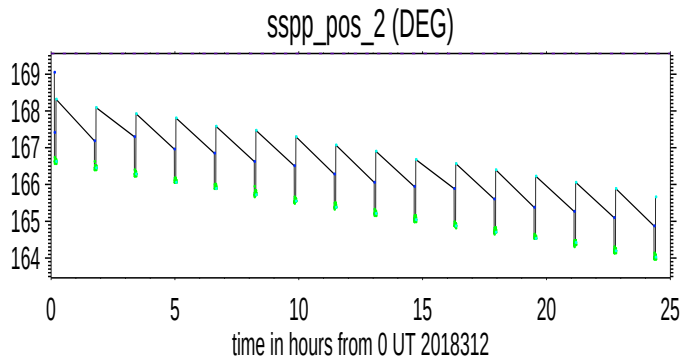
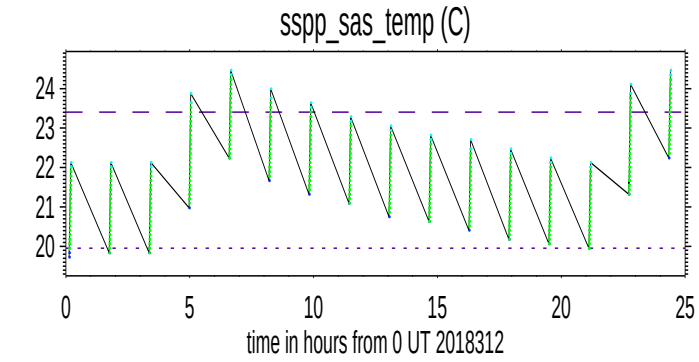
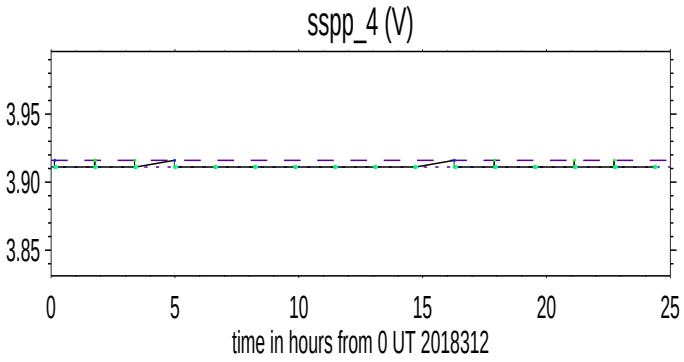
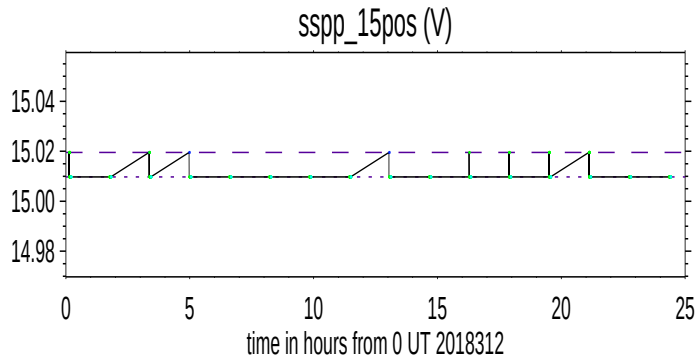
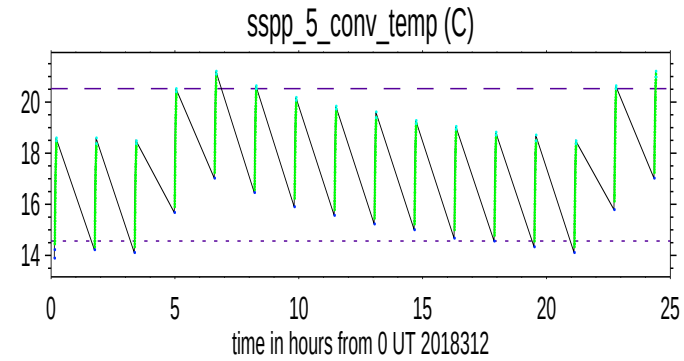
25
time in hours from 0 UT 2018312

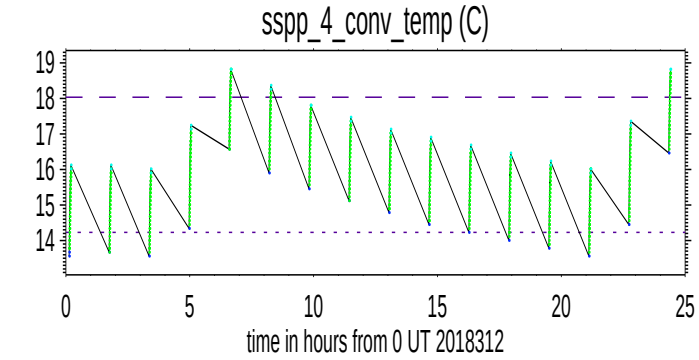

sspp_pos_2 (DEG)
169
168
167
166
165
164
0
5
10
15
20
25
time in hours from 0 UT 2018312

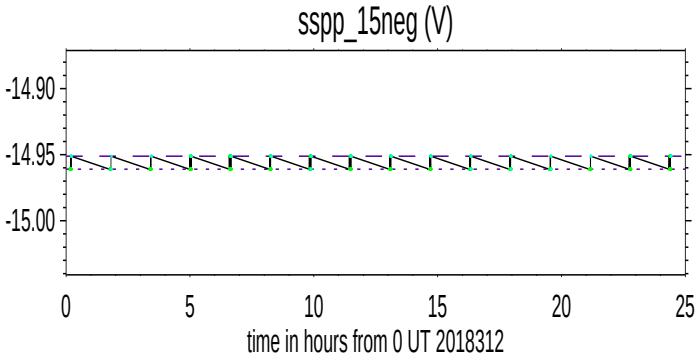

sspp_sas_temp (C)
24
23
22
21
20
0
5
10
15
20
25
time in hours from 0 UT 2018312

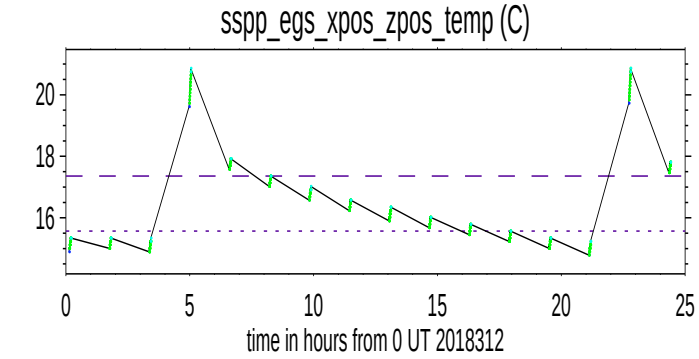

sspp_4 (V)
3.95
3.90
3.85
0
5
10
15
20
25
time in hours from 0 UT 2018312

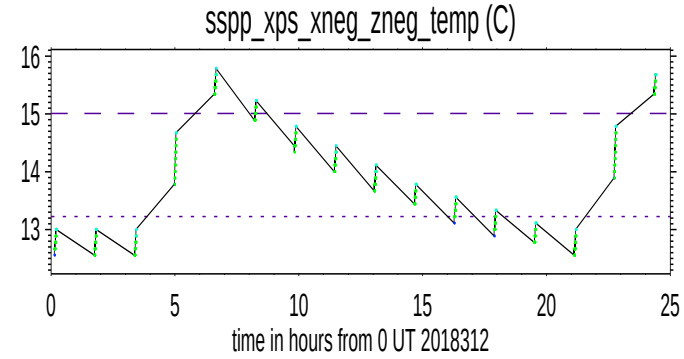

sspp_15pos (V)
15.04
15.02
15.00
14.98
0
5
10
15
20
25
time in hours from 0 UT 2018312

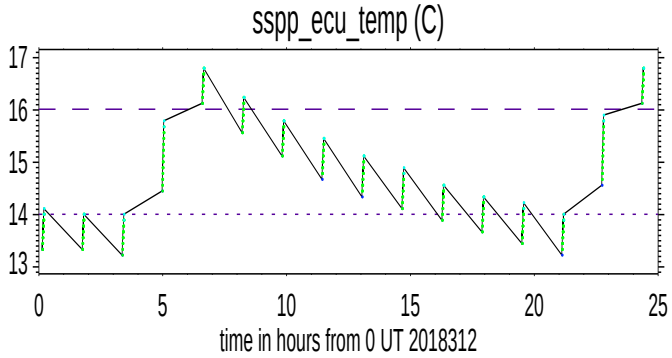

sspp_5_conv_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

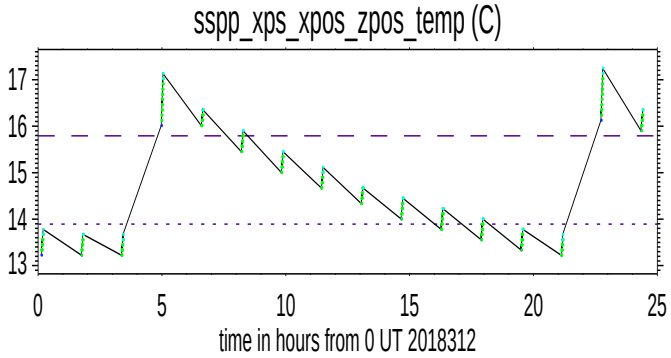

sspp_4_conv_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

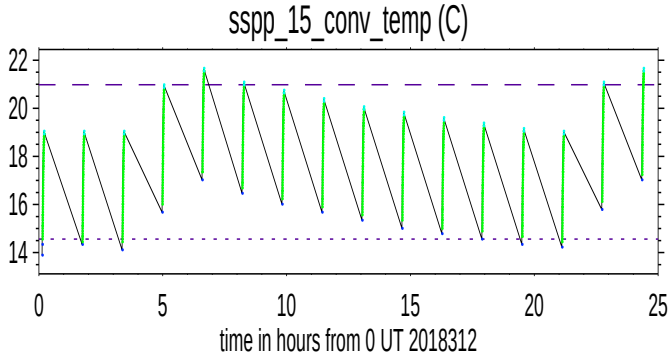

sspp_15neg (V)
-14.90
-14.95
-15.00
0
5
10
15
20
25
time in hours from 0 UT 2018312

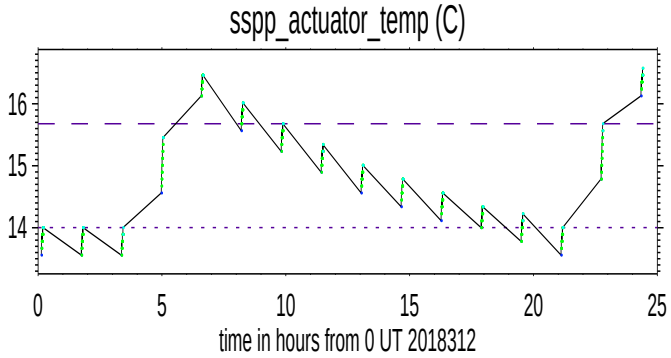

sspp_egs_xpos_zpos_temp (C)
20
18
16
0
5
10
15
20
25
time in hours from 0 UT 2018312

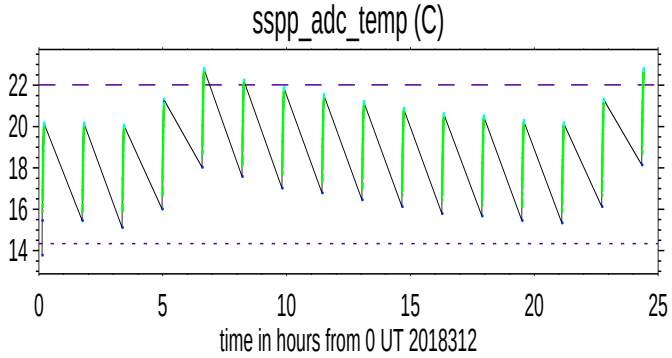

sspp_xps_xneg_zneg_temp (C)
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2018312

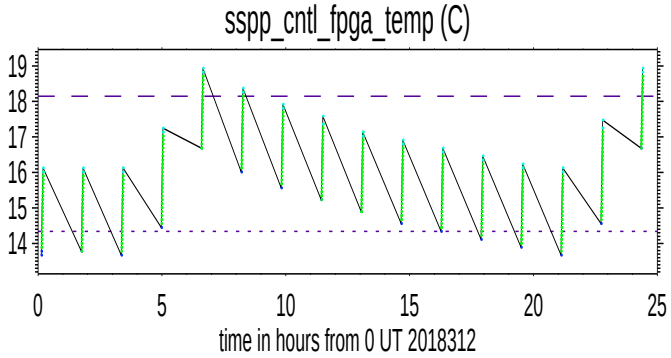

sspp_ecu_temp (C)
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2018312

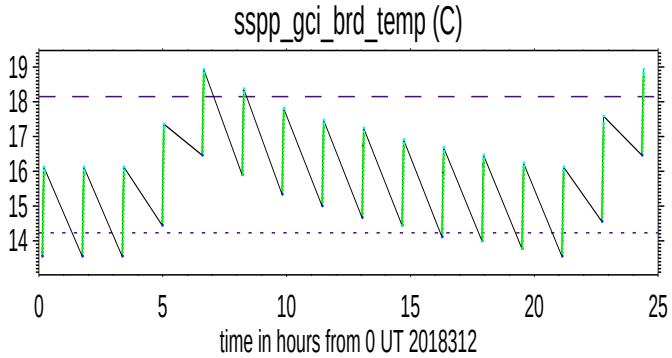

sspp_xps_xpos_zpos_temp (C)
17
16
15
14
13
0
5
10
15
20
25
time in hours from 0 UT 2018312

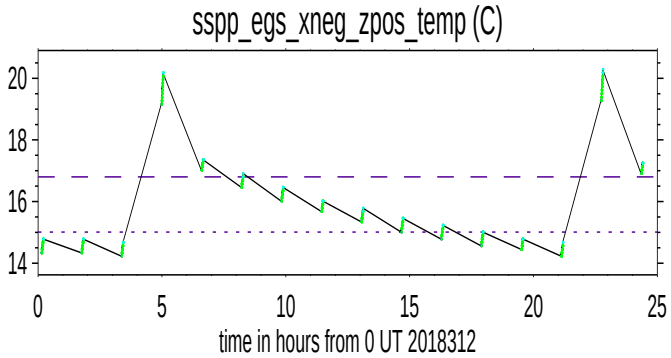

sspp_15_conv_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

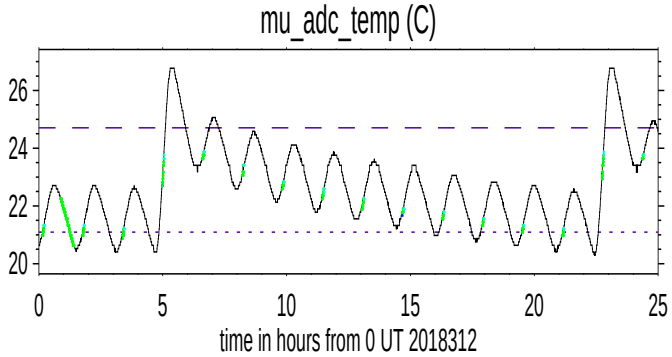

sspp_actuator_temp (C)
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

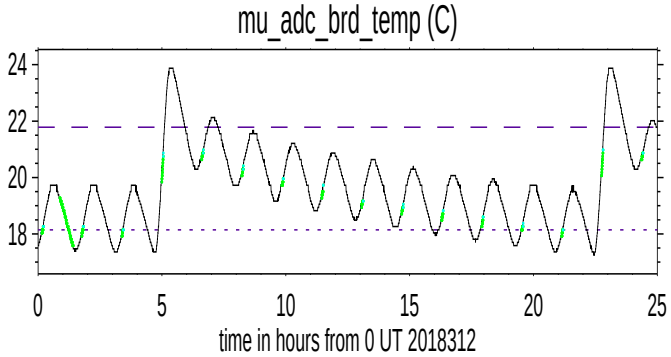

sspp_adc_temp (C)
22
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

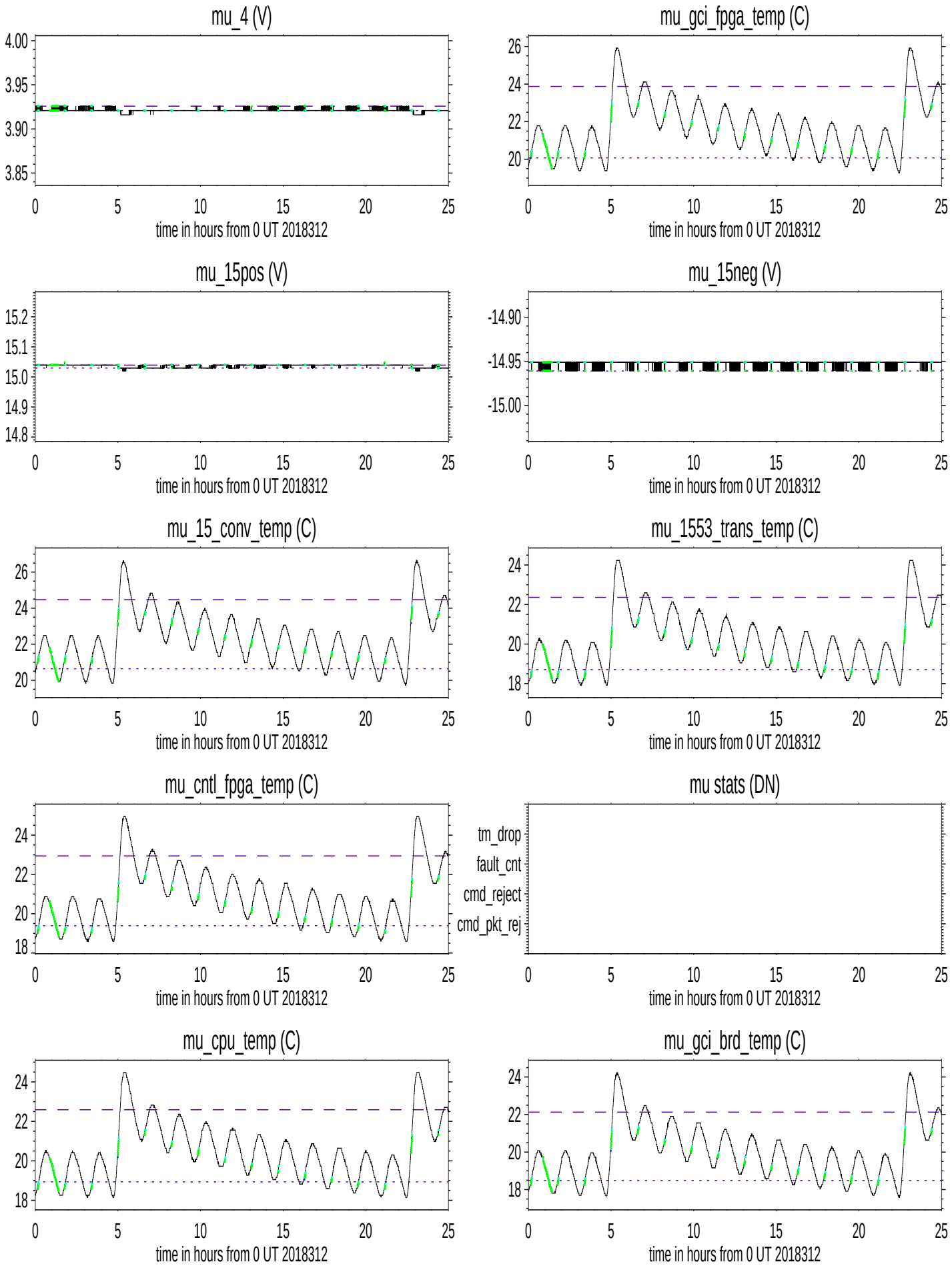

sspp_cntl_fpga_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

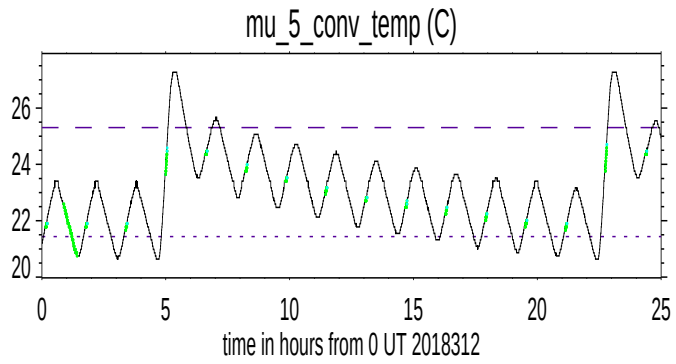

sspp_gci_brd_temp (C)
19
18
17
16
15
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

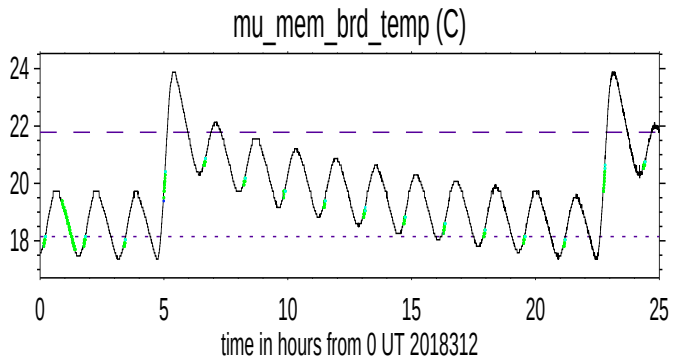

sspp_egs_xneg_zpos_temp (C)
20
18
16
14
0
5
10
15
20
25
time in hours from 0 UT 2018312

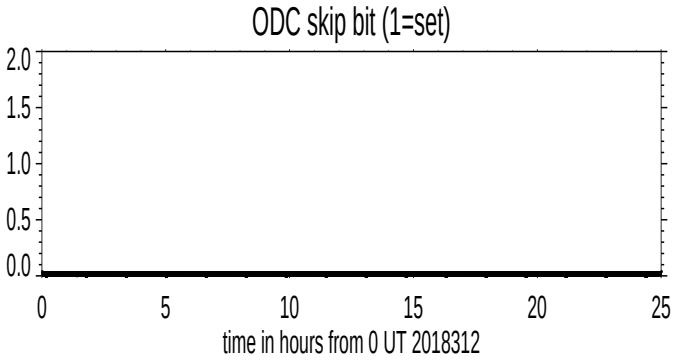

mu_adc_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2018312

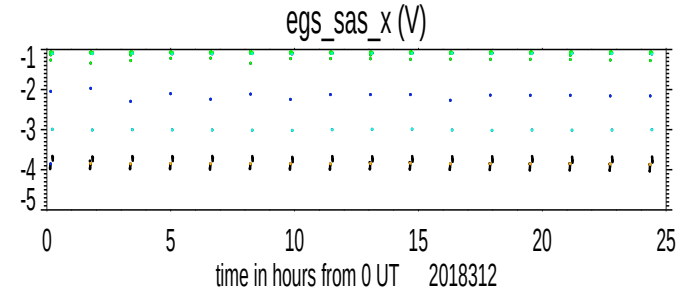

mu_adc_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2018312

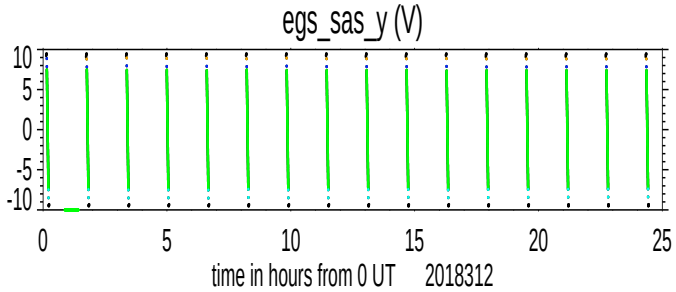

mu_4 (V)
4.00
3.95
3.90
3.85
mu_gci_fpga_temp (C)
26
24
22
20
mu_15pos (V)
15.2
15.1
15.0
14.9
14.8
mu_15neg (V)
-14.90
-14.95
-15.00
mu_15_conv_temp (C)
26
24
22
20
mu_1553_trans_temp (C)
24
22
20
18
mu_cntl_fpga_temp (C)
24
22
20
18
mu stats (DN)
tm_drop
fault_cnt
cmd_reject
cmd_pkt_rej
mu_cpu_temp (C)
24
22
20
18
mu_gci_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2018312

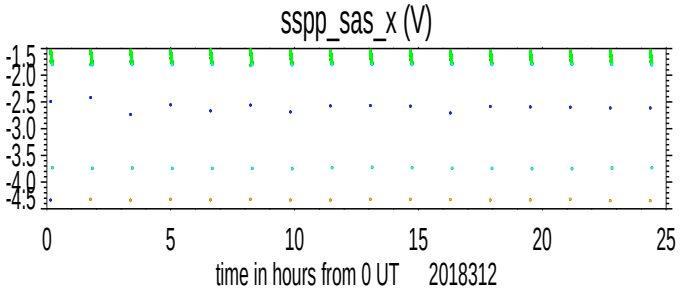

mu_5_conv_temp (C)
26
24
22
20
0
5
10
15
20
25
time in hours from 0 UT 2018312

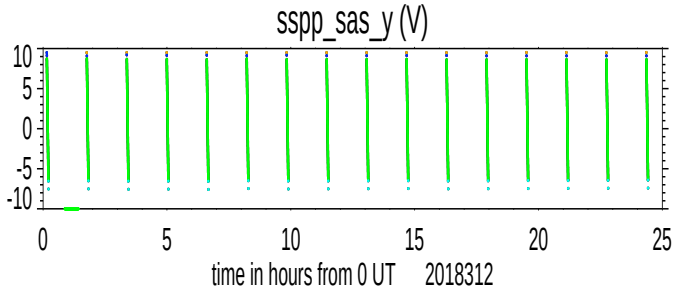

mu_mem_brd_temp (C)
24
22
20
18
0
5
10
15
20
25
time in hours from 0 UT 2018312

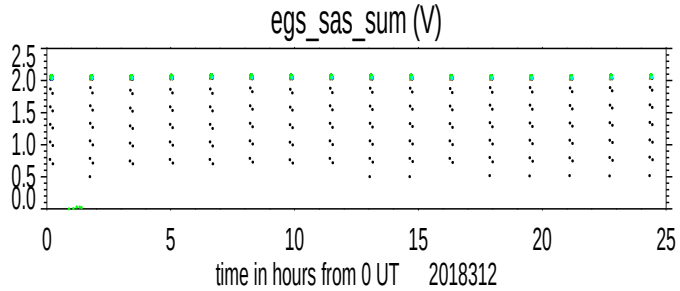

ODC skip bit (1=set)
2.0
1.5
1.0
0.5
0.0
0
5
10
15
20
25
time in hours from 0 UT 2018312

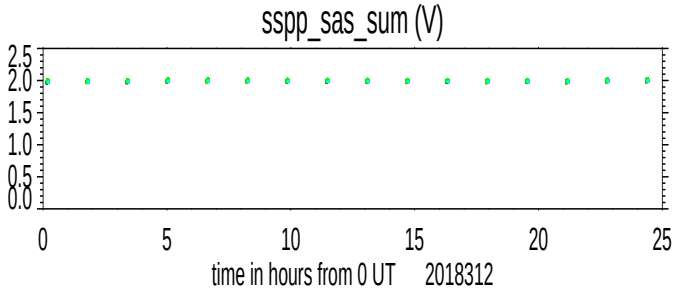

egs_sas_x (V)
time in hours from 0 UT 2018312

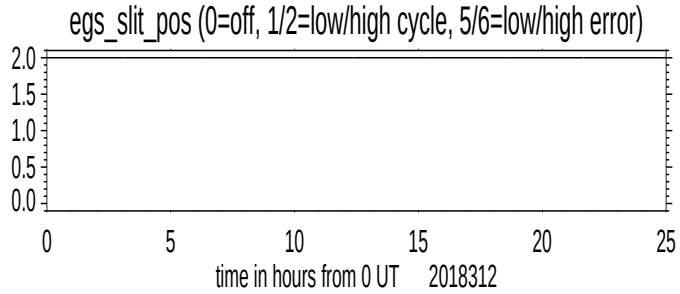

egs_sas_y (V)
time in hours from 0 UT 2018312

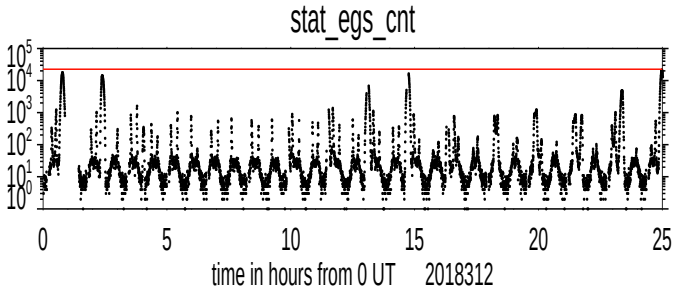

sspp_sas_x (V)
time in hours from 0 UT 2018312

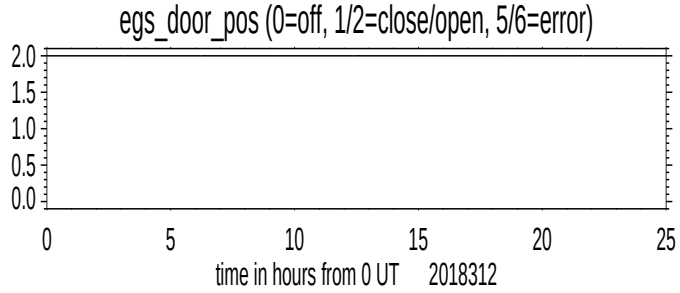

sspp_sas_y (V)
time in hours from 0 UT 2018312

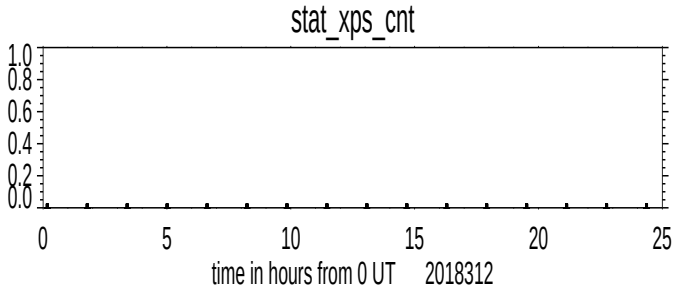

egs_sas_sum (V)
time in hours from 0 UT 2018312

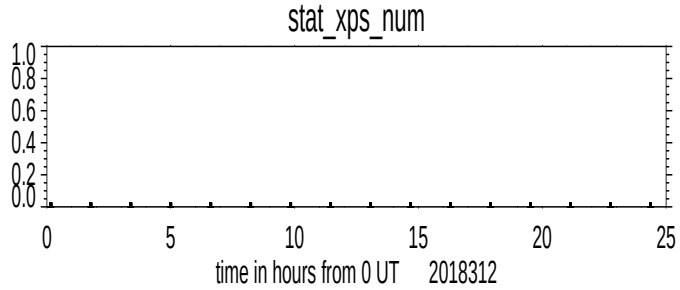

sspp_sas_sum (V)
time in hours from 0 UT 2018312

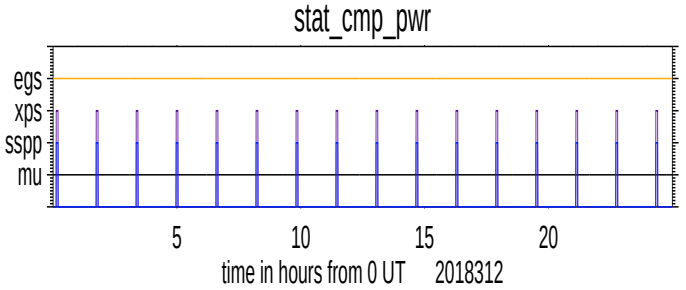

egs_slit_pos (0=off, 1/2=low/high cycle, 5/6=low/high error)
time in hours from 0 UT 2018312


stat_egs_cnt
time in hours from 0 UT 2018312

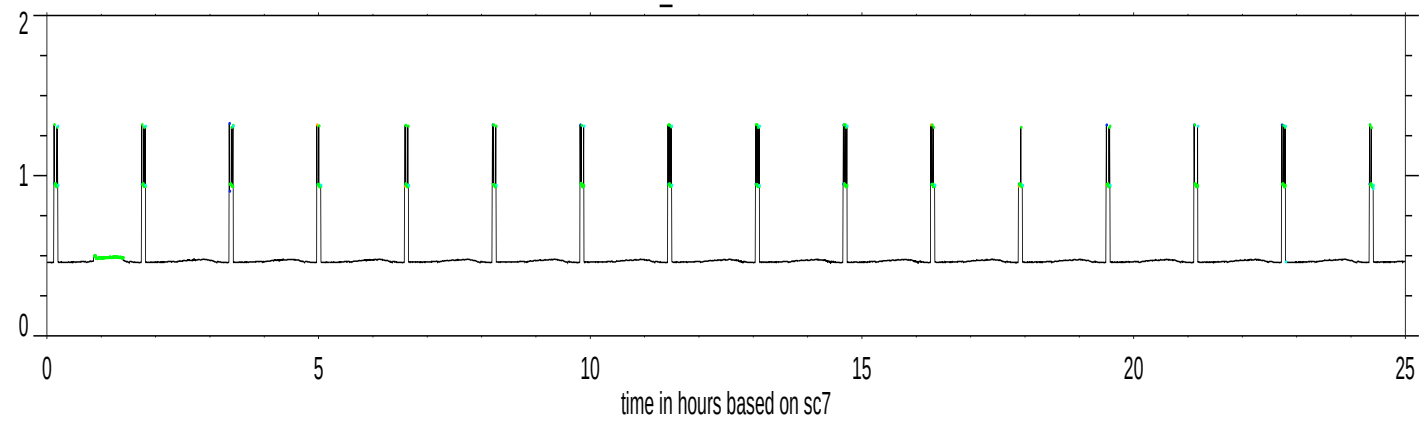

egs_door_pos (0=off, 1/2=close/open, 5/6=error)
time in hours from 0 UT 2018312


stat_xps_cnt
time in hours from 0 UT 2018312

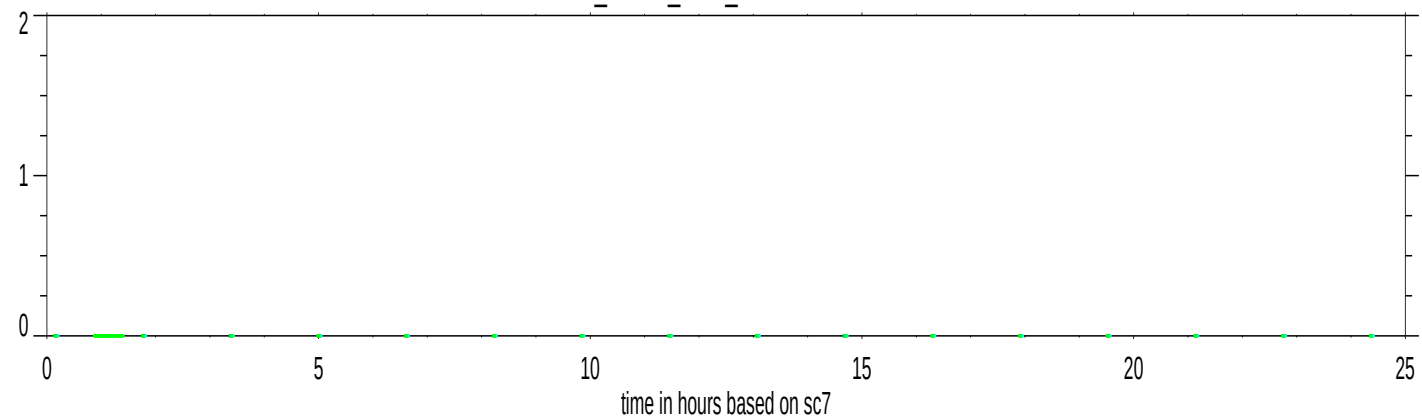

stat_xps_num
time in hours from 0 UT 2018312


stat_cmp_pwr
egs
xps
sspp
mu
time in hours from 0 UT 2018312

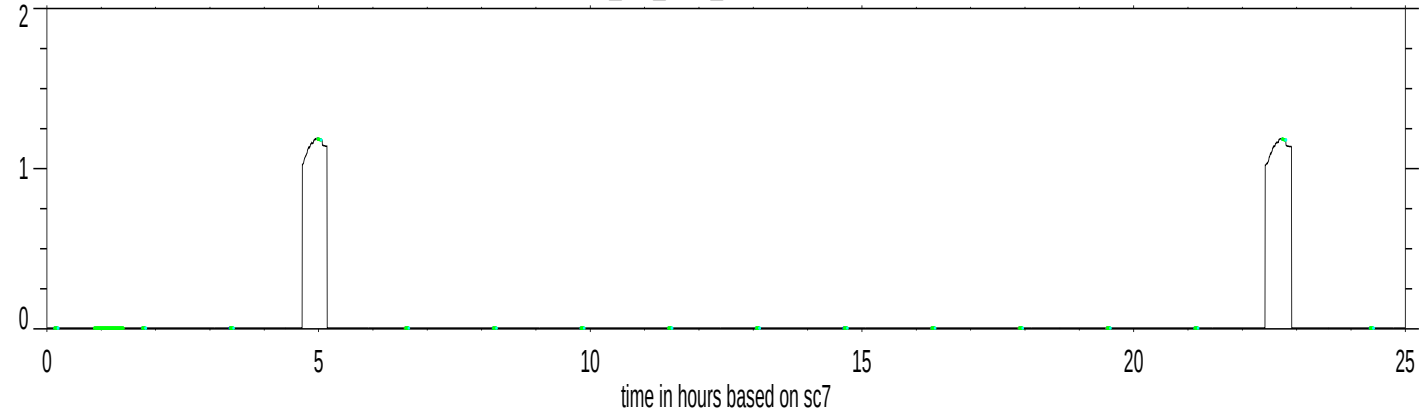
SEE_CUR 2018312



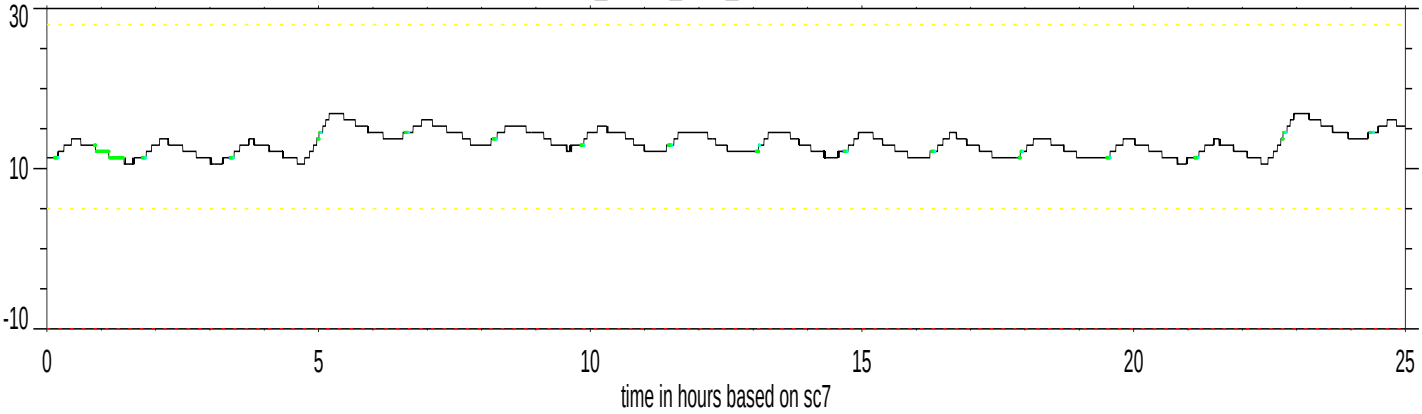
SEE_SURV_HTR_CUR 2018312



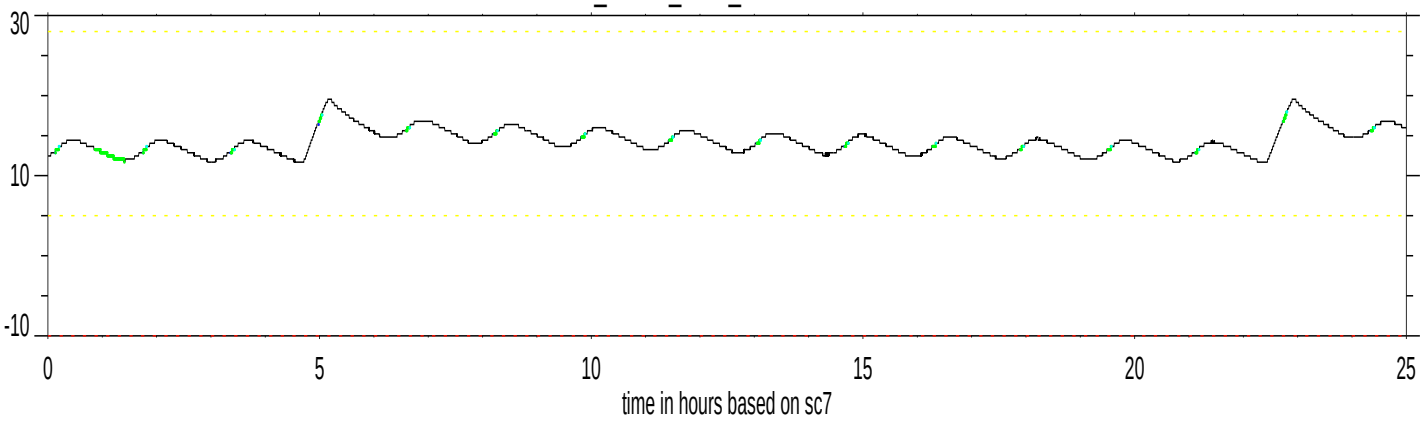
SEE_OP_HTR_CUR 2018312



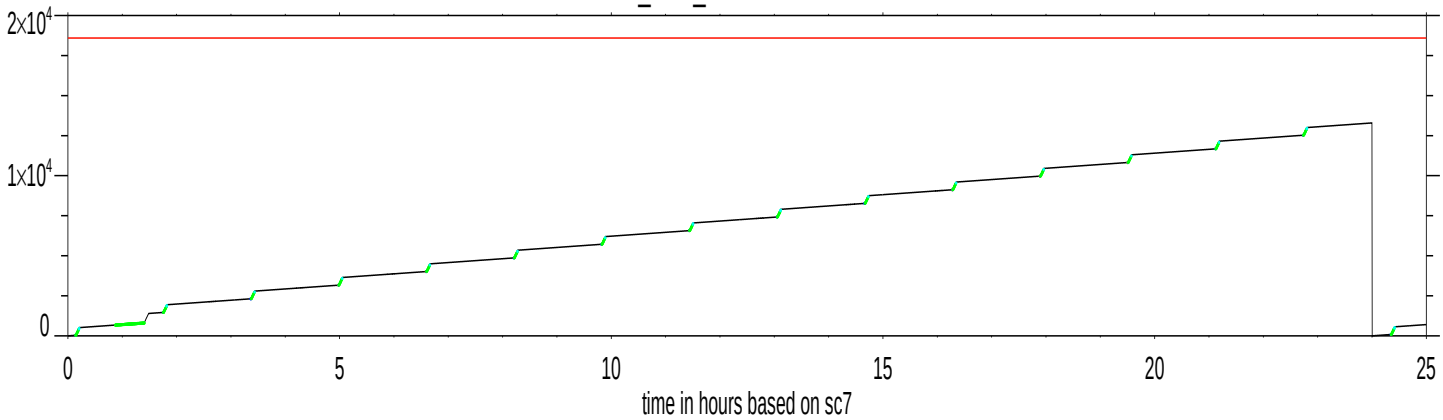
SSPP_BHND_XPS_TEMP 2018312



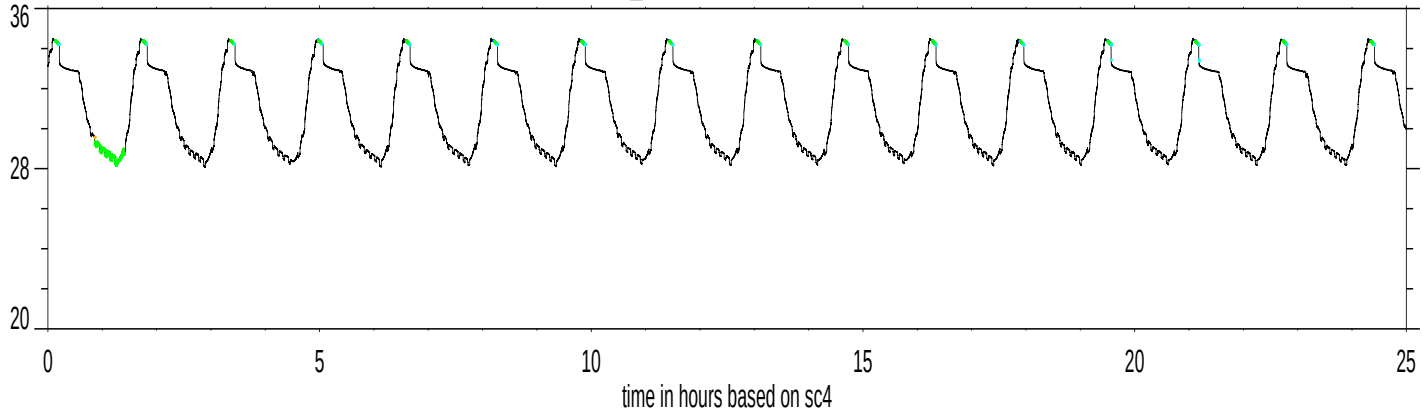
SSPP_BHND_MTR_TEMP 2018312



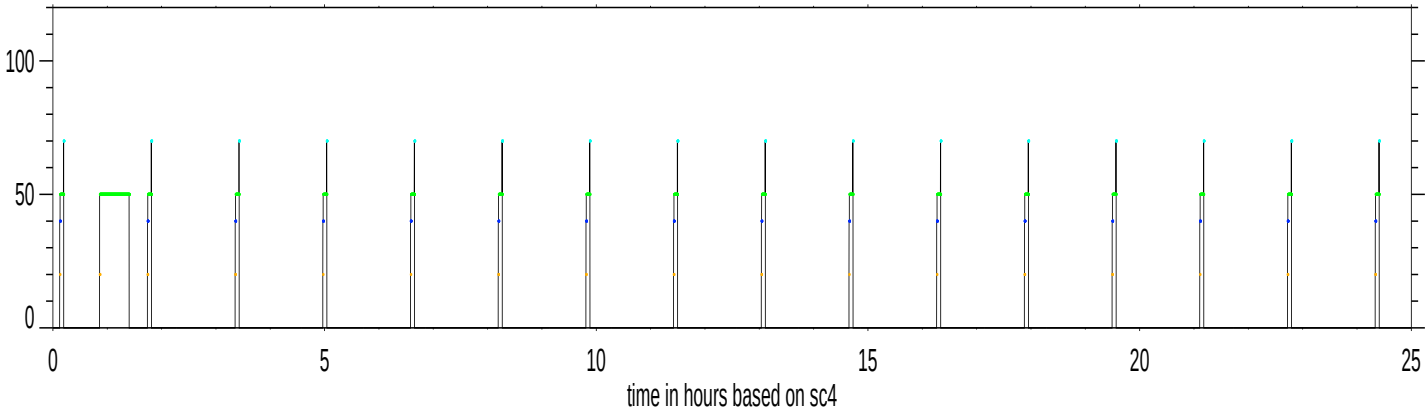
SEE_PKT_COUNT 2018312

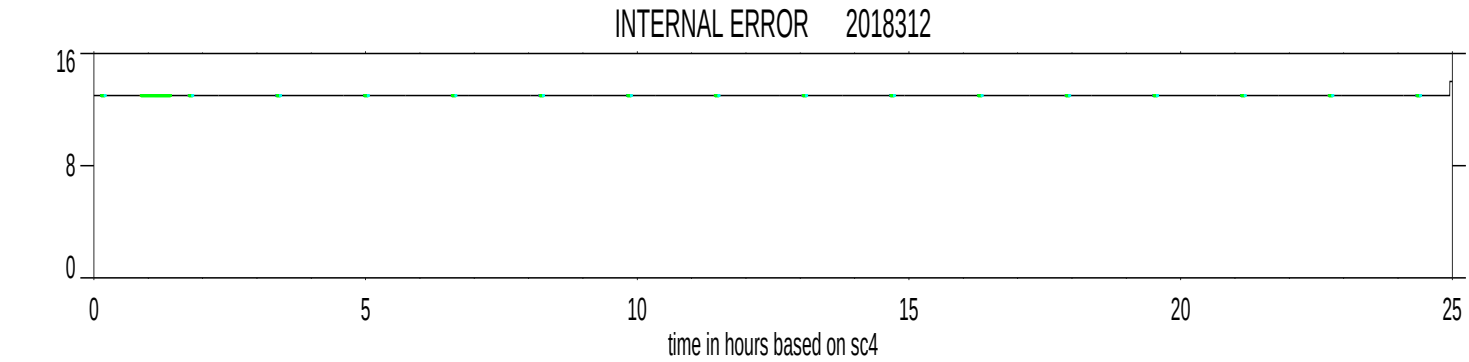


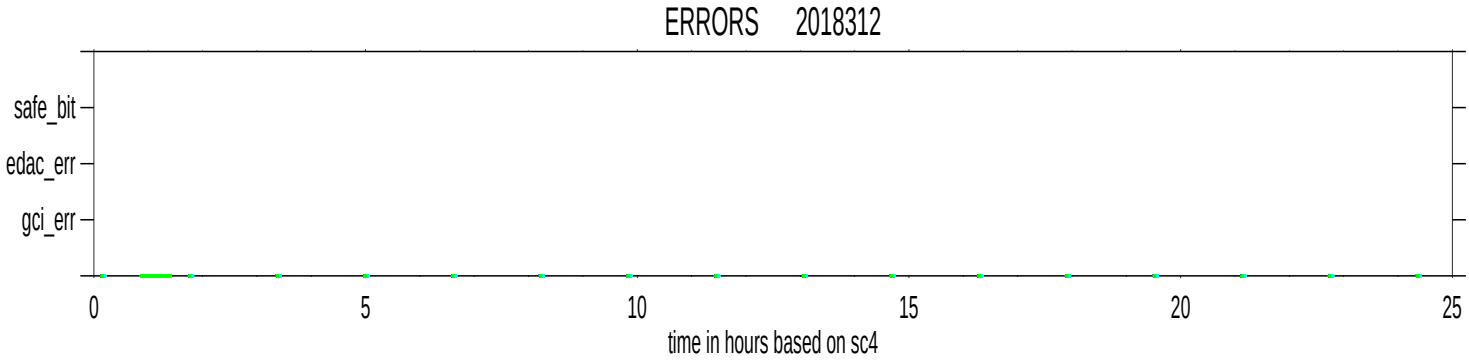
BUS_VOLTAGE 2018312

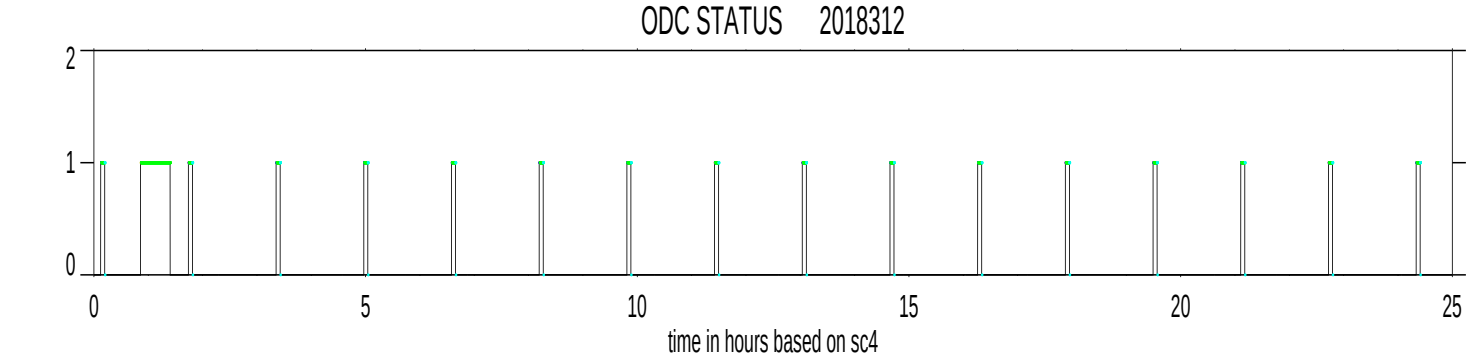


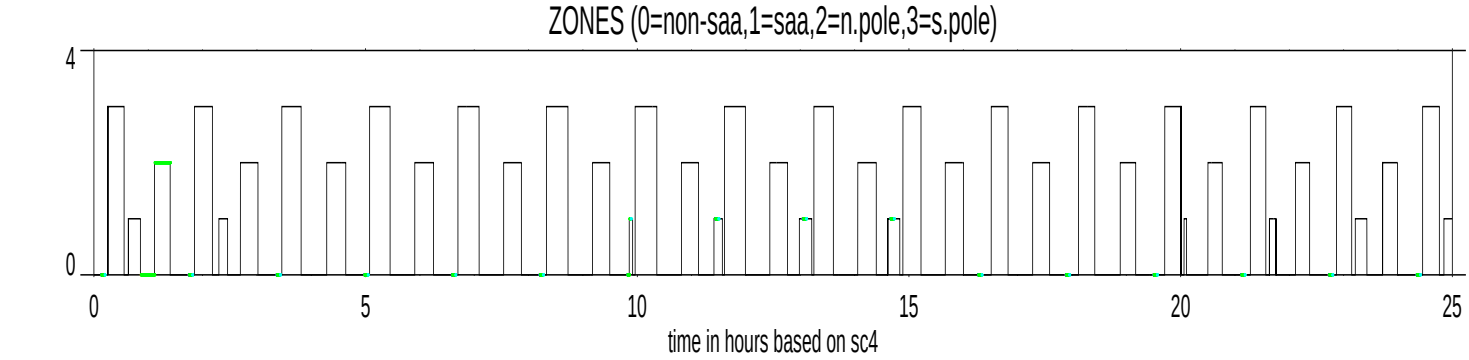
SEE_MU Operational Mode 2018312

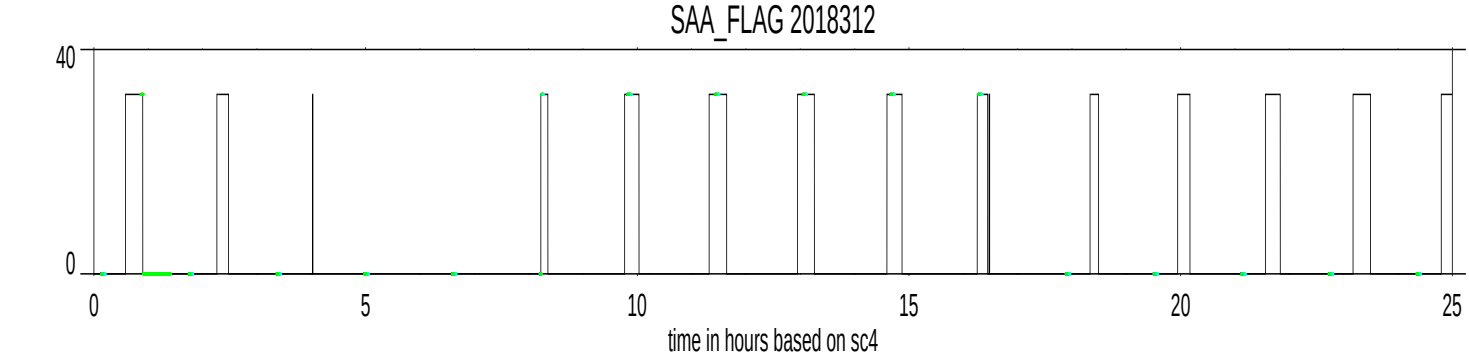


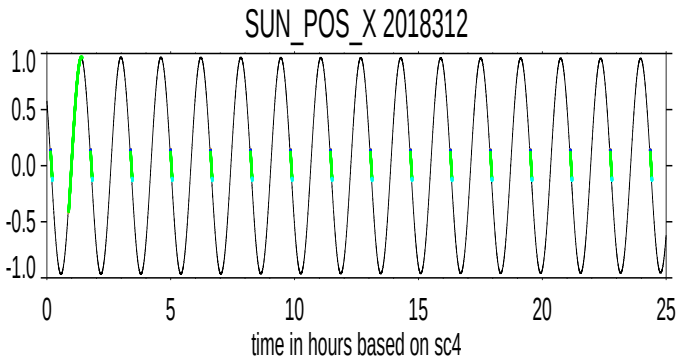

INTERNAL ERROR 2018312
16
8
0
0
5
10
15
20
25
time in hours based on sc4

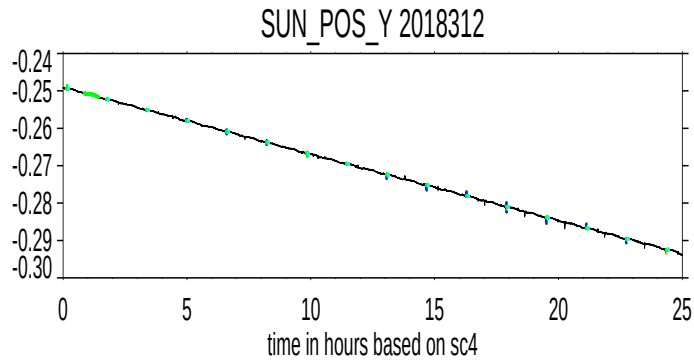

ERRORS 2018312
safe_bit
edac_err
gci_err
0
5
10
15
20
25
time in hours based on sc4

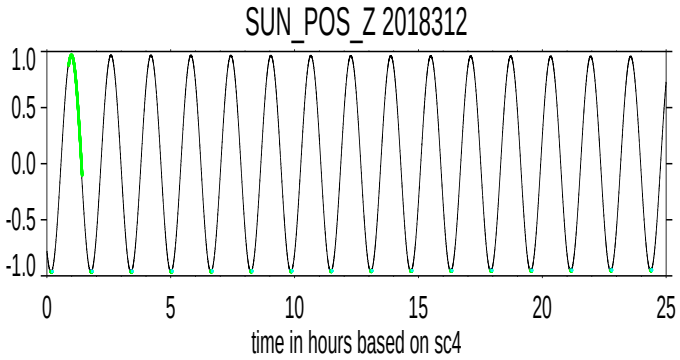

ODC STATUS 2018312
2
1
0
0
5
10
15
20
25
time in hours based on sc4

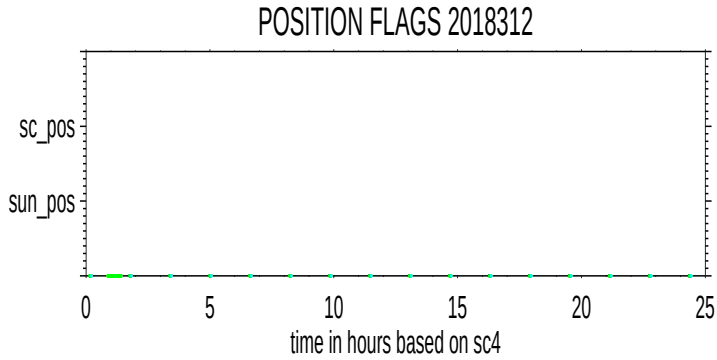

ZONES (0=non-saa,1=saa,2=n.pole,3=s.pole)
4
0
0
5
10
15
20
25
time in hours based on sc4

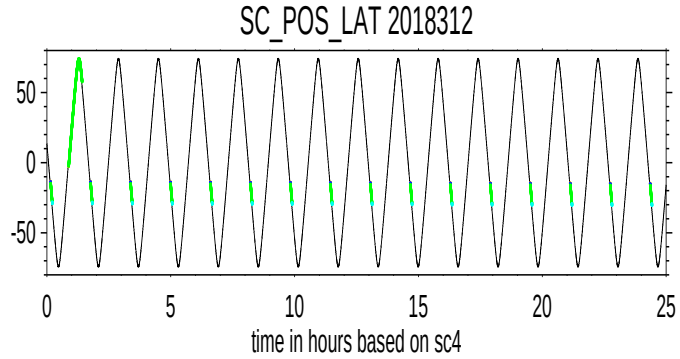

SAA_FLAG 2018312
40
0
0
5
10
15
20
25
time in hours based on sc4

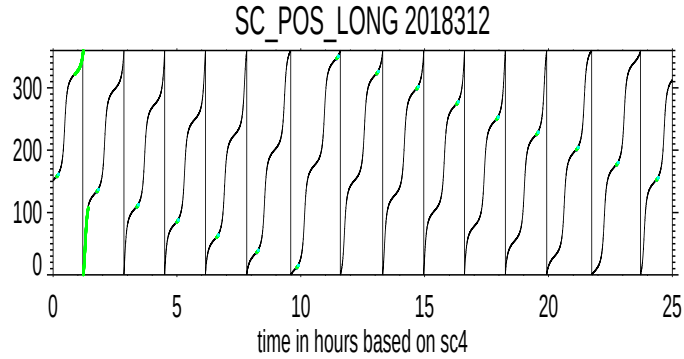

SUN_POS_X 2018312
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4

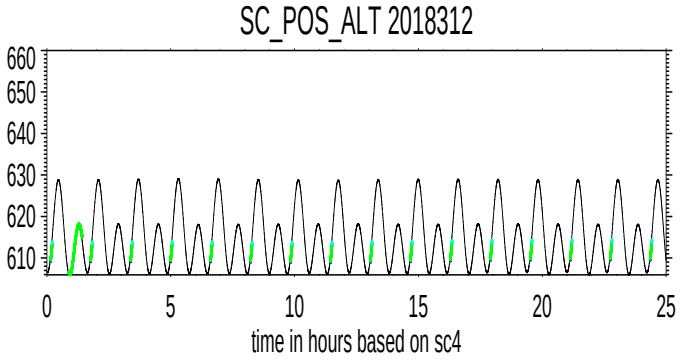

SUN_POS_Y 2018312
-0.24
-0.25
-0.26
-0.27
-0.28
-0.29
-0.30
0
5
10
15
20
25
time in hours based on sc4

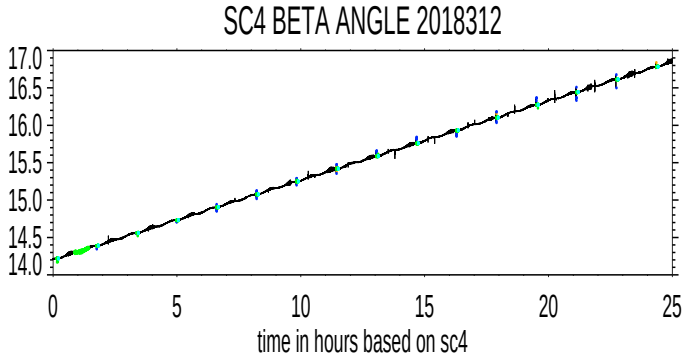

SUN_POS_Z 2018312
1.0
0.5
0.0
-0.5
-1.0
0
5
10
15
20
25
time in hours based on sc4


POSITION FLAGS 2018312
sc_pos
sun_pos
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LAT 2018312
50
0
-50
0
5
10
15
20
25
time in hours based on sc4


SC_POS_LONG 2018312
300
200
100
0
0
5
10
15
20
25
time in hours based on sc4


SC_POS_ALT 2018312
660
650
640
630
620
610
0
5
10
15
20
25
time in hours based on sc4


SC4 BETA ANGLE 2018312
17.0
16.5
16.0
15.5
15.0
14.5
14.0
0
5
10
15
20
25
time in hours based on sc4